



Rewriting logic bibliography by topic: 1990–2011[☆]

Narciso Martí-Oliet^{*}, Miguel Palomino, Alberto Verdejo

Dpto. de Sistemas Informáticos y Computación, Facultad de Informática, Universidad Complutense de Madrid, Spain

ARTICLE INFO

Article history:

Available online 26 June 2012

Keywords:

Rewriting logic
 Concurrency
 Logical frameworks
 Temporal logics
 Formal specification and verification
 Programming language semantics
 Networks and distributed systems
 Real-time systems
 Probabilistic systems
 Security
 Bioinformatics

ABSTRACT

This bibliography compiles, to the best of our knowledge, all the papers on rewriting logic and its applications which have been written during the more than 20 years that have passed since the introduction of rewriting logic in 1990. The papers are classified according to five main areas: foundations, logical and semantic framework, languages, tools, and applications.

© 2012 Elsevier Inc. All rights reserved.

Contents

1.	Introduction	783
2.	Foundations	783
	2.1. Rewriting logic	784
	2.2. The ρ -calculus	786
	2.3. Coherence and computability	786
	2.4. Termination	786
	2.5. Unification, narrowing, reachability analysis, and constraints	787
	2.6. Reflection	788
	2.7. Strategies	788
	2.8. Modal and temporal logic properties	789
	2.9. Simulation and abstraction	790
	2.10. Real-time rewrite theories	790
	2.11. Probabilistic rewrite theories	791
	2.12. Tile logic	791
3.	Rewriting logic as a logical and semantic framework	792
	3.1. Representing logics	792
	3.2. Representing models of concurrency	793
	3.3. Representing modeling languages	793
	3.4. Rewriting logic semantics of programming languages	794
	3.5. K framework	795
4.	Rewriting logic languages	796

[☆] Work supported by MICINN Spanish project *DESAFIOS10* (TIN2009-14599-C03-01) and Comunidad de Madrid program *PROMETIDOS* (S2009/TIC-1465).

^{*} Corresponding author. Address: Facultad de Informática, Universidad Complutense de Madrid, C/Profesor José García Santesmases s/n, 28040 Madrid, Spain. Tel.: +34 913947557; fax: +34 913947529.

E-mail addresses: narciso@esi.ucm.es (N. Martí-Oliet), miguelpt@sip.ucm.es (M. Palomino), alberto@sip.ucm.es (A. Verdejo).

4.1.	CafeOBJ	796
4.2.	ELAN	797
4.3.	Tom	798
4.4.	Maude	798
5.	Tools	799
5.1.	Maude tools	799
5.2.	Other tools	800
6.	Applications	801
6.1.	Network systems	801
6.2.	Real-time and hybrid systems	802
6.3.	Distributed architectures and components	804
6.4.	Software/hardware modeling and verification	807
6.5.	Security	809
6.6.	Probabilistic systems	811
6.7.	Bioinformatics and chemical systems	811
6.8.	Others	812
7.	Proceedings	812

1. Introduction

Rewriting logic was introduced in 1990 by Meseguer; since then, researchers around the world have contributed to the further development of its foundations, to the design and implementation of languages and of tools based on rewriting logic, to several extensions of the basic framework to include additional features such as time or probabilistic information, and, in general, to the application of this framework, its languages, and its associated tools to model, specify, program, verify, and analyze all kinds of systems, ranging from software systems and programming languages to bioinformatics and chemical systems.

All this work has given rise to an important number of papers, published mainly in journals and conference proceedings, but also as Master's and Ph.D. theses, and technical reports. On the occasion of preparing the roadmap that introduced the 2002 *Theoretical Computer Science* special issue on rewriting logic and its applications, we compiled a bibliography on the subject consisting of more than 300 entries. The current bibliography has two goals with the intention of making it more useful for the interested community of researchers: first, to update that previous bibliography by taking into account all the work developed in the 10 years passed since then, and, second, to classify all the compiled papers. This classification has been organized according to five main areas: foundations, logical and semantic framework, languages, tools, and applications. Each of those areas is further divided into several subareas, as summarized in the table of contents above.

These are the criteria we have followed in classifying the bibliography:

- The subareas are not disjoint at all and, in some cases, one subarea could be included in another; for example, networks are distributed systems, and the K framework is devoted to developing and implementing semantics of programming languages. However, this division comes from previous surveys on this subject and is similar to the one used in the companion 20-year survey by Meseguer in this volume.
- Each paper appears only in one subarea.
- Since a paper can cover many subjects, it has been classified in the appropriate subarea according to the main contributions and the novelty of such.

The proposed classification has been validated by the rewriting logic community, since several drafts of this paper have been distributed along its preparation.

There are several factors who have greatly helped us in producing this bibliography: the DBLP computer science bibliography, the accuracy of Google's search, and all the information provided by the authors of the papers themselves. We are very grateful to all of them for their willingness to include their work here.

The source bib file with all the entries in this bibliography is available at <http://maude.sip.ucm.es/jlap-bibliography2012.bib>.

2. Foundations

This section includes papers of a more theoretical nature on the foundations of rewriting logic, covering its proof theory and semantics; specification properties such as coherence, computability, and termination; extensions with additional features like time, probabilities, and strategies; and relations with associated logics such as the ρ -calculus, tile logic, and temporal logics.

2.1. Rewriting logic

The following papers address the theoretical foundations of rewriting logic and closely associated logics such as membership equational logic and order-sorted algebra, including denotational and categorical semantics, operational semantics, proof theory, extensions, and relationships with other logics.

- [1] M. Alpuente, M. Baggi, M. Falaschi, D. Ballis, **Completeness of unfolding for rewriting logic theories**, in: [1067], pp. 116–123.
- [2] M. Alpuente, D. Ballis, M. Baggi, M. Falaschi, **A fold/unfold transformation framework for rewrite theories extended to CCT**, in: [1057], pp. 43–52.
- [3] E. Astesiano, M. Broy, G. Reggio, **Algebraic specification of concurrent systems**, in: E. Astesiano, H.J. Kreowski, B. Krieg-Brückner (Eds.), *Algebraic Foundations of Systems Specification*, IFIP State-of-the-Art Reports, Springer, 1999, pp. 467–520.
- [4] E. Astesiano, G. Reggio, **Algebraic specification of concurrency**, in: M. Bidoit, C. Choppy (Eds.), *Recent Trends in Data Type Specification*, 8th Workshop on Specification of Abstract Data Types Joint with the 3rd COMPASS Workshop, Dourdan, France, August 26–30, 1991, Selected Papers, volume 655 of *Lecture Notes in Computer Science*, Springer, 1993, pp. 1–39.
- [5] E. Astesiano, G. Reggio, **On the relationship between labelled transition logic and rewriting logic**, Technical Report DISI-TR-97-23, Dipartimento di Informatica e Scienze dell'Informazione, Università di Genova, Italy, 1997.
- [6] E. Astesiano, G. Reggio, **Labelled transition logic: an outline**, *Acta Informatica* 37 (2001) 831–879.
- [7] A. Bouhoula, J.P. Jouannaud, J. Meseguer, **Specification and proof in membership equational logic**, in: M. Bidoit, M. Dauchet (Eds.), *TAPSOFT'97: Theory and Practice of Software Development*, 7th International Joint Conference CAAP/FASE, Lille, France, April 14–18, 1997, Proceedings, volume 1214 of *Lecture Notes in Computer Science*, Springer, 1997, pp. 67–92.
- [8] A. Bouhoula, J.P. Jouannaud, J. Meseguer, **Specification and proof in membership equational logic**, *Theoretical Computer Science* 236 (2000) 35–132.
- [9] R. Bruni, J. Meseguer, **Generalized rewrite theories**, in: J.C.M. Baeten, J.K. Lenstra, J. Parrow, G.J. Woeginger (Eds.), *Automata, Languages and Programming*, 30th International Colloquium, ICALP 2003, Eindhoven, The Netherlands, June 30–July 4, 2003, Proceedings, volume 2719 of *Lecture Notes in Computer Science*, Springer, 2003, pp. 252–266.
- [10] R. Bruni, J. Meseguer, **Semantic foundations for generalized rewrite theories**, *Theoretical Computer Science* 360 (2006) 386–414.
- [11] M.V. Cengarle, **The Rewriting Logic Institution**, Technical Report 9801, Institut für Informatik, Ludwig-Maximilians-Universität München, 1998.
- [12] A. Corradini, F. Gadducci, **CPO models for infinite term rewriting**, in: V.S. Alagar, M. Nivat (Eds.), *Algebraic Methodology and Software Technology*, 4th International Conference, AMAST '95, Montreal, Canada, July 3–7, 1995, Proceedings, volume 936 of *Lecture Notes in Computer Science*, Springer, 1995, pp. 368–384.
- [13] A. Corradini, F. Gadducci, U. Montanari, **Relating two categorial models of term rewriting**, in: [1066], pp. 225–240.
- [14] R. Diaconescu, **Foundations of behavioural specification in rewriting logic**, in: [1086], pp. 226–245.
- [15] F. Durán, J. Meseguer, **Structured theories and institutions**, in: M. Hofmann, G. Rosolini, D. Pavlovic (Eds.), *Proceedings of the Conference on Category Theory and Computer Science*, CTCS'99, Edinburgh, UK, December 10–12, 1999, volume 29 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 1999, pp. 23–41.
- [16] F. Durán, J. Meseguer, **Structured theories and institutions**, *Theoretical Computer Science* 309 (2003) 357–380.
- [17] S. Eker, **Fast matching in combination of regular equational theories**, in: [1086], pp. 90–109.
- [18] S. Eker, **Single elementary associative-commutative matching**, *Journal of Automated Reasoning* 28 (2002) 35–51.
- [19] S. Eker, **Fast sort computations for order-sorted matching and unification**, in: [1015], pp. 299–314.
- [20] F. Gadducci, **On the Algebraic Approach to Concurrent Term Rewriting**, Ph.D. thesis, Dipartimento di Informatica, Università di Pisa, 1996. Technical Report TD-2/96.
- [21] F. Gadducci, U. Montanari, **Enriched categories as models of computation**, in: A.D. Santis (Ed.), *Proceedings of the 5th Italian Conference on Theoretical Computer Science*, Ravello, Italy, November 1995, World Scientific, 1996, pp. 20–42.
- [22] T. Genet, **Decidable approximations of sets of descendants and sets of normal forms**, in: T. Nipkow (Ed.), *Rewriting Techniques and Applications*, 9th International Conference, RTA-98, Tsukuba, Japan, March 30–April 1, 1998, Proceedings, volume 1379 of *Lecture Notes in Computer Science*, Springer, 1998, pp. 151–165.
- [23] I. Gnaedig, H. Kirchner, **Computing constructor forms with non terminating rewrite programs**, in: A. Bossi, M.J. Maher (Eds.), *Proceedings of the 8th International ACM SIGPLAN Conference on Principles and Practice of Declarative Programming*, PPDP 2006, Venice, Italy, July 10–12, 2006, ACM, 2006, pp. 121–132.
- [24] J.A. Goguen, K. Lin, G. Roşu, **Behavioral and coinductive rewriting**, in: [1052], pp. 2–23.
- [25] J.C. González Moreno, M.T. Hortalá-González, F.J. López-Fraguas, M. Rodríguez-Artalejo, **An approach to declarative programming based on a rewriting logic**, *Journal of Logic Programming* 40 (1999) 47–87.
- [26] R. Gutiérrez, J. Meseguer, C. Rocha, **Order-sorted equality enrichments modulo axioms**, in: [1042], To appear.
- [27] J. Hendrix, **Decision Procedures for Equationally Based Reasoning**, Ph.D. thesis, Department of Computer Science, University of Illinois at Urbana-Champaign, 2008.
- [28] J. Hendrix, J. Meseguer, **On the completeness of context-sensitive order-sorted specifications**, in: [1023], pp. 229–245.
- [29] J. Hendrix, H. Ohsaki, **Combining equational tree automata over AC and ACI theories**, in: [1108], pp. 142–156.
- [30] J. Hendrix, H. Ohsaki, M. Viswanathan, **Propositional tree automata**, in: [1097], pp. 50–65.
- [31] H. Hilberdink, **New foundations for rewriting logic**, in: [1052], pp. 43–69.
- [32] J.P. Jouannaud, **Membership equational logic, calculus of inductive constructions, and rewrite logic**, in: [1071], pp. 388–393.
- [33] H. Kirchner, **Some extensions of rewriting**, in: H. Comon, J.P. Jouannaud (Eds.), *Term Rewriting*, French Spring School of Theoretical Computer Science, Font Romeux, France, May 17–21, 1993, Advanced Course, volume 909 of *Lecture Notes in Computer Science*, Springer, 1995, pp. 54–73.
- [34] H. Kirchner, **Term rewriting**, in: E. Astesiano, H.J. Kreowski, B. Krieg-Brückner (Eds.), *Algebraic Foundations of Systems Specification*, IFIP State-of-the-Art Reports, Springer, 1999, pp. 273–320.
- [35] C. Laneve, U. Montanari, **Axiomatizing permutation equivalence in the λ -calculus**, in: [1072], pp. 350–363.
- [36] C. Laneve, U. Montanari, **Axiomatizing permutation equivalence**, *Mathematical Structures in Computer Science* 6 (1996) 219–249.
- [37] F.J. López-Fraguas, M. Rodríguez-Artalejo, R. del Vado-Virseda, **Constraint functional logic programming revisited**, in: [1085], pp. 5–50.
- [38] F.J. López-Fraguas, M. Rodríguez-Artalejo, R. del Vado-Virseda, **A new generic scheme for functional logic programming with constraints**, *Higher-Order and Symbolic Computation* 20 (2007) 73–122.
- [39] D. Lucanu, **Relaxed models for rewriting logic**, *Theoretical Computer Science* 290 (2003) 265–289.
- [40] S. Lucas, **Context-sensitive computations in functional and functional logic programs**, *Journal of Functional and Logic Programming* 1998 (1998).
- [41] N. Martí-Oliet, J. Meseguer, **Rewriting logic: roadmap and bibliography**, *Theoretical Computer Science* 285 (2002) 121–154.
- [42] J. Meseguer, **A logical theory of concurrent objects**, in: N. Meyrowitz (Ed.), *Proceedings of the ECOOP-OOPSLA'90 Conference on Object-Oriented Programming*, Ottawa, Canada, October 21–25, 1990, ACM Press, 1990, pp. 101–115.
- [43] J. Meseguer, **Rewriting as a Unified Model of Concurrency**, Technical Report SRI-CSL-90-02, SRI International, Computer Science Laboratory, 1990. Revised June 1990.
- [44] J. Meseguer, **Rewriting as a unified model of concurrency**, in: J.C.M. Baeten, J.W. Klop (Eds.), *CONCUR '90, Theories of Concurrency: Unification and Extension*, Amsterdam, The Netherlands, August 27–30, 1990, Proceedings, volume 458 of *Lecture Notes in Computer Science*, Springer, 1990, pp. 384–400.

- [45] J. Meseguer, **Conditional rewriting logic: Deduction, models and concurrency**, in: S. Kaplan, M. Okada (Eds.), *Conditional and Typed Rewriting Systems*, 2nd International CTRS Workshop, Montreal, Canada, June 11–14, 1990, Proceedings, volume 516 of *Lecture Notes in Computer Science*, Springer, 1991, pp. 64–91.
- [46] J. Meseguer, **Conditional rewriting logic as a unified model of concurrency**, *Theoretical Computer Science* 96 (1992) 73–155.
- [47] J. Meseguer, **Multiparadigm logic programming**, in: [1072], pp. 158–200.
- [48] J. Meseguer, **A logical theory of concurrent objects and its realization in the Maude language**, in: G. Agha, P. Wegner, A. Yonezawa (Eds.), *Research Directions in Concurrent Object-Oriented Programming*, The MIT Press, 1993, pp. 314–390.
- [49] J. Meseguer, **Solving the inheritance anomaly in concurrent object-oriented programming**, in: O. Nierstrasz (Ed.), *ECOOP'93 – Object-Oriented Programming*, 7th European Conference, Kaiserslautern, Germany, July 26–30, 1993, Proceedings, volume 707 of *Lecture Notes in Computer Science*, Springer, 1993, pp. 220–246.
- [50] J. Meseguer, **Membership algebra as a logical framework for equational specification**, in: [1096], pp. 18–61.
- [51] J. Meseguer, **Formal interoperability**, in: *Proceedings of the 1998 Conference on Mathematics in Artificial Intelligence*, Fort Lauderdale, Florida. Presented also at the *14th IMACS World Congress*, Atlanta, Georgia, July 1994.
- [52] J. Meseguer, **Research directions in rewriting logic**, in: U. Berger, H. Schwichtenberg (Eds.), *Proceedings of the NATO Advanced Study Institute on Computational Logic*, Held in Marktobendorf, Germany, July 29–August 6, 1997, volume 165 of *NATO ASI Series F: Computer and Systems Sciences*, Springer, 1998, pp. 347–398.
- [53] J. Meseguer, **Rewriting logic and Maude: a wide-spectrum semantic framework for object-based distributed systems**, in: [1104], pp. 89–117.
- [54] J. Meseguer, **Rewriting logic and Maude: Concepts and applications**, in: L. Bachmair (Ed.), *Rewriting Techniques and Applications*, 11th International Conference, RTA 2000, Norwich, UK, July 10–12, 2000, Proceedings, volume 1833 of *Lecture Notes in Computer Science*, Springer, 2000, pp. 1–26.
- [55] J. Meseguer, **Executable computational logics: Combining formal methods and programming language based system design**, in: S.K. Shukla, J.P. Talpin (Eds.), *Proceedings of the 1st ACM & IEEE International Conference on Formal Methods and Models for Co-Design, MEMOCODE 2003*, Mont Saint-Michel, France, June 24–26, 2003, IEEE Computer Society, 2003, pp. 3–9.
- [56] J. Meseguer, **Functorial semantics of rewrite theories**, in: H.J. Kreowski, U. Montanari, F. Orejas, G. Rozenberg, G. Taentzer (Eds.), *Formal Methods in Software and Systems Modeling, Essays Dedicated to Hartmut Ehrig on the Occasion of His 60th Birthday*, volume 3393 of *Lecture Notes in Computer Science*, Springer, 2005, pp. 220–235.
- [57] J. Meseguer, **A rewriting logic sampler**, in: D.V. Hung, M. Wirsing (Eds.), *Theoretical Aspects of Computing – ICTAC 2005*, Second International Colloquium, Hanoi, Vietnam, October 17–21, 2005, Proceedings, volume 3722 of *Lecture Notes in Computer Science*, Springer, 2005, pp. 1–28.
- [58] J. Meseguer, **Order-sorted parameterization and induction**, in: [1094], pp. 43–80.
- [59] J. Meseguer, **Twenty years of rewriting logic**, in: [1093], pp. 15–17.
- [60] J. Meseguer, **Twenty years of rewriting logic**, *Journal of Logic and Algebraic Programming* (2012). This volume.
- [61] J. Meseguer, N. Martí-Oliet, **From abstract data types to logical frameworks**, in: [1021], pp. 48–80.
- [62] J. Meseguer, X. Qian, **A logical semantics for object-oriented databases**, in: P. Buneman, S. Jajodia (Eds.), *Proceedings of the 1993 ACM SIGMOD International Conference on Management of Data*, Washington, D.C., May 26–28, 1993, ACM Press, 1993, pp. 89–98.
- [63] J. Meseguer, G. Roşu, **A total approach to partial algebraic specification**, in: P. Widmayer, F.T. Ruiz, R.M. Bueno, M. Hennessy, S. Eidenbenz, R. Conejo (Eds.), *Automata, Languages and Programming*, 29th International Colloquium, ICALP 2002, Malaga, Spain, July 8–13, 2002, Proceedings, volume 2380 of *Lecture Notes in Computer Science*, Springer, 2002, pp. 572–584.
- [64] J. Meseguer, G. Roşu, **Towards behavioral Maude: Behavioral membership equational logic**, in: L.S. Moss (Ed.), *Proceedings of the Fifth Workshop on Coalgebraic Methods in Computer Science*, CMCS 2002, Satellite Event of ETAPS 2002, Grenoble, France, April 6–7, 2002, volume 65(1) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2002, pp. 197–253.
- [65] J. Meseguer, C.L. Talcott, **A partial order event model for concurrent objects**, in: [1025], pp. 415–430.
- [66] H. Miyoshi, **Modelling conditional rewriting logic in structured categories**, in: [1086], pp. 20–34.
- [67] J.M. Molina-Bravo, E. Pimentel, **Composing programs in a rewriting logic for declarative programming**, *Theory and Practice of Logic Programming* 3 (2003) 189–221.
- [68] K. Ogata, K. Ohhara, K. Futatsugi, **TRAM: An abstract machine for order-sorted conditional term rewriting systems**, in: [1035], pp. 335–338.
- [69] M. Palomino, **A comparison between two logical formalisms for rewriting**, *Theory and Practice of Logic Programming* 7 (2007) 183–213.
- [70] M. Palomino Tarjuelo, **Relating Meseguer's Rewriting Logic with the Constructor-Based Rewriting Logic**, Master's thesis, Facultad de Matemáticas, Universidad Complutense de Madrid, Spain, 2001.
- [71] M. Palomino Tarjuelo, **Comparing Meseguer's rewriting logic with the logic CRWL**, in: *Proceedings of the International Workshop on Functional and (Constraint) Logic Programming, WFLP 2001*, Kiel, Germany, September 13–15, 2001.
- [72] C. Rocha, J. Meseguer, **Five Isomorphic Boolean Theories and Four Equational Decision Procedures**, Technical Report UIUCDCS-R-2007-2818, Department of Computer Science, University of Illinois at Urbana-Champaign, 2007.
- [73] C. Rocha, J. Meseguer, **Theorem Proving Modulo Based on Boolean Equational Procedures**, Technical Report UIUCDCS-R-2007-2922, Department of Computer Science, University of Illinois at Urbana-Champaign, 2007.
- [74] C. Rocha, J. Meseguer, **Theorem proving modulo based on Boolean equational procedures**, in: R. Berghammer, B. Möller, G. Struth (Eds.), *Relations and Kleene Algebra in Computer Science*, 10th International Conference on Relational Methods in Computer Science, and 5th International Conference on Applications of Kleene Algebra, RelMiCS/AKA 2008, Frauenwörth, Germany, April 7–11, 2008, Proceedings, volume 4988 of *Lecture Notes in Computer Science*, Springer, 2008, pp. 337–351.
- [75] C. Rocha, J. Meseguer, **Constructors, Sufficient Completeness and Deadlock Freedom of Generalized Rewrite Theories**, Technical Report, Department of Computer Science, University of Illinois at Urbana-Champaign, 2010.
- [76] C. Rocha, J. Meseguer, **Constructors, sufficient completeness and deadlock freedom of rewrite theories**, in: C.G. Fermüller, A. Voronkov (Eds.), *Logic for Programming, Artificial Intelligence, and Reasoning*, 17th International Conference, LPAR-17, Yogyakarta, Indonesia, October 10–15, 2010, Proceedings, volume 6397 of *Lecture Notes in Computer Science*, Springer, 2010, pp. 594–609.
- [77] G. Roşu, **On implementing behavioral rewriting**, in: [1050], pp. 43–52.
- [78] G. Roşu, **Inductive behavioral proofs by un hiding**, in: H.P. Gumm (Ed.), *Proceedings of the Sixth Workshop on Coalgebraic Methods in Computer Science*, CMCS 2003, Satellite Event for ETAPS 2003, Warsaw, Poland, April 5–6, 2003, volume 82(1) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2003, pp. 285–302.
- [79] G. Roşu, **Behavioral abstraction is hiding information**, *Theoretical Computer Science* 327 (2004) 197–221.
- [80] G. Roşu, **From conditional to unconditional rewriting**, in: [1049], pp. 218–233.
- [81] G. Roşu, S. Eker, P. Lincoln, J. Meseguer, **Certifying and synthesizing membership equational proofs**, in: [1019], pp. 359–380.
- [82] G. Roşu, M. Viswanathan, **Testing extended regular language membership incrementally by rewriting**, in: [1090], pp. 499–514.
- [83] W.M. Schorlemmer, **Bi-rewriting rewriting logic**, in: [1086], pp. 266–283.
- [84] W.M. Schorlemmer, **Rewriting logic as a logic of special relations**, in: [1071], pp. 196–217.
- [85] W.M. Schorlemmer, **On Specifying and Reasoning with Special Relations**, Ph.D. thesis, Universitat Politècnica de Catalunya, 1999.
- [86] W.M. Schorlemmer, **Term rewriting in a logic of special relations**, in: A.M. Haeblerer (Ed.), *Algebraic Methodology and Software Technology*, 7th International Conference, AMAST '98, Amazonia, Brasil, January 4–8, 1999, Proceedings, volume 1548 of *Lecture Notes in Computer Science*, Springer, 1999, pp. 178–195.
- [87] T.F. Şerbănuţă, G. Roşu, **Computationally equivalent elimination of conditions**, in: [1097], pp. 19–34.
- [88] N. Shankar, **Rewriting, inference, and proof**, in: [1093], pp. 1–14.
- [89] J.G. Stell, **Modelling term rewriting systems by sesqui-categories**, Technical Report TR94-02, Keele University, 1994.

- [90] P. Viry, *Rewriting: An effective model of concurrency*, in: [1064], pp. 648–660.
 [91] E. Visser, Z. el Abidine Benaissa, *A core language for rewriting*, in: [1071], pp. 422–441.

2.2. The ρ -calculus

The ρ -calculus is to rewriting what the λ -calculus is to functional programming; being another theoretical basis for rewriting, it is closely related to rewriting logic.

- [92] P. Baldan, C. Bertolissi, H. Cirstea, C. Kirchner, *A rewriting calculus for cyclic higher-order term graphs*, *Mathematical Structures in Computer Science* 17 (2007) 363–406.
 [93] C. Bertolissi, H. Cirstea, C. Kirchner, *Translating combinatory reduction systems into the rewriting calculus*, in: [1058], pp. 28–44.
 [94] C. Bertolissi, H. Cirstea, C. Kirchner, *Expressing combinatory reduction systems derivations in the rewriting calculus*, *Higher-Order and Symbolic Computation* 19 (2006) 345–376.
 [95] C. Bertolissi, C. Kirchner, *The rewriting calculus as a combinatory reduction system*, in: H. Seidl (Ed.), *Foundations of Software Science and Computational Structures, 10th International Conference, FOSSACS 2007, Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2007, Braga, Portugal, March 24–April 1, 2007, Proceedings*, volume 4423 of *Lecture Notes in Computer Science*, Springer, 2007, pp. 78–92.
 [96] H. Cirstea, *Calcul de Réécriture: Fondements et Applications*, Ph.D. thesis, Université Henri Poincaré – Nancy I, 2000.
 [97] H. Cirstea, G. Faure, M. Fernández, I. Mackie, F.R. Sinot, *From functional programs to interaction nets via the rewriting calculus*, in: [1017], pp. 39–56.
 [98] H. Cirstea, G. Faure, C. Kirchner, *A ρ -calculus of explicit constraint applications*, in: [1085], pp. 51–67.
 [99] H. Cirstea, G. Faure, C. Kirchner, *A ρ -calculus of explicit constraint application*, *Higher-Order and Symbolic Computation* 20 (2007) 37–72.
 [100] H. Cirstea, C. Houtmann, B. Wack, *Distributive ρ -calculus*, in: [1039], pp. 95–111.
 [101] H. Cirstea, C. Kirchner, *Combining higher-order and first-order computations using ρ -calculus: Towards a semantics of ELAN*, in: D. Gabbay, M. de Rijke (Eds.), *Frontiers of Combining Systems 2, Research Studies*, Wiley, 1999, pp. 95–120.
 [102] H. Cirstea, C. Kirchner, *The simply typed rewriting calculus*, in: [1052], pp. 24–42.
 [103] H. Cirstea, C. Kirchner, *The rewriting calculus – Part I*, *Logic Journal of the IGPL* 9 (2001) 363–399.
 [104] H. Cirstea, C. Kirchner, *The rewriting calculus – Part II*, *Logic Journal of the IGPL* 9 (2001) 401–434.
 [105] H. Cirstea, C. Kirchner, L. Liquori, *Matching power*, in: A. Middeldorp (Ed.), *Rewriting Techniques and Applications, 12th International Conference, RTA 2001, Utrecht, The Netherlands, May 22–24, 2001, Proceedings*, volume 2051 of *Lecture Notes in Computer Science*, Springer, 2001, pp. 77–92.
 [106] H. Cirstea, C. Kirchner, L. Liquori, *The rho cube*, in: F. Honsell, M. Miculan (Eds.), *Foundations of Software Science and Computation Structures, 4th International Conference, FOSSACS 2001, Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2001, Genova, Italy, April 2–6, 2001, Proceedings*, volume 2030 of *Lecture Notes in Computer Science*, Springer, 2001, pp. 168–183.
 [107] H. Cirstea, C. Kirchner, L. Liquori, *Rewriting calculus with(out) types*, in: [1056], pp. 3–19.
 [108] H. Cirstea, C. Kirchner, L. Liquori, B. Wack, *Rewrite strategies in the rewriting calculus*, in: [1062], pp. 593–624.
 [109] H. Cirstea, L. Liquori, B. Wack, *Rewriting calculus with fixpoints: Untyped and first-order systems*, in: S. Berardi, M. Coppo, F. Damiani (Eds.), *Types for Proofs and Programs, International Workshop, TYPES 2003, Torino, Italy, April 30–May 4, 2003, Revised Selected Papers*, volume 3085 of *Lecture Notes in Computer Science*, Springer, 2004, pp. 147–161.
 [110] G. Faure, C. Kirchner, *Exceptions in the rewriting calculus*, in: [1106], pp. 66–82.
 [111] L. Liquori, B. Wack, *The polymorphic rewriting-calculus: [type checking vs. type inference]*, in: [1085], pp. 89–111.
 [112] A. Stump, R. Besand, J.C. Brodman, J. Hseu, B. Kinnarsley, *From Rogue to MicroRogue*, in: [1085], pp. 69–87.

2.3. Coherence and computability

For rewrite theories to become executable specifications, they must satisfy some requirements so that the states and the rewrite relation become computable. The following papers address this issue in the form of coherence properties.

- [113] T. Noll, *On coherence properties in term rewriting models of concurrency*, in: [1025], pp. 478–493.
 [114] P. Viry, *La Réécriture Concurrente*, Ph.D. thesis, Université de Nancy I, 1992.
 [115] P. Viry, *Rewriting modulo a rewrite system*, Technical Report TR-95-20, Dipartimento di Informatica, Università di Pisa, 1995.
 [116] P. Viry, *Equational rules for rewriting logic*, *Theoretical Computer Science* 285 (2002) 487–517.

2.4. Termination

The rich features of the rewriting logic framework and of the languages based on it have required the development of new methods to prove termination of rewrite theories in such framework and of programs in such languages. These new methods have also been integrated in appropriate tools, such as CARIBOO and MU-TERM.

- [117] B. Alarcón, R. Gutiérrez, S. Lucas, *Context-sensitive dependency pairs*, *Information and Computation* 208 (2010) 922–968.
 [118] B. Alarcón, R. Gutiérrez, S. Lucas, R. Navarro-Marset, *Proving termination properties with MU-TERM*, in: [1068], pp. 201–208.
 [119] B. Alarcón, S. Lucas, *Termination of innermost context-sensitive rewriting using dependency pairs*, in: B. Konev, F. Wolter (Eds.), *Frontiers of Combining Systems, 6th International Symposium, FroCoS 2007, Liverpool, UK, September 10–12, 2007, Proceedings*, volume 4720 of *Lecture Notes in Computer Science*, Springer, 2007, pp. 73–87.
 [120] B. Alarcón, S. Lucas, J. Meseguer, *A dependency pair framework for $A \vee C$ -termination*, in: [1093], pp. 35–51.
 [121] F. Durán, S. Lucas, C. Marché, J. Meseguer, X. Urbain, *Proving operational termination of membership equational programs*, *Higher-Order and Symbolic Computation* 21 (2008) 59–88.
 [122] F. Durán, S. Lucas, J. Meseguer, *Methods for proving termination of rewriting-based programming languages by transformation*, in: [1016], pp. 93–113.
 [123] F. Durán, S. Lucas, J. Meseguer, *Termination modulo combinations of equational theories*, in: S. Ghilardi, R. Sebastiani (Eds.), *Frontiers of Combining Systems, 7th International Symposium, FroCoS 2009, Trento, Italy, September 16–18, 2009, Proceedings*, volume 5749 of *Lecture Notes in Computer Science*, Springer, 2009, pp. 246–262.
 [124] F. Durán, S. Lucas, J. Meseguer, C. Marché, X. Urbain, *Proving termination of membership equational programs*, in: N. Heintze, P. Sestoft (Eds.), *Proceedings of the 2004 ACM SIGPLAN Workshop on Partial Evaluation and Semantics-based Program Manipulation, PEPM 2004, Verona, Italy, August 24–25, 2004, ACM, 2004*, pp. 147–158.
 [125] O. Fissore, I. Gnaedig, H. Kirchner, *System presentation – CARIBOO: An induction based proof tool for termination with strategies*, in: [1070], pp. 62–73.

- [126] O. Fissore, I. Gnaedig, H. Kirchner, **Outermost ground termination**, in: [1056], pp. 188–207.
- [127] T. Genet, **Contraintes d'Ordre et Automates d'Arbres pour les Preuves de Terminaison**, Ph.D. thesis, Université Henri Poincaré – Nancy I, 1998.
- [128] I. Gnaedig, **Induction for positive almost sure termination**, in: [1079], pp. 167–178.
- [129] I. Gnaedig, H. Kirchner, **Termination of rewriting strategies: a generic approach**, in: M. Hofmann, H.W. Loidl (Eds.), Proceedings of the Third Workshop on Applied Semantics, APPSEM 2005, Chiemsee, Germany, September 12–15, 2005, Computing Research Repository (CoRR), 2005.
- [130] I. Gnaedig, H. Kirchner, **Termination of rewriting under strategies**, ACM Transactions on Computational Logic 10 (2009).
- [131] B. Gramlich, S. Lucas, **Modular termination of context-sensitive rewriting**, in: [1070], pp. 50–61.
- [132] B. Gramlich, S. Lucas, **Simple termination of context-sensitive rewriting**, in: [1050], pp. 29–42.
- [133] R. Gutiérrez, **Automatic Proofs of Termination of Context-Sensitive Rewriting**, Ph.D. thesis, Departamento de Sistemas Informáticos y Computación, Universidad Politécnica de Valencia, Spain, 2010.
- [134] R. Gutiérrez, S. Lucas, **Proving termination in the context-sensitive dependency pair framework**, in: [1093], pp. 18–34.
- [135] H. Kirchner, I. Gnaedig, **Termination and normalisation under strategy – Proofs in ELAN**, in: [1052], pp. 93–120.
- [136] S. Lucas, **Termination of on-demand rewriting and termination of OB programs**, in: Proceedings of the 3rd International ACM SIGPLAN Conference on Principles and Practice of Declarative Programming, PPDP 2001, Florence, Italy, September 5–7, 2001, ACM, 2001, pp. 82–93.
- [137] S. Lucas, **Termination of rewriting with strategy annotations**, in: R. Nieuwenhuis, A. Voronkov (Eds.), Logic for Programming, Artificial Intelligence, and Reasoning, 8th International Conference, LPAR 2001, Havana, Cuba, December 3–7, 2001, Proceedings, volume 2250 of *Lecture Notes in Computer Science*, Springer, 2001, pp. 669–684.
- [138] S. Lucas, C. Marché, J. Meseguer, **Operational termination of conditional term rewriting systems**, Information Processing Letters 95 (2005) 446–453.
- [139] S. Lucas, J. Meseguer, **Termination of fair computations in term rewriting**, in: [1105], pp. 184–198.
- [140] S. Lucas, J. Meseguer, **Order-sorted dependency pairs**, in: S. Antoy, E. Albert (Eds.), Proceedings of the 10th International ACM SIGPLAN Conference on Principles and Practice of Declarative Programming, PPDP 2008, Valencia, Spain, July 15–17, 2008, ACM, 2008, pp. 108–119.
- [141] S. Lucas, J. Meseguer, **Termination of just/fair computations in term rewriting**, Information and Computation 206 (2008) 652–675.
- [142] S. Lucas, J. Meseguer, **Operational termination of membership equational programs: the order-sorted way**, in: [1099], pp. 207–225.
- [143] F. Schernhammer, B. Gramlich, **Characterizing and proving operational termination of deterministic conditional term rewriting systems**, Journal of Logic and Algebraic Programming 79 (2010) 659–688.
- [144] F. Schernhammer, J. Meseguer, **Incremental checking of well-founded recursive specifications modulo axioms**, in: [1101], pp. 5–16.

2.5. Unification, narrowing, reachability analysis, and constraints

Narrowing generalizes rewriting by allowing unification (modulo equations) instead of matching (modulo equations) when applying a rule in a given step, thus instantiating variables both in the lefthand side of the rule being applied and in the term being reduced. In the context of rewriting logic, narrowing becomes a powerful method for symbolic reachability analysis with many important applications in automated deduction, constraint solving, model checking and security, among others.

- [145] M. Alpuente, D. Ballis, J. Espert, D. Romero, **Backward trace slicing for rewriting logic theories**, in: N. Bjørner, V. Sofronie-Stokkermans (Eds.), Automated Deduction – CADE-23 – 23rd International Conference on Automated Deduction, Wrocław, Poland, July 31–August 5, 2011, Proceedings, volume 6803 of *Lecture Notes in Computer Science*, Springer, 2011, pp. 34–48.
- [146] M. Alpuente, D. Ballis, F. Frechina, D. Romero, **Backward trace slicing for conditional rewrite theories**, in: N. Bjørner, A. Voronkov (Eds.), Logic for Programming, Artificial Intelligence, and Reasoning – 18th International Conference, LPAR-18, Mérida, Venezuela, March 11–15, 2012, Proceedings, volume 7180 of *Lecture Notes in Computer Science*, Springer, 2012, pp. 62–76.
- [147] M. Alpuente, S. Escobar, J. Meseguer, P. Ojeda, **A modular equational generalization algorithm**, in: M. Hanus (Ed.), Logic-Based Program Synthesis and Transformation, 18th International Symposium, LOPSTR 2008, Valencia, Spain, July 17–18, 2008, Revised Selected Papers, volume 5438 of *Lecture Notes in Computer Science*, Springer, 2009, pp. 24–39.
- [148] M. Alpuente, S. Escobar, J. Meseguer, P. Ojeda, **Order-sorted generalization**, in: M. Falaschi (Ed.), Proceedings of the 17th International Workshop on Functional and (Constraint) Logic Programming, WFLP 2008, Siena, Italy, July 3–4, 2008, volume 246 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2009, pp. 27–38.
- [149] P. Borovanský, **Implementation of higher-order unification based on calculus of explicit substitution**, in: M. Bartosek, J. Staudek, J. Wiedermann (Eds.), SOFSEM '95, 22nd Seminar on Current Trends in Theory and Practice of Informatics, Milovy, Czech Republic, November 23–December 1, 1995, Proceedings, volume 1012 of *Lecture Notes in Computer Science*, Springer, 1995, pp. 363–368.
- [150] G. Dowek, T. Hardin, C. Kirchner, **Higher order unification via explicit substitutions**, Information and Computation 157 (2000) 183–235.
- [151] G. Dowek, T. Hardin, C. Kirchner, **Theorem proving modulo**, Journal of Automated Reasoning 31 (2003) 33–72.
- [152] S. Escobar, J. Meseguer, **Symbolic model checking of infinite-state systems using narrowing**, in: [1023], pp. 153–168.
- [153] S. Escobar, J. Meseguer, R. Sasse, **Effectively checking the finite variant property**, in: [1108], pp. 79–93.
- [154] S. Escobar, J. Meseguer, R. Sasse, **Variant narrowing and equational unification**, in: [1099], pp. 103–119.
- [155] S. Escobar, J. Meseguer, P. Thati, **Natural narrowing for general term rewriting systems**, in: [1059], pp. 279–293.
- [156] S. Escobar, J. Meseguer, P. Thati, **Natural rewriting for general term rewriting systems**, in: S. Etalle (Ed.), Logic Based Program Synthesis and Transformation, 14th International Symposium, LOPSTR 2004, Verona, Italy, August 26–28, 2004, Revised Selected Papers, volume 3573 of *Lecture Notes in Computer Science*, Springer, 2005, pp. 101–116.
- [157] S. Escobar, J. Meseguer, P. Thati, **Narrowing and rewriting logic: from foundations to applications**, in: F.J. López-Fraguas (Ed.), Proceedings of the 15th Workshop on Functional and (Constraint) Logic Programming, WFLP 2006, Madrid, Spain, November 16–17, 2006, volume 177 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2007, pp. 5–33.
- [158] S. Escobar, R. Sasse, J. Meseguer, **Folding variant narrowing and optimal variant termination**, in: [1093], pp. 52–68.
- [159] S. Escobar, R. Sasse, J. Meseguer, **Folding variant narrowing and optimal variant termination**, Journal of Logic and Algebraic Programming (2012). This volume.
- [160] J. Hendrix, J. Meseguer, **Order-sorted equational unification revisited**, in: [1073], pp. 16–29.
- [161] H. Kirchner, **On the use of constraints in automated deduction**, in: A. Podelski (Ed.), Constraint Programming: Basics and Trends, Châtillon Spring School, Châtillon-sur-Seine, France, May 16–20, 1994, Selected Papers, volume 910 of *Lecture Notes in Computer Science*, Springer, 1995, pp. 128–146.
- [162] H. Kirchner, C. Ringeissen, **Combining symbolic constraint solvers on algebraic domains**, Journal of Symbolic Computation 18 (1994) 113–155.
- [163] H. Kirchner, C. Ringeissen, **Constraint solving by narrowing in combined algebraic domains**, in: Proceedings of the 11th International Conference on Logic Programming, The MIT Press, 1994, pp. 617–631.
- [164] J. Meseguer, P. Thati, **Symbolic reachability analysis using narrowing and its application to verification of cryptographic protocols**, in: [1085], pp. 153–182.
- [165] J. Meseguer, P. Thati, **Symbolic reachability analysis using narrowing and its application to verification of cryptographic protocols**, Higher-Order and Symbolic Computation 20 (2007) 123–160.

- [166] P. Thati, J. Meseguer, **Complete symbolic reachability analysis using back-and-forth narrowing**, in: J.L. Fiadeiro, N. Harman, M. Roggenbach, J.J.M.M. Rutten (Eds.), *Algebra and Coalgebra in Computer Science: First International Conference, CALCO 2005*, Swansea, UK, September 3–6, 2005, Proceedings, volume 3629 of *Lecture Notes in Computer Science*, Springer, 2005, pp. 379–394.
- [167] P. Thati, J. Meseguer, **Complete symbolic reachability analysis using back-and-forth narrowing**, *Theoretical Computer Science* 366 (2006) 163–179.

2.6. Reflection

Rewriting logic is reflective, that is, it can represent metalevel concepts, such as terms, theories, and deduction, at the object level, where they become usual data that can be used and manipulated by means of appropriate functions. All this is done in a so-called universal finitely presented rewrite theory. Logical reflection is a very powerful feature, related to computational reflection, and allows, among many other applications, the development of theorem proving and theory transformation tools, including theory composition operations that can be used to endow a language with a module algebra.

- [168] D. Basin, M. Clavel, J. Meseguer, **Reflective metalogical frameworks**, in: Proceedings of the Workshop on Logical Frameworks and Meta-languages, LFM'99, Paris, France, September 28, 1999.
- [169] D. Basin, M. Clavel, J. Meseguer, **Rewriting logic as a metalogical framework**, in: S. Kapoor, S. Prasad (Eds.), *Foundations of Software Technology and Theoretical Computer Science, 20th Conference, FST TCS 2000 New Delhi, India, December 13–15, 2000*, Proceedings, volume 1974 of *Lecture Notes in Computer Science*, Springer, 2000, pp. 55–80.
- [170] D. Basin, M. Clavel, J. Meseguer, **Reflective metalogical frameworks**, *ACM Transactions on Computational Logic* 5 (2004) 528–576.
- [171] M. Clavel, **Reflection in General Logics and in Rewriting Logic**, with Applications to the Maude Language, Ph.D. thesis, Universidad de Navarra, Spain, 1998.
- [172] M. Clavel, **Reflection in general logics, rewriting logic, and Maude**, in: [1071], pp. 71–82.
- [173] M. Clavel, **Reflection in Rewriting Logic: Metalogical Foundations and Metaprogramming Applications**, CSLI Publications, 2000.
- [174] M. Clavel, F. Durán, S. Eker, P. Lincoln, N. Martí-Oliet, J. Meseguer, **Metalevel computation in Maude**, in: [1071], pp. 331–352.
- [175] M. Clavel, F. Durán, S. Eker, P. Lincoln, N. Martí-Oliet, J. Meseguer, **Language prototyping in the Maude metalanguage**, in: F. Orejas, F. Cuartero, D. Cazorla (Eds.), *Actas de las Primeras Jornadas sobre Programación y Lenguajes, PROLE 2001*, Almagro (Ciudad Real), España, Noviembre 23–24, 2001, Universidad de Castilla La Mancha, 2001, pp. 93–110.
- [176] M. Clavel, F. Durán, S. Eker, P. Lincoln, N. Martí-Oliet, J. Meseguer, J.F. Quesada, **Maude as a metalanguage**, in: [1071], pp. 147–160.
- [177] M. Clavel, F. Durán, S. Eker, J. Meseguer, **Building equational proving tools by reflection in rewriting logic**, in: [1051].
- [178] M. Clavel, F. Durán, S. Eker, J. Meseguer, **Building equational proving tools by reflection in rewriting logic**, in: [1055], pp. 1–31.
- [179] M. Clavel, F. Durán, S. Eker, J. Meseguer, M.O. Stehr, **Maude as a formal meta-tool**, in: [1109], pp. 1684–1703.
- [180] M. Clavel, N. Martí-Oliet, M. Palomino, **Formalizing and proving semantic relations between specifications by reflection**, in: [1098], pp. 72–86.
- [181] M. Clavel, N. Martí-Oliet, M. Palomino, **Parameterized metareasoning in membership equational logic**, in: [1015], pp. 277–298.
- [182] M. Clavel, J. Meseguer, **Axiomatizing reflective logics and languages**, in: G. Kiczales (Ed.), *Proceedings of Reflection'96*, San Francisco, California, April 1996, pp. 263–288.
- [183] M. Clavel, J. Meseguer, **Reflection and strategies in rewriting logic**, in: [1086], pp. 126–148.
- [184] M. Clavel, J. Meseguer, **Internal strategies in a reflective logic**, in: [1061], pp. 1–12.
- [185] M. Clavel, J. Meseguer, **Reflection in rewriting logic and its applications in the Maude language**, in: Proceedings of IMSA'97, Information-Technology Promotion Agency, Japan, 1997, pp. 128–139.
- [186] M. Clavel, J. Meseguer, **Reflection in conditional rewriting logic**, *Theoretical Computer Science* 285 (2002) 245–288.
- [187] M. Clavel, J. Meseguer, M. Palomino, **Reflection in membership equational logic, many-sorted equational logic, Horn logic with equality, and rewriting logic**, in: [1056], pp. 110–116.
- [188] M. Clavel, J. Meseguer, M. Palomino, **Reflection in membership equational logic, many-sorted equational logic, Horn logic with equality, and rewriting logic**, *Theoretical Computer Science* 373 (2007) 70–91.
- [189] G. Denker, J. Meseguer, C.L. Talcott, **Rewriting semantics of meta-objects and composable distributed services**, in: [1052], pp. 405–425.
- [190] E.I. Goriac, G. Caltais, D. Lucanu, O. Andrei, G. Grigoras, **Patterns for Maude metalanguage applications**, in: [1099], pp. 121–138.
- [191] H. Kirchner, P.E. Moreau, **A reflective extension of ELAN**, in: [1086], pp. 149–168.
- [192] J. Meseguer, C.L. Talcott, **Semantic models for distributed object reflection**, in: B. Magnusson (Ed.), *ECOOP 2002 – Object-Oriented Programming*, 16th European Conference, Malaga, Spain, June 10–14, 2002, Proceedings, volume 2374 of *Lecture Notes in Computer Science*, Springer, 2002, pp. 1–36.
- [193] M. Numazawa, M. Kurihara, A. Ohuchi, **A reflective language based on conditional term rewriting**, in: J. Calmet, C. Limongelli (Eds.), *Design and Implementation of Symbolic Computation Systems, International Symposium, DISCO '96*, Karlsruhe, Germany, September 18–20, 1996, Proceedings, volume 1128 of *Lecture Notes in Computer Science*, Springer, 1996, pp. 65–66.
- [194] I. Pita, N. Martí-Oliet, **Using reflection to specify transaction sequences in rewriting logic**, in: [1047], pp. 261–276.
- [195] Y. Tahara, F. Kumeno, A. Ohsuga, S. Honiden, **An algebraic semantics of reflective objects**, in: K. Futatsugi, S. Matsuoka (Eds.), *Object Technologies for Advanced Software, Second JSSST International Symposium, ISOTAS '96*, Kanazawa, Japan, March 11–15, 1996, Proceedings, volume 1049 of *Lecture Notes in Computer Science*, Springer, 1996, pp. 173–189.
- [196] T. Watanabe, **Towards a foundation of computational reflection based on abstract rewriting (preliminary result)**, in: Proceedings of IMSA'95, Information-Technology Promotion Agency, Japan, 1995, pp. 143–145.
- [197] T. Watanabe, H. Ishikawa, K. Futatsugi, **Towards declarative description of computational reflection**, in: Proceedings of IMSA'96, Information-Technology Promotion Agency, Japan, 1996, pp. 113–128.

2.7. Strategies

Strategies are pervasive in computer science. In the context of rewriting logic, they can be used to control rewriting by equations (in the form of operator evaluation strategies), rewriting by rules, and more recently narrowing. A typical way of controlling the nondeterministic behavior of rules is to provide a strategy language which can be used to guide the application of rules. Several languages based on rewriting logic, such as ELAN, Maude, Stratego, and Tom, have developed this approach in different ways; in particular, Maude has made use of the reflective features of rewriting logic to define so-called internal strategies before providing a explicit strategy language.

- [198] M. Alpuente, S. Escobar, B. Gramlich, S. Lucas, **On-demand strategy annotations revisited: An improved on-demand evaluation strategy**, *Theoretical Computer Science* 411 (2010) 504–541.
- [199] M. Alpuente, S. Escobar, S. Lucas, **On-demand evaluation by program transformation**, in: [1058], pp. 92–118.

- [200] M. Alpuente, S. Escobar, S. Lucas, **OnDemandOBJ: A laboratory for strategy annotations**, in: [1058], pp. 1–27.
- [201] M. Alpuente, S. Escobar, S. Lucas, **Correct and complete (positive) strategy annotations for OBJ**, in: [1056], pp. 70–89.
- [202] O. Andrei, G. Ciobanu, D. Lucanu, **Expressing control mechanisms of membranes by rewriting strategies**, in: H.J. Hoogboom, G. Paur, G. Rozenberg, A. Salamoa (Eds.), *Membrane Computing, 7th International Workshop, WMC 2006, Leiden, The Netherlands, July 17–21, 2006, Revised, Selected, and Invited Papers*, volume 4361 of *Lecture Notes in Computer Science*, Springer, 2006, pp. 154–169.
- [203] O. Andrei, D. Lucanu, **Strategy-based proof calculus for membrane systems**, in: [1099], pp. 23–43.
- [204] L. Astefanoaei, F.S. de Boer, M. Dastani, **Rewriting agent societies strategically**, in: *Proceedings of the 2009 IEEE/WIC/ACM International Conference on Web Intelligence and International Conference on Intelligent Agent Technology – Workshops*, Milan, Italy, September 15–18, 2009, IEEE, 2009, pp. 441–444.
- [205] P. Borovský, **Controlling rewriting: Study and implementation of a strategy formalism**, in: [1071], pp. 299–310.
- [206] P. Borovský, **Le Contrôle de la Réécriture: Étude et Implémentation d'un Formalisme de Stratégies**, Ph.D. thesis, Université Henri Poincaré – Nancy I, 1998.
- [207] P. Borovský, C. Kirchner, H. Kirchner, **Controlling rewriting by rewriting**, in: [1086], pp. 169–189.
- [208] P. Borovský, H. Kirchner, **Strategies of ELAN: Meta-interpretation and partial evaluation**, in: [1103].
- [209] T. Bourdier, H. Cirstea, D.J. Dougherty, H. Kirchner, **Extensional and intensional strategies**, in: M. Fernández (Ed.), *Proceedings of the Ninth International Workshop on Reduction Strategies in Rewriting and Programming, WRS 2009, Brasília, Brazil, June 28, 2009*, volume 15 of *Electronic Proceedings in Theoretical Computer Science*, Computing Research Repository (CoRR), 2010, pp. 1–19.
- [210] C. Braga, A. Verdejo, **Modular structural operational semantics with strategies**, in: R. van Glabbeek, P.D. Mosses (Eds.), *Proceedings of the Third Workshop on Structural Operational Semantics, SOS 2006, Bonn, Germany, August 26, 2006*, volume 175(1) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2007, pp. 3–17.
- [211] C. Castro, **Solving binary CSP using computational systems**, in: [1086], pp. 246–265.
- [212] C. Castro, **Constraint manipulation using rewrite rules and strategies**, in: *Proceedings of the Second ESSLLI Student Session, 9th European Summer School in Logic, Language and Information, ESSLLI'97, Aix-en-Provence, France, August 11–22, 1997*, pp. 45–56.
- [213] C. Castro, **Building constraint satisfaction problem solvers using rewrite rules and strategies**, *Fundamenta Informaticae* 34 (1998) 263–293.
- [214] C. Castro, **Une Approche Dédicative de la Résolution de Problèmes de Satisfacion de Contraintes**, Ph.D. thesis, Université Henri Poincaré – Nancy I, 1998.
- [215] H. Cirstea, **Specifying authentication protocols using rewriting and strategies**, in: I.V. Ramakrishnan (Ed.), *Practical Aspects of Declarative Languages, Third International Symposium, PADL 2001, Las Vegas, Nevada, March 11–12, 2001*, *Proceedings*, volume 1990 of *Lecture Notes in Computer Science*, Springer, 2001, pp. 138–152.
- [216] H. Cirstea, C. Kirchner, **Theorem proving using computational systems: The case of the B predicate prover**, Presented at *CCL'97 Workshop*, Schloss Dagstuhl, Germany, 1997.
- [217] H. Cirstea, C. Kirchner, **Using rewriting and strategies for describing the B predicate prover**, in: [1063], pp. 25–36.
- [218] M. Clavel, **Strategies and user interfaces in Maude at work**, in: [1062], pp. 570–592.
- [219] F. Durán, S. Escobar, S. Lucas, **On-demand evaluation for Maude**, in: [1014], pp. 25–39.
- [220] F. Durán, M. Roldán, **Invariant-based control of the execution of Maude specifications: The LTL case**, in: [1082], pp. 137–152.
- [221] F. Durán, M. Roldán, A. Vallecillo, **Invariant-driven strategies for Maude**, in: [1018], pp. 17–28.
- [222] S. Eker, **Term rewriting with operator evaluation strategies**, in: [1071], pp. 311–330.
- [223] S. Eker, N. Martí-Oliet, J. Meseguer, A. Verdejo, **Deduction, strategies, and rewriting**, in: M. Archer, T.B. de la Tour, C. Muñoz (Eds.), *Proceedings of the 6th International Workshop on Strategies in Automated Deduction, STRATEGIES 2006, Seattle, WA, USA, August 16, 2006*, volume 174(11) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2007, pp. 3–25.
- [224] O. Fissore, I. Gnaedig, H. Kirchner, **Simplification and termination of strategies in rule-based languages**, in: D. Miller, K. Sagonas (Eds.), *Proceedings of the 5th International ACM SIGPLAN Conference on Principles and Practice of Declarative Programming, PPDP 2003, Uppsala, Sweden, August 27–29, 2003*, ACM, 2003, pp. 124–135.
- [225] C. Kirchner, **Strategic rewriting**, in: [1018], pp. 3–9.
- [226] C. Kirchner, F. Kirchner, H. Kirchner, **Strategic computation and deduction**, in: C. Benzmüller, C.E. Brown, J. Siekmann, R. Statman (Eds.), *Reasoning in Simple Type Theory. Festschrift in Honour of Peter B. Andrews on His 70th Birthday*, volume 17 of *Studies in Logic and the Foundations of Mathematics*, College Publications, 2008, pp. 339–364.
- [227] C. Kirchner, F. Kirchner, H. Kirchner, **Constraint based strategies**, in: S. Escobar (Ed.), *Functional and Constraint Logic Programming, 18th International Workshop, WFLP 2009, Brasília, Brazil, June 28, 2009, Revised Selected Papers*, volume 5979 of *Lecture Notes in Computer Science*, Springer, 2010, pp. 13–26.
- [228] P. Lincoln, J. Meseguer, **Strategic reflection**, in: [1063], pp. 3–9.
- [229] D. Lucanu, G. Roşu, G. Grigoras, **Regular strategies as proof tactics for CIRC**, in: [1060], pp. 83–98.
- [230] S. Lucas, **Context-sensitive rewriting strategies**, *Information and Computation* 178 (2002) 294–343.
- [231] N. Martí-Oliet, J. Meseguer, A. Verdejo, **Towards a strategy language for Maude**, in: [1085], pp. 417–441.
- [232] N. Martí-Oliet, J. Meseguer, A. Verdejo, **A rewriting semantics for Maude strategies**, in: [1099], pp. 227–247.
- [233] H. Merouani, F. Mokhati, H. Seridi-Bouchelaghem, **Towards formalizing web service composition in Maude's strategy language**, in: A. Alnsour, S. Aljawarneh (Eds.), *Proceedings of the 1st International Conference on Intelligent Semantic Web-Services and Applications, ISWSA 2010, Amman, Jordan, June 14–16, 2010*, ACM, 2010, p. 15.
- [234] M. Nakamura, K. Ogata, **The evaluation strategy for head normal form with and without on-demand flags**, in: [1052], pp. 212–228.
- [235] M. Roldán, F. Durán, A. Vallecillo, **Invariant-driven specifications in Maude**, *Science of Computer Programming* 74 (2009) 812–835.
- [236] G. Santos-García, M. Palomino, **Solving sudoku puzzles with rewriting rules**, in: [1039], pp. 79–93.
- [237] G. Santos-García, M. Palomino, A. Verdejo, **Neural networks in Maude**, in: G. Kiesel, J.S. Pinto (Eds.), *Preliminary Proceedings of the Ninth International Workshop on Rule-Based Programming, RULE 2008, Hagenberg Castle, Austria, June 18, 2008*, pp. 61–75.
- [238] G. Santos-García, M. Palomino, A. Verdejo, **Rewriting logic using strategies for neural networks: An implementation in Maude**, in: J.M. Corchado, S. Rodríguez, J. Llinas, J.M. Molina (Eds.), *Proceedings of the International Symposium on Distributed Computing and Artificial Intelligence, DCAI 2008, University of Salamanca, Spain, October 22–24, 2008*, volume 50 of *Advances in Soft Computing*, Springer, 2009, pp. 424–433.
- [239] A. Verdejo, N. Martí-Oliet, **Basic completion strategies as another application of the Maude strategy language**, in: S. Escobar (Ed.), *Proceedings of the 10th International Workshop on Reduction Strategies in Rewriting and Programming, WRS 2011, Novi Sad, Serbia, May 29, 2011*, volume 82 of *Electronic Proceedings in Theoretical Computer Science*, pp. 17–36.
- [240] E. Visser, **Program transformation with Stratego/XT: Rules, strategies, tools, and systems in Stratego/XT 0.9**, in: C. Lengauer, D.S. Batory, C. Consel, M. Odersky (Eds.), *Domain-Specific Program Generation, International Seminar, Dagstuhl Castle, Germany, March 23–28, 2003, Revised Papers*, volume 3016 of *Lecture Notes in Computer Science*, Springer, 2004, pp. 216–238.

2.8. Modal and temporal logic properties

Rewrite theories specify concurrent systems, whose properties can be specified in different logics, namely, appropriate modal and temporal logics. Indeed, on the one hand one can associate to a rewrite theory a transition system and, with suitable definitions of state predicates, a Kripke structure, and then one can use standard procedures to model check temporal properties in classical temporal logics, such as linear temporal logic (LTL). This path has led to the integration of an LTL model checker into Maude, for example. On the other hand, it is also possible to develop new modal and temporal logics, more

directly related to rewriting, such as the temporal logic of rewriting (TLR), and then it is necessary to develop new model-checking algorithms for such logics.

- [241] K. Bae, J. Meseguer, State/event-based LTL model checking under parametric generalized fairness, in: G. Gopalakrishnan, S. Qadeer (Eds.), Computer Aided Verification – 23rd International Conference, CAV 2011, Snowbird, UT, USA, July 14–20, 2011. Proceedings, volume 6806 of *Lecture Notes in Computer Science*, Springer, 2011, pp. 132–148.
- [242] G. Denker, From rewrite theories to temporal logic theories, in: [1071], pp. 105–126.
- [243] J.L. Fiadeiro, T.S.E. Maibaum, N. Martí-Oliet, J. Meseguer, I. Pita, Towards a verification logic for rewriting logic, in: D. Bert, C. Choppy, P.D. Mosses (Eds.), Recent Trends in Algebraic Development Techniques, 14th International Workshop, WADT '99, Château de Bonas, France, September 15–18, 1999, Selected Papers, volume 1827 of *Lecture Notes in Computer Science*, Springer, 2000, pp. 438–458.
- [244] K. Havelund, G. Roşu, Monitoring programs using rewriting, in: [1044], pp. 135–143.
- [245] K. Havelund, G. Roşu, Testing Linear Temporal Logic Formulae on Finite Execution Traces, Technical Report RIACS 01.08, Research Institute for Advanced Computer Science, 2001.
- [246] K. Havelund, G. Roşu, Efficient monitoring of safety properties, *International Journal on Software Tools for Technology Transfer* 6 (2004) 158–173.
- [247] N. Martí-Oliet, I. Pita, J.L. Fiadeiro, J. Meseguer, T.S.E. Maibaum, A verification logic for rewriting logic, *Journal of Logic and Computation* 15 (2005) 317–352.
- [248] J. Meseguer, Localized fairness: A rewriting semantics, in: [1059], pp. 250–263.
- [249] J. Meseguer, The Temporal Logic of Rewriting, Technical Report UIUCDCS-R-2007-2815, Department of Computer Science, University of Illinois at Urbana-Champaign, 2007.
- [250] J. Meseguer, The temporal logic of rewriting: A gentle introduction, in: [1038], pp. 354–382.
- [251] M. Palomino, I. Pita, Proving VLR action properties with the Maude model checker, in: [1085], pp. 113–133.
- [252] D. Pattinson, Modal logic for rewriting theories, in: [1052], pp. 175–193.
- [253] I. Pita, Técnicas de Especificación Formal de Sistemas Orientados a Objetos Basadas en Lógica de Reescritura, Ph.D. thesis, Facultad de Matemáticas, Universidad Complutense de Madrid, Spain, 2003.
- [254] I. Pita, M. Palomino, Proving modal properties of rewrite theories using Maude's metalevel, in: [1081], pp. 133–150.
- [255] C. Rocha, J. Meseguer, Proving Safety Properties of Rewrite Theories, Technical Report, Department of Computer Science, University of Illinois at Urbana-Champaign, 2010.
- [256] C. Rocha, J. Meseguer, Proving safety properties of rewrite theories, in: [1036], pp. 314–328.
- [257] G. Roşu, F. Chen, T. Ball, Synthesizing monitors for safety properties: This time with calls and returns, in: M. Leucker (Ed.), Runtime Verification, 8th International Workshop, RV 2008, Budapest, Hungary, March 30, 2008. Selected Papers, volume 5289 of *Lecture Notes in Computer Science*, Springer, 2008, pp. 51–68.
- [258] G. Roşu, K. Havelund, Generating Optimal Monitors from Temporal Formulae, Technical Report, Research Institute for Advanced Computer Science, 2001.
- [259] G. Roşu, K. Havelund, Rewriting-based techniques for runtime verification, *Automated Software Engineering* 12 (2005) 151–197.
- [260] B.Y. Wang, μ -calculus model checking in Maude, in: [1085], pp. 135–152.
- [261] B.Y. Wang, Specification of an infinite-state local model checker in rewriting logic, in: W.C. Chu, N.J. Juzgado, W.E. Wong (Eds.), Proceedings of the 17th International Conference on Software Engineering and Knowledge Engineering, SEKE 2005, Taipei, Taiwan, Republic of China, July 14–16, 2005, pp. 442–447.

2.9. Simulation and abstraction

A simulation relates two transition systems or two Kripke structures so that, under appropriate conditions, properties are translated from one system or structure to the other. This can be very useful, for example, to model check a temporal logic property of a system that in principle does not satisfy model-checking requirements such as being finite. The rewriting logic distinction between equations and rules provides direct abstraction methods consisting of turning rules into equations and of adding more equations, always trying to preserve the properties of interest and the executability conditions of the specifications.

- [262] A. Farzan, J. Meseguer, State space reduction of rewrite theories using invisible transitions, in: [1069], pp. 142–157.
- [263] A. Farzan, J. Meseguer, Partial order reduction for rewriting semantics of programming languages, in: [1039], pp. 61–78.
- [264] L.H. Haß, T. Noll, Equational abstractions for reducing the state space of rewrite theories, in: [1099], pp. 139–154.
- [265] U. Lechner, Constructs, concepts and criteria for reuse in concurrent object-oriented languages, in: E. Astesiano (Ed.), Fundamental Approaches to Software Engineering, First International Conference, FASE'98, Held as Part of ETAPS'98, Lisbon, Portugal, March 28–April 4, 1998, Proceedings, volume 1382 of *Lecture Notes in Computer Science*, Springer, 1998, pp. 171–187.
- [266] N. Martí-Oliet, J. Meseguer, M. Palomino, Theoretical maps as algebraic simulations, in: [1049], pp. 126–143.
- [267] N. Martí-Oliet, J. Meseguer, M. Palomino, Algebraic stuttering simulations, in: E. Pimentel (Ed.), Proceedings of the Seventh Spanish Conference on Programming and Computer Languages, PROLE 2007, Zaragoza, Spain, September 12–14, 2007, volume 206 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2008, pp. 91–110.
- [268] N. Martí-Oliet, M. Palomino, A. Verdejo, Strategies and simulations in a semantic framework, *Journal of Algorithms* 62 (2007) 95–116.
- [269] J. Meseguer, M. Palomino, N. Martí-Oliet, Equational abstractions, in: [1022], pp. 2–16.
- [270] J. Meseguer, M. Palomino, N. Martí-Oliet, Equational abstractions, *Theoretical Computer Science* 403 (2008) 239–264.
- [271] J. Meseguer, M. Palomino, N. Martí-Oliet, Algebraic simulations, *Journal of Logic and Algebraic Programming* 79 (2010) 103–143.
- [272] M.R. Neuhäuser, T. Noll, Abstraction and model checking of Core Erlang programs in Maude, in: [1039], pp. 147–163.
- [273] P.C. Ölveczky, J. Meseguer, Abstraction and completeness for Real-Time Maude, in: [1039], pp. 5–27.
- [274] M. Palomino, Reflexión, Abstracción y Simulación en la Lógica de Reescritura, Ph.D. thesis, Facultad de Matemáticas, Universidad Complutense de Madrid, Spain, 2005.
- [275] M. Palomino, J. Meseguer, N. Martí-Oliet, A categorical approach to simulations, in: [1048], pp. 313–330.
- [276] D.E. Rodríguez, Combining techniques to reduce state space and prove strong properties, in: [1099], pp. 267–280.

2.10. Real-time rewrite theories

The essential role played by real-time properties in the specification of many systems has led to the development of suitable methodologies to treat such properties in rewrite theories. Although it is possible to consider an extension of rewriting logic by adding explicit time to transitions, called timed rewriting logic, the most popular approach has been to consider a special kind of rewrite theories, called real-time rewrite theories, where time is taken care of by appropriate rules.

This second approach allows to represent different time domains as well as a wide range of models of real-time systems, and has been implemented in the Real-Time Maude tool, considered in Section 6.2.

- [277] P. Kosiuczenko, M. Wirsing, Timed rewriting logic for the specification of time-sensitive systems, in: H. Schwichtenberg (Ed.), Proceedings of the NATO Advanced Study Institute on Logic of Computation, Held in Marktobendorf, Germany, July 25–August 6, 1997, volume 157 of *NATO ASI Series F: Computer and Systems Sciences*, Springer, 1997, pp. 229–264.
- [278] P. Kosiuczenko, M. Wirsing, Timed rewriting logic with an application to object-based specification, *Science of Computer Programming* 28 (1997) 225–246.
- [279] D. Lepri, P.C. Ölveczky, E. Abraham, Timed CTL model checking in Real-Time Maude, in: [1042]. To appear.
- [280] P.C. Ölveczky, Specification and Analysis of Real-Time and Hybrid Systems in Rewriting Logic, Ph.D. thesis, University of Bergen, Norway, 2000.
- [281] P.C. Ölveczky, J. Meseguer, Specifying real-time systems in rewriting logic, in: [1086], pp. 284–309.
- [282] P.C. Ölveczky, J. Meseguer, Specification of real-time and hybrid systems in rewriting logic, *Theoretical Computer Science* 285 (2002) 359–405.
- [283] P.C. Ölveczky, J. Meseguer, Semantics and pragmatics of Real-Time Maude, *Higher-Order and Symbolic Computation* 20 (2007) 161–196.

2.11. Probabilistic rewrite theories

To take into account the nondeterministic and probabilistic aspects in many distributed systems, probabilistic rewrite rules have extra variables whose instantiation is controlled by a probability distribution. These rules constitute probabilistic rewrite theories, which can be used to represent different models of probabilistic systems. See Section 6.6 for applications of probabilistic rewrite theories.

- [284] M. Aiguier, D. Bahrami, D. Longuet, An abstract way to define rewriting logic, in: [1020], pp. 205–226.
- [285] O. Bournez, M. Hoyrup, Rewriting logic and probabilities, in: [1090], pp. 61–75.
- [286] N. Kumar, K. Sen, J. Meseguer, G. Agha, Probabilistic Rewrite Theories: Unifying Models, Logics and Tools, Technical Report UIUCDCS-R-2003-2347, Department of Computer Science, University of Illinois at Urbana-Champaign, 2003.
- [287] N. Kumar, K. Sen, J. Meseguer, G. Agha, A rewriting based model for probabilistic distributed object systems, in: [1089], pp. 32–46.

2.12. Tile logic

Tile logic was designed as “a logic for modular descriptions of asynchronous and synchronized concurrent systems,” and it consists of an extension of (unconditional) rewriting logic obtained by adding state changes with side effects and synchronization. This allows the representation of process calculi, reactive systems, coordination languages, mobile calculi, etc.

- [288] F. Arbab, R. Bruni, D. Clarke, I. Lanese, U. Montanari, Tiles for Reo, in: [1037], pp. 37–55.
- [289] R. Bruni, A logic for modular descriptions of asynchronous and synchronized concurrent systems, in: [1071], pp. 161–172.
- [290] R. Bruni, Tile Logic for Synchronized Rewriting of Concurrent Systems, Ph.D. thesis, Dipartimento di Informatica, Università di Pisa, 1999. Technical Report TD-1/99.
- [291] R. Bruni, J.L. Fiadeiro, I. Lanese, A. Lopes, U. Montanari, New insights on architectural connectors, in: J.J. Lévy, E.W. Mayr, J.C. Mitchell (Eds.), Exploring New Frontiers of Theoretical Informatics, IFIP 18th World Computer Congress, TC1 3rd International Conference on Theoretical Computer Science, TCS 2004, Toulouse, France, August 22–27, 2004, Kluwer, 2004, pp. 367–380.
- [292] R. Bruni, D. de Frutos-Escrig, N. Martí-Oliet, U. Montanari, Bisimilarity congruences for open terms and term graphs via tile logic, in: C. Palamidessi (Ed.), CONCUR 2000 – Concurrency Theory, 11th International Conference, University Park, PA, USA, August 22–25, 2000, Proceedings, volume 1877 of *Lecture Notes in Computer Science*, Springer, 2000, pp. 259–274.
- [293] R. Bruni, D. de Frutos-Escrig, N. Martí-Oliet, U. Montanari, Tile Bisimilarity Congruences for Open Terms and Term Graphs, Technical Report TR-00-06, Dipartimento di Informatica, Università di Pisa, 2000.
- [294] R. Bruni, F. Gadducci, U. Montanari, P. Sobocinski, Deriving weak bisimulation congruences from reduction systems, in: M. Abadi, L. de Alfaro (Eds.), CONCUR 2005 – Concurrency Theory, 16th International Conference, CONCUR 2005, San Francisco, CA, USA, August 23–26, 2005, Proceedings, volume 3653 of *Lecture Notes in Computer Science*, Springer, 2005, pp. 293–307.
- [295] R. Bruni, F. Honsell, M. Lenisa, M. Miculan, Comparing higher-order encodings in logical frameworks and tile logic, in: [1077], pp. 136–156.
- [296] R. Bruni, I. Lanese, On graph(ic) encodings, in: B. König, U. Montanari, P. Gardner (Eds.), Graph Transformations and Process Algebras for Modeling Distributed and Mobile Systems, Dagstuhl, Germany, June 6–11, 2004, volume 04241 of *Dagstuhl Seminar Proceedings*, IBFI, Schloss Dagstuhl, Germany, 2005.
- [297] R. Bruni, H.C. Melgratti, U. Montanari, A connector algebra for P/T nets interactions, in: J.P. Katoen, B. König (Eds.), CONCUR 2011 – Concurrency Theory – 22nd International Conference, CONCUR 2011, Aachen, Germany, September 6–9, 2011. Proceedings, volume 6901 of *Lecture Notes in Computer Science*, Springer, 2011, pp. 312–326.
- [298] R. Bruni, H.C. Melgratti, U. Montanari, Connector algebras, Petri nets, and BIP, in: E. Clarke, I. Virbitskaite, A. Voronkov (Eds.), Perspectives of Systems Informatics, 8th Andrei Ershov Informatics Conference, PSI 2011, Novosibirsk, Akademgorodok, Russia, June, 27–July, 1, 2011. Proceedings, *Lecture Notes in Computer Science*, Springer, 2011. To appear.
- [299] R. Bruni, J. Meseguer, U. Montanari, Implementing tile systems: Some examples from process calculi, in: P. Degano, U. Vaccaro, G. Pirillo (Eds.), Proceedings of the 6th Italian Conference on Theoretical Computer Science, ICTCS’98, Prato, Italy, November 9–11, 1998, World Scientific, 1998, pp. 168–179.
- [300] R. Bruni, J. Meseguer, U. Montanari, Process and Term Tile Logic, Technical Report SRI-CSL-98-06, Computer Science Laboratory, SRI International, 1998. Also TR-98-09, Dipartimento di Informatica, Università di Pisa.
- [301] R. Bruni, J. Meseguer, U. Montanari, Executable tile specifications for process calculi, in: J.P. Finance (Ed.), Fundamental Approaches to Software Engineering, Second International Conference, FASE’99, Held as Part of the European Joint Conferences on the Theory and Practice of Software, ETAPS’99, Amsterdam, The Netherlands, March 22–28, 1999, Proceedings, volume 1577 of *Lecture Notes in Computer Science*, Springer, 1999, pp. 60–76.
- [302] R. Bruni, J. Meseguer, U. Montanari, Symmetric monoidal and cartesian double categories as a semantic framework for tile logic, *Mathematical Structures in Computer Science* 12 (2002) 53–90.
- [303] R. Bruni, J. Meseguer, U. Montanari, Tiling transactions in rewriting logic, in: [1056], pp. 90–109.
- [304] R. Bruni, U. Montanari, Cartesian closed double categories, their lambda-notation, and the pi-calculus, in: Proceedings of the Fourteenth Annual IEEE Symposium on Logic in Computer Science, LICS’99, Trento, Italy, July 2–5, 1999, pp. 246–265.
- [305] R. Bruni, U. Montanari, Dynamic connectors for concurrency, *Theoretical Computer Science* 281 (2002) 131–176.
- [306] R. Bruni, U. Montanari, J. Meseguer, Internal strategies in a rewriting implementation of tile systems, in: [1071], pp. 263–284.
- [307] R. Bruni, U. Montanari, F. Rossi, An interactive semantics of logic programming, *Theory and Practice of Logic Programming* 1 (2001) 647–690.

- [308] R. Bruni, U. Montanari, V. Sassone, **Open ended systems, dynamic bisimulation and tile logic**, in: J. van Leeuwen, O. Watanabe, M. Hagiya, P.D. Mosses, T. Ito (Eds.), *Theoretical Computer Science, Exploring New Frontiers of Theoretical Informatics*, International Conference IFIP TCS 2000, Sendai, Japan, August 17–19, 2000, Proceedings, volume 1872 of *Lecture Notes in Computer Science*, Springer, 2000, pp. 440–456.
- [309] R. Bruni, U. Montanari, V. Sassone, **Observational congruences for dynamically reconfigurable tile systems**, *Theoretical Computer Science* 335 (2005) 331–372.
- [310] A. Corradini, R. Heckel, U. Montanari, **Tile transition systems as structured coalgebras**, in: G. Ciobanu, G. Paun (Eds.), *Fundamentals of Computation Theory, 12th International Symposium, FCT '99*, Iasi, Romania, August 30–September 3, 1999, Proceedings, volume 1684 of *Lecture Notes in Computer Science*, Springer, 1999, pp. 13–38.
- [311] G.L. Ferrari, U. Montanari, **A tile-based coordination view of asynchronous π -calculus**, in: I. Prívara, P. Ruzicka (Eds.), *Mathematical Foundations of Computer Science 1997, 22nd International Symposium, MFCS'97*, Bratislava, Slovakia, August 25–29, 1997, Proceedings, volume 1295 of *Lecture Notes in Computer Science*, Springer, 1997, pp. 52–70.
- [312] G.L. Ferrari, U. Montanari, **Tiles for concurrent and located calculi**, in: C. Palamidessi, J. Parrow (Eds.), *Proceedings of the 4th workshop on Expressiveness in Concurrency, EXPRESS'97*, Santa Margherita Ligure, Italy, September 8–12, 1997, volume 7 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 1997, pp. 115–140.
- [313] G.L. Ferrari, U. Montanari, **Tile formats for located and mobile systems**, *Information and Computation* 156 (2000) 173–235.
- [314] F. Gadducci, P. Katis, U. Montanari, N. Sabadini, R.F.C. Walters, **Comparing cospan-spans and tiles via a Hoare-style process calculus**, in: [1077], pp. 157–176.
- [315] F. Gadducci, U. Montanari, **Tiles, rewriting rules, and CCS**, in: [1086], pp. 1–19.
- [316] F. Gadducci, U. Montanari, **The tile model**, in: G.D. Plotkin, C. Stirling, M. Tofte (Eds.), *Proof, Language, and Interaction, Essays in Honour of Robin Milner*, The MIT Press, 2000, pp. 133–166.
- [317] F. Gadducci, U. Montanari, **Comparing logics for rewriting: rewriting logic, action calculi and tile logic**, *Theoretical Computer Science* 285 (2002) 319–358.
- [318] J. Meseguer, U. Montanari, **Mapping tile logic into rewriting logic**, in: [1096], pp. 62–91.
- [319] U. Montanari, C.L. Talcott, **Can actors and π -agents live together?**, in: A.D. Gordon, A.M. Pitts, C.L. Talcott (Eds.), *Proceedings of the Second Workshop on Higher-Order Operational Techniques in Semantics, HOOTS II*, Stanford University, California, December 8–12, 1997, volume 10 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 1998, pp. 189–196.

3. Rewriting logic as a logical and semantic framework

Since rewriting logic was already introduced as “a general framework for unifying a wide variety of models of concurrency,” it is not surprising that many different models of concurrent systems can be represented as rewrite theories; however, this also holds for many other models of computation, as well as very diverse logics (such as linear logic and type theories) and inference systems. For all these reasons, rewriting logic clearly deserves the designation of logical and semantic framework.

3.1. Representing logics

The following papers make use of rewriting logic as a logical framework by representing logics of a varied nature, including higher-order logic, linear logic, the connection calculus, inference systems, sequent calculi, explicit substitutions, pure type systems, the open calculus of constructions, and others.

- [320] M. Casadei, A. Omicini, M. Viroli, **Prototyping A&A ReSpecT in Maude**, in: C. Canal, P. Poizat, M. Viroli (Eds.), *Proceedings of the 6th International Workshop on the Foundations of Coordination Languages and Software Architectures, FOCLASA 2007*, Lisbon, Portugal, September 8, 2007, volume 194 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2008, pp. 93–109.
- [321] J.M. Cleva, I. Pita, **Verification of CRWL programs with rewriting logic**, *Journal of Universal Computer Science* 12 (2006) 1594–1617.
- [322] G. Dowek, T. Hardin, C. Kirchner, **HOL- $\lambda\sigma$: An intentional first-order expression of higher-order logic**, *Mathematical Structures in Computer Science* 11 (2001) 21–45.
- [323] B. Holen, E.B. Johnsen, A. Waaler, **Proof search for the first-order connection calculus in Maude**, in: [1099], pp. 173–188.
- [324] O. Kahramanogullari, **Nondeterminism and Language Design in Deep Inference**, Ph.D. thesis, Technische Universität Dresden Informatik, Germany, 2007.
- [325] O. Kahramanogullari, **Maude as a platform for designing and implementing deep inference systems**, in: [1107], pp. 35–50.
- [326] N. Martí-Oliet, J. Meseguer, **General logics and logical frameworks**, in: D.M. Gabbay (Ed.), *What is a Logical System?*, volume 4 of *Studies in Logic and Computation*, Oxford University Press, 1994, pp. 355–392.
- [327] N. Martí-Oliet, J. Meseguer, **Rewriting logic as a logical and semantic framework**, in: [1086], pp. 190–225.
- [328] N. Martí-Oliet, J. Meseguer, **Action and change in rewriting logic**, in: R. Pareschi, B. Fronhöfer (Eds.), *Dynamic Worlds: From the Frame Problem to Knowledge Management*, volume 12 of *Applied Logic Series*, Kluwer Academic Publishers, 1999, pp. 1–53.
- [329] N. Martí-Oliet, J. Meseguer, **Rewriting logic as a logical and semantic framework**, in: D.M. Gabbay, F. Guentner (Eds.), *Handbook of Philosophical Logic*, Second Edition, Volume 9, Kluwer Academic Publishers, 2002, pp. 1–87.
- [330] J. Meseguer, C.L. Talcott, **Mapping OMRS to rewriting logic**, in: [1071], pp. 33–54.
- [331] C. Schürmann, M.O. Stehr, **An executable formalization of the HOL/Nuprl connection in the metalogical framework Twelf**, in: M. Hermann, A. Voronkov (Eds.), *Logic for Programming, Artificial Intelligence, and Reasoning*, 13th International Conference, LPAR 2006, Phnom Penh, Cambodia, November 13–17, 2006, Proceedings, volume 4246 of *Lecture Notes in Computer Science*, Springer, 2006, pp. 150–166.
- [332] M.O. Stehr, **CINNI – A generic calculus of explicit substitutions and its application to λ -, ζ - and π -calculi**, in: [1052], pp. 70–92.
- [333] M.O. Stehr, **Programming, Specification, and Interactive Theorem Proving – Towards a Unified Language based on Equational Logic, Rewriting Logic, and Type Theory**, Ph.D. thesis, Fachbereich Informatik, Universität Hamburg, 2002.
- [334] M.O. Stehr, **The open calculus of constructions (part I): An equational type theory with dependent types for programming, specification, and interactive theorem proving**, *Fundamenta Informaticae* 68 (2005) 131–174.
- [335] M.O. Stehr, **The open calculus of constructions (part II): An equational type theory with dependent types for programming, specification, and interactive theorem proving**, *Fundamenta Informaticae* 68 (2005) 249–288.
- [336] M.O. Stehr, J. Meseguer, **Pure type systems in rewriting logic**, in: A. Felty (Ed.), *Proceedings of the Workshop on Logical Frameworks and Meta-languages, LFM'99*, Paris, France, September 28, 1999.
- [337] M.O. Stehr, J. Meseguer, **Pure type systems in rewriting logic: Specifying typed higher-order languages in a first-order logical framework**, in: O. Owe, S. Krogdahl, T. Lyche (Eds.), *From Object-Oriented to Formal Methods, Essays in Memory of Ole-Johan Dahl*, volume 2635 of *Lecture Notes in Computer Science*, Springer, 2004, pp. 334–375.
- [338] M.O. Stehr, P. Naumov, J. Meseguer, **A proof-theoretic approach to the HOL-Nuprl connection with applications to proof translation**, in: M. Cerioli, P.D. Mosses, G. Reggio (Eds.), *Preliminary Proceedings of the 15th International Workshop on Algebraic Development Techniques and General Workshop of the CoFI WG, WADT/CoFI'01*, Genova, Italy, April 1–3, 2001.
- [339] E. Tushkanova, A. Giorgetti, C. Ringeissen, O. Kouchnarenko, **A rule-based framework for building superposition-based decision procedures**, in: [1042]. To appear.
- [340] P. Viry, **Adventures in sequent calculus modulo equations**, in: [1071], pp. 21–32.

3.2. Representing models of concurrency

As the following papers show, in addition to the original papers by Meseguer, many researchers have devoted their attention to the representation in rewriting logic of many models of concurrent systems, including Petri nets of many kinds, actors, process calculi like Milner's CCS and π -calculus, LOTOS, mobile ambients, and membrane and P systems.

- [341] O. Agrigoroaiei, G. Ciobanu, *Rewriting logic specification of membrane systems with promoters and inhibitors*, in: [1099], pp. 5–22.
- [342] O. Andrei, G. Ciobanu, D. Lucanu, *A rewriting logic framework for operational semantics of membrane systems*, *Theoretical Computer Science* 373 (2007) 163–181.
- [343] O. Andrei, D. Lucanu, G. Ciobanu, *Operational semantics and rewriting logic in membrane computing*, in: [1088], pp. 57–78.
- [344] G. Carabetta, P. Degano, F. Gadducci, *CCS semantics via proved transition systems and rewriting logic*, in: [1071], pp. 369–387.
- [345] G. Ciobanu, D. Lucanu, *Communicating concurrent objects in hiddenCCS*, in: [1085], pp. 353–373.
- [346] P. Degano, F. Gadducci, C. Priami, *A causal semantics for CCS via rewriting logic*, *Theoretical Computer Science* 275 (2002) 259–282.
- [347] D. Lucanu, *Rewriting logic-based semantics of P systems and the maximal concurrency*, *International Journal of Foundations of Computer Science* 20 (2009) 395–410.
- [348] D. Lucanu, *Strategy-based rewrite semantics for membrane systems preserves maximal concurrency of evolution rule actions*, in: A. Middeldorp (Ed.), *Proceedings of the Eighth International Workshop on Reduction Strategies in Rewriting and Programming, WRS 2008, Castle of Hagenberg, Austria, July 14, 2008*, volume 237 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2009, pp. 107–125.
- [349] J. Meseguer, *Rewriting logic as a semantic framework for concurrency: a progress report*, in: U. Montanari, V. Sassone (Eds.), *CONCUR '96, Concurrency Theory, 7th International Conference, Pisa, Italy, August 26–29, 1996*, Proceedings, volume 1119 of *Lecture Notes in Computer Science*, Springer, 1996, pp. 331–372.
- [350] J. Meseguer, *A logical framework for distributed systems and communication protocols*, in: S. Budkowski, A.R. Cavalli, E. Najm (Eds.), *Formal Description Techniques and Protocol Specification, Testing and Verification, FORTE XI / PSTV XVIII'98, IFIP TC6 WG6.1 Joint International Conference on Formal Description Techniques for Distributed Systems and Communication Protocols (FORTE XI) and Protocol Specification, Testing and Verification (PSTV XVIII)*, Paris, France, November 3–6, 1998, Proceedings, volume 135 of *IFIP Conference Proceedings*, Kluwer, 1998, pp. 327–333.
- [351] F. Rosa-Velardo, *Coding mobile synchronizing Petri Nets into rewriting logic*, in: [1046], pp. 83–98.
- [352] F. Rosa-Velardo, *Multiset rewriting: A semantic framework for concurrency with name binding*, in: [1093], pp. 191–207.
- [353] F. Rosa-Velardo, C. Segura, A. Verdejo, *Typed mobile ambients in Maude*, in: [1034], pp. 135–161.
- [354] M.O. Stehr, *A rewriting semantics for algebraic nets*, in: C. Girault, R. Valk (Eds.), *Petri Nets for System Engineering – A Guide to Modeling, Verification, and Applications*, Springer, 2001, pp. 318–338.
- [355] M.O. Stehr, J. Meseguer, P.C. Ölveczky, *Representation and execution of Petri nets using rewriting logic as a uniform framework*, in: H. Ehrig, C. Ermel, J. Padberg (Eds.), *Proceedings of the Uniform Approaches to Graphical Process Specification Techniques Satellite Event of ETAPS 2001, UNIGRA 2001, Genova, Italy, March 31–April 1, 2001*, volume 44(4) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2001, pp. 140–162.
- [356] M.O. Stehr, J. Meseguer, P.C. Ölveczky, *Rewriting logic as a unifying framework for Petri nets*, in: H. Ehrig, G. Juhás, J. Padberg, G. Rozenberg (Eds.), *Unifying Petri Nets, Advances in Petri Nets*, volume 2128 of *Lecture Notes in Computer Science*, Springer, 2001, pp. 250–303.
- [357] C.L. Talcott, *An actor rewriting theory*, in: [1086], pp. 361–384.
- [358] C.L. Talcott, *Interaction semantics for components of distributed systems*, in: E. Najm, J.B. Stefani (Eds.), *Proceedings of the IFIP Conference on Formal Methods for Open Object-Based Distributed Systems, FMOODS'96, Paris, France, March 4–6, 1996*, Chapman & Hall, 1997, pp. 154–169.
- [359] C.L. Talcott, *Actor theories in rewriting logic*, *Theoretical Computer Science* 285 (2002) 441–485.
- [360] C.L. Talcott, *Coordination models based on a formal model of distributed object reflection*, in: L. Brim, I. Linden (Eds.), *Proceedings of the First International Workshop on Methods and Tools for Coordinating Concurrent, Distributed and Mobile Systems, MTCoord 2005, Namur, Belgium, April 23, 2005*, volume 150(1) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2006, pp. 143–157.
- [361] P. Thati, K. Sen, N. Martí-Oliet, *An executable specification of asynchronous pi-calculus semantics and may testing in Maude 2.0*, in: [1056], pp. 261–281.
- [362] P. Thati, C.L. Talcott, G. Agha, *Techniques for executing and reasoning about specification diagrams*, in: [1098], pp. 521–536.
- [363] A. Verdejo, *Building tools for LOTOS symbolic semantics in Maude*, in: D. Peled, M.Y. Vardi (Eds.), *Formal Techniques for Networked and Distributed Systems – FORTE 2002, 22nd IFIP WG 6.1 International Conference Houston, Texas, USA, November 11–14, 2002*, Proceedings, volume 2529 of *Lecture Notes in Computer Science*, Springer, 2002, pp. 292–307.
- [364] A. Verdejo, N. Martí-Oliet, *Executing and verifying CCS in Maude*, Technical Report 99-00, Departamento de Sistemas Informáticos y Programación, Universidad Complutense de Madrid, 2000.
- [365] A. Verdejo, N. Martí-Oliet, *Executing E-LOTOS processes in Maude*, in: H. Ehrig, M. Grosse-Rhode, F. Orejas (Eds.), *Integration of Specification Techniques with Applications in Engineering, INT 2000, Berlin, Germany, March 21–April 2, 2000*, Extended Abstracts, pp. 49–53. Technical report 2000/04, Technische Universität Berlin, March 2000.
- [366] A. Verdejo, N. Martí-Oliet, *Implementing CCS in Maude*, in: T. Bolognesi, D. Latella (Eds.), *Formal Techniques for Distributed System Development, FORTE/PSTV 2000, IFIP TC6 WG6.1 Joint International Conference on Formal Description Techniques for Distributed Systems and Communication Protocols (FORTE XIII) and Protocol Specification, Testing and Verification (PSTV XX)*, October 10–13, 2000, Pisa, Italy, Proceedings, volume 183 of *International Federation for Information Processing Conference Proceedings*, Kluwer, 2000, pp. 351–366.
- [367] A. Verdejo, N. Martí-Oliet, *Implementing CCS in Maude 2*, in: [1056], pp. 282–300.
- [368] A. Verdejo, N. Martí-Oliet, *Two case studies of semantics execution in Maude: CCS and LOTOS*, *Formal Methods in System Design* 27 (2005) 113–172.
- [369] Y. Yu, S. Ren, C.L. Talcott, *Coordinating asynchronous and open distributed systems under semiring-based timing constraints*, in: C. Canal, P. Poizat, M. Sirjani (Eds.), *Proceedings of the 7th International Workshop on the Foundations of Coordination Languages and Software Architectures, FOCLASA 2008, Reykjavik, Iceland, July 13, 2008*, volume 229(2) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2009, pp. 133–153.
- [370] N. Zeghib, K. Barkaoui, M. Bettaz, *Contextual ECATNets semantics in terms of conditional rewriting logic*, in: M.A. Langston (Ed.), *Proceedings of the 2006 IEEE/ACS International Conference on Computer Systems and Applications, AICCSA 2006, Dubai/Sharjah, UAE, March 8–11, 2006*, IEEE, 2006, pp. 936–943.

3.3. Representing modeling languages

Since rewriting logic provides a good semantic framework to represent many different models of computation, it has been used to design, define, relate and give semantics to languages of all kinds, including agent languages, active networks languages, hardware and software architecture description languages, UML sublanguages, and domain-specific modeling languages. The particular case of operational semantics for concurrent programming languages is considered in the next section.

- [371] N. Aoumeur, G. Saake, *Integrating and rapid-prototyping UML structural and behavioural diagrams using rewriting logic*, in: A.B. Pidduck, J. Mylopoulos, C.C. Woo, M.T. Özsu (Eds.), *Advanced Information Systems Engineering, 14th International Conference, CAiSE 2002, Toronto, Canada, May 27–31, 2002*, Proceedings, volume 2348 of *Lecture Notes in Computer Science*, Springer, 2002, pp. 296–310.

- [372] N. Boudiaf, F. Mokhati, M. Badri, **Supporting formal verification of DIMA multi-agents models: towards a framework based on Maude model checking**, International Journal of Software Engineering and Knowledge Engineering 18 (2008) 853–875.
- [373] N. Boudiaf, F. Mokhati, M. Badri, L. Badri, **Specifying DIMA multi-agents models using Maude**, in: M. Barley, N.K. Kasabov (Eds.), Intelligent Agents and Multi-Agent Systems, 7th Pacific Rim International Workshop on Multi-Agents, PRIMA 2004, Auckland, New Zealand, August 8–13, 2004, Revised Selected Papers, volume 3371 of *Lecture Notes in Computer Science*, Springer, 2005, pp. 29–42.
- [374] C. Braga, A. Sztajnberg, **Towards a rewriting semantics for a software architecture description language**, in: A. Cavalcanti, P. Machado (Eds.), Proceedings of the Brazilian Workshop on Formal Methods, Campina Grande, Brazil, October 12–14, 2003, volume 95 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2004, pp. 149–168.
- [375] C. Castro, **Binary CSP solving as an inference process**, in: Proceedings of the Eighth International Conference on Tools in Artificial Intelligence, ICTAI'96, Toulouse, France, November 16–19, 1996, pp. 462–463.
- [376] A. Ciampolini, E. Lamma, P. Mello, C. Stefanelli, **Distributed logic objects: A fragment of rewriting logic and its implementation**, in: [1086], pp. 110–125.
- [377] G. Denker, M. Gogolla, **Translating TROLL light concepts to Maude**, in: H. Ehrig (Ed.), Recent Trends in Data Type Specification, 9th Workshop on Specification of Abstract Data Types Joint with the 4th COMPASS Workshop, Caldes de Malavella, Spain, October 26–30, 1992, Selected Papers, volume 785 of *Lecture Notes in Computer Science*, Springer, 1994, pp. 173–187.
- [378] Y. Fu, Z. Dong, J. Ding, X. He, **Mapping software architecture specification to rewriting logic (short paper)**, in: H. Zhu (Ed.), Proceedings of the Eighth International Conference on Quality Software, QSIC 2008, Oxford, UK, August 12–13, 2008, IEEE Computer Society, 2008, pp. 376–381.
- [379] M. Katelman, J. Meseguer, **A rewriting semantics for ABEL with applications to hardware/software co-design and analysis**, in: [1039], pp. 47–60.
- [380] M. Leucker, T. Noll, **Rewriting logic as a framework for generic verification tools**, in: [1052], pp. 121–137.
- [381] P. Meredith, M. Katelman, J. Meseguer, G. Roşu, **A formal executable semantics of Verilog**, in: B. Jobstmann, L. Carloni (Eds.), Proceedings of the Eighth ACM/IEEE International Conference on Formal Methods and Models for Codesign, MEMOCODE 2010, Grenoble, France, July 26–28, 2010, IEEE Computer Society, 2010, pp. 179–188.
- [382] J. Meseguer, C.L. Talcott, **Using rewriting logic to interoperate architectural description languages (I and II)**, 1997. Lectures at the Santa Fe and Seattle DARPA-EDCS Workshops, March and July 1997.
- [383] F. Mokhati, B. Sahraroui, S. Bouzaher, M.T. Kimour, **A tool for specifying and validating agents' interaction protocols: From Agent UML to Maude**, Journal of Object Technology 9 (2010) 59–77.
- [384] E. Najm, J.B. Stefani, **A formal operational semantics for the ODP computational model with signals, explicit binding, and reactive objects**, 1994. Manuscript, ENST, Paris, France.
- [385] E. Najm, J.B. Stefani, **A formal semantics for the ODP computational model**, Computer Networks and ISDN Systems 27 (1995) 1305–1329.
- [386] E. Najm, J.B. Stefani, **Computational models for open distributed systems**, in: H. Bowman, J. Derrick (Eds.), Proceedings of the Second IFIP Conference on Formal Methods for Open Object-Based Distributed Systems, FMOODS'97, July 21–23, 1997, Canterbury, Kent, UK, Chapman & Hall, 1997, pp. 157–176.
- [387] M.B. van Riemsdijk, L. Astefanoaei, F.S. de Boer, **Using the Maude term rewriting language for agent development with formal foundations**, in: M. Dastani, K.V. Hindriks, J.J.C. Meyer (Eds.), Specification and Verification of Multi-agent Systems, Springer, 2010, pp. 255–287.
- [388] M.B. van Riemsdijk, F.S. de Boer, M. Dastani, J.J.C. Meyer, **Prototyping 3APL in the Maude term rewriting language**, in: K. Inoue, K. Satoh, F. Toni (Eds.), Computational Logic in Multi-Agent Systems, 7th International Workshop, CLIMA VII, Hakodate, Japan, May 8–9, 2006, Revised Selected and Invited Papers, volume 4371 of *Lecture Notes in Computer Science*, Springer, 2007, pp. 95–114.
- [389] V. Rusu, **Embedding domain-specific modelling languages in Maude specifications**, SIGSOFT Software Engineering Notes 36 (2011) 1–8.
- [390] V. Rusu, **Embedding domain-specific modeling languages into Maude specifications**, Software and Systems Modeling (2012). To appear.
- [391] M.O. Stehr, C.L. Talcott, **PLAN in Maude: Specifying an active network programming language**, in: [1056], pp. 240–260.
- [392] M.O. Stehr, C.L. Talcott, **Practical techniques for language design and prototyping**, in: J.L. Fiadeiro, U. Montanari, M. Wirsing (Eds.), Foundations of Global Computing, February 20–25, 2005, volume 05081 of *Dagstuhl Seminar Proceedings*, Internationales Begegnungs- und Forschungszentrum für Informatik (IBFI), Schloss Dagstuhl, Germany Internationales Begegnungs- und Forschungszentrum für Informatik (IBFI), Schloss Dagstuhl, Germany, 2006.
- [393] J. Troya, A. Vallecillo, **A rewriting logic semantics for ATL**, Journal of Object Technology 10 (2011) 5:1–29.
- [394] A. Verdejo, N. Martí-Oliet, T. Robles, J. Salvachúa, L. Llana, M. Bradley, **Transforming information in RDF to rewriting logic**, in: M. Steffen, G. Zavattaro (Eds.), Formal Methods for Open Object-Based Distributed Systems, 7th IFIP WG 6.1 International Conference, FMOODS 2005, Athens, Greece, June 15–17, 2005, Proceedings, volume 3535 of *Lecture Notes in Computer Science*, Springer, 2005, pp. 227–242.
- [395] M. Wirsing, F. Nickl, U. Lechner, **Concurrent object-oriented specification in SPECTRUM**, in: Y. Inagaki (Ed.), Proceedings of the Workshop on Algebraic and Object-Oriented Approaches to Software Science, Nagoya, Japan, pp. 39–70.
- [396] J. Wu, H. Miao, **A rewriting logic approach to OWL-S composite process formal specification**, in: R.C.H. Hsu, K.C. Li, B. Chapman (Eds.), Proceedings of the 3rd IEEE Asia-Pacific Services Computing Conference, APSCC 2008, Yilan, Taiwan, December 9–12, 2008, IEEE, 2008, pp. 343–348.

3.4. Rewriting logic semantics of programming languages

When a programming language is defined by means of a rewrite theory, if such a theory satisfies some requirements making it an executable specification, then the operational semantics of the programming language is obtained and programs in the language can be executed by means of rewriting. This methodology has been applied to many languages, as the following papers attest, but, more interesting than applying it to a given language, is to show that many techniques for defining operational semantics of concurrent programming languages can be mapped quite directly into rewriting logic. This has been shown for structural operational semantics (SOS) in its different variations (evaluation semantics or big step vs. computation semantics or small step), its extension to modular SOS, reduction semantics with evaluation contexts, continuation-based semantics, and the chemical abstract machine. A recent technique directly inspired by rewriting logic is the K framework, considered in the following section.

- [397] M. Alturki, **A Rewriting Logic Approach to the Semantics of Orc**, Master's thesis, Department of Computer Science, University of Illinois at Urbana-Champaign, 2005.
- [398] M. Alturki, J. Meseguer, **Real-time rewriting semantics of Orc**, in: [1079], pp. 131–142.
- [399] M. Alturki, J. Meseguer, **Reduction semantics and formal analysis of Orc programs**, in: [1026], pp. 25–41.
- [400] M. Alturki, J. Meseguer, **Dist-Orc: A rewriting-based distributed implementation of Orc with formal analysis**, in: [1092], pp. 26–45.
- [401] L. Astefanoaei, F.S. de Boer, M.B. van Riemsdijk, **Using rewrite strategies for testing BLP agents**, in: [1102], pp. 143–157.
- [402] C. Braga, **Rewriting Logic as a Semantic Framework for Modular Structural Operational Semantics**, Ph.D. thesis, Departamento de Informática, Pontificia Universidade Católica do Rio de Janeiro, Brazil, 2001.
- [403] C. Braga, E.H. Haeusler, J. Meseguer, P.D. Mosses, **Maude action tool: Using reflection to map action semantics to rewriting logic**, in: [1100], pp. 407–421.
- [404] C. Braga, E.H. Haeusler, J. Meseguer, P.D. Mosses, **Mapping Modular SOS to rewriting logic**, in: M. Leuschel (Ed.), Logic Based Program Synthesis and Transformation, 12th International Workshop, LOPSTR 2002, Madrid, Spain, September 17–20, 2002, Revised Selected Papers, volume 2664 of *Lecture Notes in Computer Science*, Springer, 2003, pp. 262–277.
- [405] C. Braga, J. Meseguer, **Modular rewriting semantics in practice**, in: [1085], pp. 393–416.

- [406] F. Chalub, An Implementation of Modular SOS in Maude, Master's thesis, Universidade Federal Fluminense, Niterói, RJ, Brazil, 2005.
- [407] F. Chalub, C. Braga, A modular rewriting semantics for CML, *Journal of Universal Computer Science* 10 (2004) 789–807.
- [408] F. Chalub, C. Braga, Maude MSOS tool, in: [1039], pp. 133–146.
- [409] F. Chen, M. Hills, G. Roşu, A Rewrite Logic Approach to Semantic Definition, Design and Analysis of Object-Oriented Languages, Technical Report UIUCDCS-R-2006-2702, Department of Computer Science, University of Illinois at Urbana-Champaign, 2006.
- [410] M. d'Amorim, G. Roşu, An equational specification for the Scheme language, *Journal of Universal Computer Science* 11 (2005) 1327–1348. Selected papers from the 9th Brazilian Symposium on Programming Languages, SBLP'05.
- [411] G. Dowek, C. Muñoz, C. Rocha, Rewriting Logic Semantics of a Plan Execution Language, Technical Memorandum NASA/TM-2009-215770, NASA, Langley Research Center, Hampton VA 23681-2199, USA, 2009.
- [412] G. Dowek, C. Muñoz, C. Rocha, Rewriting logic semantics of a plan execution language, in: B. Klin, P. Sobociński (Eds.), Proceedings of the Sixth Workshop on Structural Operational Semantics, SOS 2009, Bologna, Italy, August 31, 2009, volume 18 of *Electronic Proceedings in Theoretical Computer Science*, pp. 77–91.
- [413] M. Hidalgo-Herrero, A. Verdejo, Y. Ortega-Mallén, Looking for Eden through Maude and its strategies, in: [1080], pp. 13–23.
- [414] M. Hidalgo-Herrero, A. Verdejo, Y. Ortega-Mallén, Using Maude and its strategies for defining a framework for analyzing Eden semantics, in: [1017], pp. 119–137.
- [415] M. Hills, T.B. Aktemur, G. Roşu, An Executable Semantic Definition of the Beta Language using Rewriting Logic, Technical Report UIUCDCS-R-2005-2650, Department of Computer Science, University of Illinois at Urbana-Champaign, 2005.
- [416] H. Ishikawa, K. Futatsugi, T. Watanabe, An operational semantics of GAEA in CafeOBJ, in: [1053], pp. 213–227.
- [417] H. Ishikawa, J. Meseguer, T. Watanabe, K. Futatsugi, H. Nakashima, On the semantics of GAEA – An object-oriented specification of a concurrent reflective language in rewriting logic, in: Proceedings of IMSA'97, Information-Technology Promotion Agency, Japan, 1997, pp. 70–109.
- [418] E.B. Johnsen, O. Owe, E.W. Axelsen, A run-time environment for concurrent objects with asynchronous method calls, in: [1085], pp. 375–392.
- [419] M. Kulaš, C. Beierle, Defining Standard Prolog in rewriting logic, in: [1052], pp. 158–174.
- [420] J. Meseguer, C. Braga, Modular rewriting semantics of programming languages, in: [1098], pp. 364–378.
- [421] J. Meseguer, G. Roşu, Rewriting logic semantics: From language specifications to formal analysis tools, in: D. Basin, M. Rusinowitch (Eds.), Automated Reasoning – Second International Joint Conference, IJCAR 2004, Cork, Ireland, July 4–8, 2004, Proceedings, volume 3097 of *Lecture Notes in Computer Science*, Springer, 2004, pp. 1–44.
- [422] J. Meseguer, G. Roşu, The rewriting logic semantics project, in: [1088], pp. 27–56.
- [423] J. Meseguer, G. Roşu, The rewriting logic semantics project, *Theoretical Computer Science* 373 (2007) 213–237.
- [424] J. Meseguer, G. Roşu, Computational logical frameworks and generic program analysis technologies, in: B. Meyer, J. Woodcock (Eds.), Verified Software: Theories, Tools, Experiments, First IFIP TC 2/WG 2.3 Conference, VSTTE 2005, Zurich, Switzerland, October 10–13, 2005, Revised Selected Papers and Discussions, volume 4171 of *Lecture Notes in Computer Science*, Springer, 2008, pp. 256–267.
- [425] J. Meseguer, G. Roşu, The rewriting logic semantics project: A progress report, in: O. Owe, M. Steffen, J.A. Telle (Eds.), Fundamentals of Computation Theory – 18th International Symposium, FCT 2011, Oslo, Norway, August 22–25, 2011. Proceedings, volume 6914 of *Lecture Notes in Computer Science*, Springer, 2011, pp. 1–37.
- [426] P.D. Mosses, Semantics, modularity, and rewriting logic, in: [1071], pp. 404–421.
- [427] P.D. Mosses, Logical specification of operational semantics, in: J. Flum, M. Rodríguez-Artalejo (Eds.), Computer Science Logic, 13th International Workshop, CSL '99, 8th Annual Conference of the EACSL, Madrid, Spain, September 20–25, 1999, Proceedings, volume 1683 of *Lecture Notes in Computer Science*, Springer, 1999, pp. 32–49.
- [428] P.D. Mosses, Modular structural operational semantics, *Journal of Logic and Algebraic Programming* 60–61 (2004) 195–228.
- [429] M.R. Mousavi, M.A. Reniers, Prototyping SOS meta-theory in Maude, in: [1088], pp. 135–150.
- [430] M. Nakamura, M. Watanabe, K. Futatsugi, A behavioral specification of imperative programming languages, *IEICE Transactions on Information and Systems* 89-A (2006) 1558–1565.
- [431] T. Noll, A rewriting logic implementation of Erlang, in: P. Klint (Ed.), Proceedings of the First Workshop on Language Descriptions, Tools and Applications, LDTA'01, a Satellite Event of ETAPS 2001, Genova, Italy, April 7, 2001, volume 44(2) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2001, pp. 206–224.
- [432] A. Riesco, J. Rodríguez-Hortalá, A natural implementation of plural semantics in Maude, in: T. Ekman, J. Vinju (Eds.), Proceedings of the Ninth Workshop on Language Descriptions Tools and Applications, LDTA 2009, York, UK, March 27–28, 2009, volume 253(7) of *Electronic Notes in Computer Science*, Elsevier, 2010, pp. 165–175.
- [433] A. Riesco, J. Rodríguez-Hortalá, Programming with singular and plural non-deterministic functions, in: [1057], pp. 83–92.
- [434] C. Rocha, C. Muñoz, Simulation and verification of synchronous set relations in rewriting logic, in: A. Simao, C. Morgan (Eds.), Formal Methods, 14th Brazilian Symposium, SBMF 2011, São Paulo, Brazil, September 26–30, 2011, Proceedings, volume 7021 of *Lecture Notes in Computer Science*, Springer, 2011, pp. 60–75.
- [435] C. Rocha, C. Muñoz, H. Cadavid, A graphical environment for the semantic validation of a plan execution language, in: S. Grenander, L. Bergman (Eds.), Proceedings of the Third IEEE International Conference on Space Mission Challenges for Information Technology, SMC-IT 2009, Pasadena, California, USA, July 19–23, 2009, IEEE Computer Society, Los Alamitos, CA, USA, 2009, pp. 201–207.
- [436] C. Rocha, C. Muñoz, G. Dowek, A formal library of set relations and its application to synchronous languages, *Theoretical Computer Science* 412 (2011) 4853–4866.
- [437] R. Sasse, Taclets vs. Rewriting Logic – Relating Semantics of Java, Master's thesis, Fakultät für Informatik, Universität Karlsruhe, Germany, 2005. Technical Report in Computing Science No. 2005-16.
- [438] T.F. Şerbănuţă, G. Roşu, J. Meseguer, A rewriting logic approach to operational semantics (extended abstract), in: R. van Glabbeek, M. Hennessy (Eds.), Proceedings of the Fourth Workshop on Structural Operational Semantics, SOS 2007, Wrocław, Poland, July 9, 2007, volume 192(1) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2007, pp. 125–141.
- [439] T.F. Şerbănuţă, G. Roşu, J. Meseguer, A rewriting logic approach to operational semantics, *Information and Computation* 207 (2009) 305–340.
- [440] A. Verdejo, Maude como Marco Semántico Ejecutable, Ph.D. thesis, Facultad de Informática, Universidad Complutense de Madrid, Spain, 2003.
- [441] A. Verdejo, N. Martí-Oliet, Executable Structural Operational Semantics in Maude, Technical Report 134-03, Dpto. Sistemas Informáticos y Programación, Universidad Complutense de Madrid, 2003.
- [442] A. Verdejo, N. Martí-Oliet, Executable structural operational semantics in Maude, *Journal of Logic and Algebraic Programming* 67 (2006) 226–293.

3.5. K framework

K is a recent and ambitious new technique for defining operational semantics of concurrent programming languages which tries to combine the advantages of previous techniques (evaluation, computation, and reduction semantics, modular SOS, etc.) and at the same time to overcome their limitations such as the difficulties to deal with control, non-modularity, and the lack of support for true concurrency. Furthermore, it is also expected that the executable semantics provide prototype interpreters even for complex and big languages. The K framework is based on configurations, computations, and rules, and has been inspired by continuation-based semantics, the chemical abstract machine, and rewriting logic. The ongoing

work on K includes an implementation based on the rewriting logic language Maude, and the development of semantics for different real-world languages.

- [443] I.M. Asăvoae, M. Asăvoae, Collecting semantics under predicate abstraction in the K framework, in: [1093], pp. 123–139.
- [444] I.M. Asăvoae, M. Asăvoae, D. Lucanu, Path Directed Symbolic Execution in the K Framework, in: [1067], pp. 133–141.
- [445] C. Ellison, A Rewriting Logic Approach to Defining Type Systems, Master's thesis, Department of Computer Science, University of Illinois at Urbana-Champaign, 2008.
- [446] C. Ellison, G. Roşu, A Formal Semantics of C with Applications, Technical Report, Department of Computer Science, University of Illinois at Urbana-Champaign, 2010.
- [447] C. Ellison, G. Roşu, An executable formal semantics of C with applications, in: Proceedings of the 39th Symposium on Principles of Programming Languages, POPL 2012, Philadelphia, USA, January 25–27, 2012, ACM, 2012, pp. 533–544.
- [448] C. Ellison, T.F. Şerbănuţă, G. Roşu, A rewriting logic approach to type inference, in: [1037], pp. 135–151.
- [449] R. Frei, G.D.M. Serugendo, T.F. Şerbănuţă, Ambient intelligence in self-organising assembly systems using the chemical reaction model, Journal of Ambient Intelligence and Humanized Computing 1 (2010) 163–184.
- [450] M. Hills, Memory representations in rewriting logic semantics definitions, in: [1099], pp. 155–172.
- [451] M. Hills, A Modular Rewriting Approach to Language Design, Evolution and Analysis, Ph.D. thesis, Department of Computer Science, University of Illinois at Urbana-Champaign, 2009.
- [452] M. Hills, G. Roşu, KOOL: A K-based Object-Oriented Language, Technical Report UIUCDCS-R-2006-2779, Department of Computer Science, University of Illinois at Urbana-Champaign, 2006.
- [453] M. Hills, G. Roşu, A Rewriting Based Approach to OO Language Prototyping and Design, Technical Report UIUCDCS-R-2006-2786, Department of Computer Science, University of Illinois at Urbana-Champaign, 2006.
- [454] M. Hills, G. Roşu, KOOL: An application of rewriting logic to language prototyping and analysis, in: [1023], pp. 246–256.
- [455] M. Hills, G. Roşu, On formal analysis of OO languages using rewriting logic: Designing for performance, in: [1029], pp. 107–121.
- [456] M. Hills, G. Roşu, A rewriting approach to the design and evolution of object-oriented languages, in: R.P. Gabriel, D.F. Bacon, C.V. Lopes, G.L.S. Jr. (Eds.), Companion to the 22nd Annual ACM SIGPLAN Conference on Object-Oriented Programming, Systems, Languages, and Applications, OOPSLA 2007, Montreal, Quebec, Canada, October 21–25, 2007, ACM, 2007, pp. 827–828.
- [457] M. Hills, G. Roşu, Towards a module system for K, in: [1037], pp. 187–205.
- [458] M. Hills, G. Roşu, A rewriting logic semantics approach to modular program analysis, in: [1084], pp. 151–160.
- [459] M. Hills, T.F. Şerbănuţă, G. Roşu, A rewrite framework for language definitions and for generation of efficient interpreters, in: [1039], pp. 215–231.
- [460] P. Meredith, M. Hills, G. Roşu, An executable rewriting logic semantics of K-Scheme, in: D. Dube (Ed.), Proceedings of the 2007 Workshop on Scheme and Functional Programming, SCHEME 2007, Freiburg, Germany, September 30, 2007, Laval University, 2007, pp. 91–103.
- [461] P. Meredith, M. Hills, G. Roşu, A K Definition of Scheme, Technical Report UIUCDCS-R-2007-2907, Department of Computer Science, University of Illinois at Urbana-Champaign, 2007.
- [462] G. Roşu, C. Ellison, W. Schulte, From Rewriting Logic Executable Semantics to Matching Logic Program Verification, Technical Report, Department of Computer Science, University of Illinois at Urbana-Champaign, 2009.
- [463] G. Roşu, C. Ellison, W. Schulte, Matching logic: An alternative to Hoare/Floyd logic, in: [1068], pp. 142–162.
- [464] G. Roşu, W. Schulte, Matching Logic—Extended Report, Technical Report UIUCDCS-R-2009-3026, Department of Computer Science, University of Illinois at Urbana-Champaign, 2009.
- [465] G. Roşu, W. Schulte, T.F. Şerbănuţă, Runtime verification of C memory safety, in: S. Bensalem, D. Peled (Eds.), Runtime Verification, 9th International Workshop, RV 2009, Grenoble, France, June 26–28, 2009. Selected Papers, volume 5779 of *Lecture Notes in Computer Science*, Springer, 2009, pp. 132–151.
- [466] G. Roşu, T.F. Şerbănuţă, An overview of the K semantic framework, Journal of Logic and Algebraic Programming 79 (2010) 397–434.
- [467] G. Roşu, A. Ştefănescu, Matching logic: A new program verification approach, in: T. Ball, L. Zuck, N. Shankar (Eds.), Proceedings of the 2010 Workshop on Usable Verification, UV 2010, Redmond, Washington, November 15–16, 2010, Microsoft Research, 2010.
- [468] G. Roşu, A. Ştefănescu, Matching logic: a new program verification approach (new ideas and emerging results track), in: R.N. Taylor, H. Gall, N. Medvidovic (Eds.), Proceedings of the 33rd International Conference on Software Engineering, ICSE 2011, Waikiki, Honolulu, HI, USA, May 21–28, 2011, ACM, 2011, pp. 868–871.
- [469] G. Roşu, A. Ştefănescu, Matching Logic Rewriting: Unifying Operational and Axiomatic Semantics in a Practical and Generic Framework, Technical Report, Department of Computer Science, University of Illinois at Urbana-Champaign, 2011.
- [470] T.F. Şerbănuţă, A Rewriting Approach to Concurrent Programming Language Design and Semantics, Ph.D. thesis, Department of Computer Science, University of Illinois at Urbana-Champaign, 2010.
- [471] T.F. Şerbănuţă, G. Roşu, K-Maude: A rewriting based tool for semantics of programming languages, in: [1093], pp. 104–122.
- [472] T.F. Şerbănuţă, G. Roşu, KRAM—Extended Report, Technical Report, Department of Computer Science, University of Illinois at Urbana-Champaign, 2010.
- [473] T.F. Şerbănuţă, G. Ştefănescu, G. Roşu, Defining and executing P systems with structured data in K, in: D.W. Corne, P. Frisco, G. Paun, G. Rozenberg, A. Salomaa (Eds.), Membrane Computing – 9th International Workshop, WMC 2008, Edinburgh, UK, July 28–31, 2008, Revised Selected and Invited Papers, volume 5391 of *Lecture Notes in Computer Science*, Springer, 2009, pp. 374–393.

4. Rewriting logic languages

Although there are many rule-based languages, we have singled out those whose designs have been directly inspired by rewriting logic, supporting in this way specification and programming based on rewrite theories. From the mid-nineties on there have been three main implementation efforts directed towards such languages: CafeOBJ in Japan, ELAN in France, and Maude in the USA. More recently, the developers of ELAN have started another project called Tom.

4.1. CafeOBJ

CafeOBJ is developed at the Japan Advanced Institute of Science and Technology (JAIST) as an extension of the order-sorted equational language OBJ. The extension goes in several directions, according to the so-called “CafeOBJ cube”: rewriting rules, hidden-sorted behavioral specifications, and object-oriented specifications. In particular, the behavioral approach has given rise to a methodology based on observational transition systems and proof scores, which is well suited for the specification of network protocols and distributed systems.

[474] R. Diaconescu, K. Futatsugi, CafeOBJ Report. The Language, Proof Techniques, and Methodologies for Object-Oriented Algebraic Specification, volume 6 of *AMAST Series in Computing*, World Scientific, 1998.

[475] R. Diaconescu, K. Futatsugi, Logical foundations of CafeOBJ, Theoretical Computer Science 285 (2002) 289–318.

- [476] R. Diaconescu, K. Futatsugi, S. Iida, **Component-based algebraic specification and verification in CafeOBJ**, in: [1109], pp. 1644–1663.
- [477] R. Diaconescu, K. Futatsugi, S. Iida, **CafeOBJ jewels**, in: [1055], pp. 33–60.
- [478] R. Diaconescu, K. Futatsugi, M. Ishihone, A.T. Nakagawa, T. Sawada, **An overview of CafeOBJ**, in: [1071], pp. 285–298.
- [479] R. Diaconescu, K. Futatsugi, K. Ogata, **CafeOBJ: Logical foundations and methodologies**, Computers and Artificial Intelligence 22 (2003).
- [480] K. Futatsugi, **Formal methods in CafeOBJ**, in: Z. Hu, M. Rodríguez-Artalejo (Eds.), *Functional and Logic Programming*, 6th International Symposium, FLOPS 2002, Aizu, Japan, September 15–17, 2002, Proceedings, volume 2441 of *Lecture Notes in Computer Science*, Springer, 2002, pp. 1–20.
- [481] K. Futatsugi, A.T. Nakagawa, **An overview of CAFE specification environment – An algebraic approach for creating, verifying, and maintaining formal specifications over networks**, in: Proceedings of the First International Conference on Formal Engineering Methods, pp. 170–181.
- [482] K. Futatsugi, T. Sawada, **Cafe as an extensible specification environment**, in: Proceedings of the Kunming International CASE Symposium, Kunming, China.
- [483] M. Ishihone, T. Sawada, **Brute: Brute force rewriting engine**, in: [1051].
- [484] A. Knapp, **Case studies with CafeOBJ**, in: [1051].
- [485] T. Sawada, K. Kishida, K. Futatsugi, **Past, present, and future of SRA implementation of CafeOBJ: Annex**, in: [1019], pp. 7–17.
- [486] J. Senachak, T. Seino, K. Ogata, K. Futatsugi, **Provably correct translation from CafeOBJ into Java**, in: [1033], pp. 614–619.

4.2. ELAN

ELAN was developed at LORIA, France, with a special emphasis on using strategies to guide rewriting. Together with the implementation of an interpreter and a compiler supporting new techniques for associative-commutative rewriting, the ELAN team and their collaborators developed the notion of computational system, consisting of a rewrite theory and a collection of strategies, and used it to represent many case studies in logic programming, constraint solving, higher-order unification, and equational theorem-proving, thus greatly contributing to the idea of rewriting logic as a logical and semantic framework. The techniques developed in this context have been more recently transferred to the new language Tom, discussed in the next section.

- [487] P. Borovanský, C. Castro, **Cooperation of constraint solvers: Using the new process control facilities of ELAN**, in: [1071], pp. 1–20.
- [488] P. Borovanský, H. Cirstea, H. Dubois, C. Kirchner, H. Kirchner, P.E. Moreau, Q.H. Nguyen, C. Ringeissen, M. Vittek, **ELAN V 3.6 User Manual**, Technical Report, INRIA Lorraine & LORIA, Nancy, France, 2004.
- [489] P. Borovanský, S. Jamoussi, P.E. Moreau, C. Ringeissen, **Handling ELAN rewrite programs via an exchange format**, in: [1071], p. 173.
- [490] P. Borovanský, C. Kirchner, H. Kirchner, **Strategies and rewriting in ELAN**, in: [1061], pp. 13–24.
- [491] P. Borovanský, C. Kirchner, H. Kirchner, **A functional view of rewriting and strategies for a semantics of ELAN**, in: M. Sato, Y. Toyama (Eds.), *Proceedings of the Third Fuji International Symposium on Functional and Logic Programming*, Kyoto, Japan, April 2–4, 1998, World Scientific, 1998, pp. 143–167.
- [492] P. Borovanský, C. Kirchner, H. Kirchner, **Rewriting as a unified specification tool for logic and control: The ELAN language**, in: [1103].
- [493] P. Borovanský, C. Kirchner, H. Kirchner, P.E. Moreau, **ELAN from a rewriting logic point of view**, *Theoretical Computer Science* 285 (2002) 155–185.
- [494] P. Borovanský, C. Kirchner, H. Kirchner, P.E. Moreau, C. Ringeissen, **An overview of ELAN**, in: [1071], pp. 55–70.
- [495] P. Borovanský, C. Kirchner, H. Kirchner, P.E. Moreau, M. Vittek, **ELAN: A logical framework based on computational systems**, in: [1086], pp. 35–50.
- [496] P. Borovanský, C. Kirchner, H. Kirchner, C. Ringeissen, **Rewriting with strategies in ELAN: A functional semantics**, *International Journal of Foundations of Computer Science* 12 (2001) 69–95.
- [497] M. van den Brand, P.E. Moreau, C. Ringeissen, **The ELAN environment: a rewriting logic environment based on ASF+SDF technology – system demonstration**, in: M. van den Brand, R. Lammel (Eds.), *Proceedings of the Second Workshop on Language Descriptions, Tools and Applications*, Satellite Event of ETAPS 2002, LDTA 2002, Grenoble, France, April 13, 2002, volume 65(3) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2002, pp. 50–56.
- [498] M. van den Brand, C. Ringeissen, **ASF+SDF parsing tools applied to ELAN**, in: [1052], pp. 138–157.
- [499] E. Deplagne, **Sequent calculus viewed modulo**, in: C. Pilière (Ed.), *Proceedings of the Fifth ESSLI Student Session*, University of Birmingham, 2000, pp. 66–76.
- [500] E. Deplagne, C. Kirchner, H. Kirchner, Q.H. Nguyen, **Proof search and proof check for equational and inductive theorems**, in: [1022], pp. 297–316.
- [501] H. Dubois, H. Kirchner, **Actions and plans in ELAN**, in: [1063], pp. 35–45. Also Technical Report LORIA 98-R-275.
- [502] H. Dubois, H. Kirchner, **Modelling Planning Problems with Rules and Strategies**, Technical Report 99-R-029, LORIA, Nancy, France, 1999. Poster Session at JFPLC'99, Lyon, France, June 1999.
- [503] H. Dubois, H. Kirchner, **Objects, constraints, rules, and strategies in ELAN**, in: A. Nijholt (Ed.), *Proceedings of the Second AMAST Workshop on Algebraic Methods in Language Processing*, AMILP 2000, Iowa City, Iowa, USA, May 20–22, 2000.
- [504] H. Dubois, H. Kirchner, **Rule based programming with constraints and strategies**, in: K.R. Apt, A.C. Kakas, E. Monfroy, F. Rossi (Eds.), *New Trends in Constraints*, Joint ERCIM/Compulog Net Workshop, Paphos, Cyprus, October 25–27, 1999, Selected Papers, volume 1865 of *Lecture Notes in Computer Science*, Springer, 2000, pp. 274–297.
- [505] C. Kirchner, H. Kirchner, **Rule-based programming and proving: The ELAN experience outcomes**, in: M.J. Maher (Ed.), *Advances in Computer Science – ASIAN 2004, Higher-Level Decision Making*, 9th Asian Computing Science Conference, Dedicated to Jean-Louis Lassez on the Occasion of His 5th Cycle Birthday, Chiang Mai, Thailand, December 8–10, 2004, Proceedings, volume 3321 of *Lecture Notes in Computer Science*, Springer, 2004, pp. 363–379.
- [506] C. Kirchner, H. Kirchner, M. Vittek, **Implementing computational systems with constraints**, in: P. Kanellakis, J.L. Lassez, V. Saraswat (Eds.), *Proceedings of the First Workshop on Principles and Practice of Constraint Programming*, pp. 156–165.
- [507] C. Kirchner, H. Kirchner, M. Vittek, **Designing constraint logic programming languages using computational systems**, in: V. Saraswat, P. van Hentenryck (Eds.), *Principles and Practice of Constraint Programming: The Newport Papers*, The MIT Press, 1995, pp. 133–160.
- [508] C. Kirchner, C. Ringeissen, **Rule-based constraint programming**, *Fundamenta Informaticae* 34 (1998) 225–262.
- [509] H. Kirchner, **ELAN**, in: F. Fages (Ed.), *JFPLC'99, Journées Francophones de Programmation Logique et Programmation par Contraintes*, HERMES Science Publications, Lyon, France, 1999, pp. 241–248. Also Technical Report 99-R-129, LORIA, Nancy, France.
- [510] H. Kirchner, P.E. Moreau, **Prototyping completion with constraints using computational systems**, in: [1066], pp. 438–443.
- [511] H. Kirchner, P.E. Moreau, **Non-deterministic computations in ELAN**, in: [1047], pp. 168–182.
- [512] H. Kirchner, P.E. Moreau, **Promoting rewriting to a programming language: a compiler for non-deterministic rewrite programs in associative-commutative theories**, *Journal of Functional Programming* 11 (2001) 207–251.
- [513] H. Kirchner, C. Ringeissen, **Executing CASL Equational Specifications with the ELAN Rewrite Engine**, Technical Report 99-R-278, LORIA, Nancy, France, 1999.
- [514] P.E. Moreau, **A choice-point library for backtrack programming**, in: Proceedings of the Post-Conference Workshop on Implementation Technologies for Programming Languages based on Logic, JICSLP'98, Manchester, UK, June 15–19, 1998, pp. 16–31.
- [515] P.E. Moreau, **Compilation de Règles de Réécriture et de Stratégies Non-Déterministes**, Ph.D. thesis, Université Henri Poincaré – Nancy I, 1999.
- [516] P.E. Moreau, H. Kirchner, **Compilation of Associative-Commutative Normalisation with Strategies in ELAN (Full Version)**, Technical Report 97-R-129, LORIA, Nancy, France, 1997.
- [517] P.E. Moreau, H. Kirchner, **Compilation techniques for associative-commutative normalisation**, in: [1103].

- [518] P.E. Moreau, H. Kirchner, **A compiler for rewrite programs in associative-commutative theories**, in: C. Palamidessi, H. Glaser, K. Meinke (Eds.), Principles of Declarative Programming, 10th International Symposium, PLILP'98 Held Jointly with the 7th International Conference, ALP'98, Pisa, Italy, September 16–18, 1998, Proceedings, volume 1490 of *Lecture Notes in Computer Science*, Springer, 1998, pp. 230–249.
- [519] A.M. Moreira, C. Ringeissen, A. Santana, **A tool support for reusing ELAN rule-based components**, in: [1058], pp. 77–91.
- [520] Q.H. Nguyen, **Certifying term rewriting proofs in ELAN**, in: [1030], pp. 320–340.
- [521] Q.H. Nguyen, C. Kirchner, H. Kirchner, **External rewriting for skeptical proof assistants**, *Journal of Automated Reasoning* 29 (2002) 309–336.
- [522] C. Ringeissen, **Prototyping combination of unification algorithms with the ELAN rule-based programming language**, in: [1035], pp. 323–326.
- [523] C. Ringeissen, **Handling relations over finite domains in the rule-based system ELAN**, in: [1052], pp. 194–211.
- [524] C. Scharff, **Déduction avec Contraintes et Simplification dans les Théories Équationnelles**, Ph.D. thesis, Université Henri Poincaré – Nancy I, 1999.
- [525] L.J. Steggle, **Rewriting logic and Elan: Prototyping tools for Petri nets with time**, in: J.M. Colom, M. Koutny (Eds.), Application and Theory of Petri Nets 2001, 22nd International Conference, ICATPN 2001, Newcastle upon Tyne, UK, June 25–29, 2001, Proceedings, volume 2075 of *Lecture Notes in Computer Science*, Springer, 2001, pp. 363–381.
- [526] P. Viry, **Input/output for ELAN**, in: [1086], pp. 51–64.
- [527] M. Vittek, **ELAN: Un Cadre Logique pour le Prototypage de Langages de Programmation avec Contraintes**, Ph.D. thesis, Université Henri Poincaré – Nancy I, 1994.
- [528] M. Vittek, **A compiler for nondeterministic term rewriting systems**, in: H. Ganzinger (Ed.), Rewriting Techniques and Applications, 7th International Conference, RTA-96, New Brunswick, NJ, USA, July 27–30, 1996, Proceedings, volume 1103 of *Lecture Notes in Computer Science*, Springer, 1996, pp. 154–167.

4.3. Tom

The Tom language is an extension of Java with capabilities based on matching and rewriting, thus sitting in an intermediate level between lower-level programming and higher-level specification. These rewriting capabilities are inherited from the ELAN language, commented in the previous section, and include support for strategies to guide non-deterministic computations and pattern matching modulo associativity and commutativity; a newer addition is anti-pattern matching, which is like a negative version of the more usual pattern matching.

- [529] E. Balland, P. Brauner, R. Kopetz, P.E. Moreau, A. Reilles, **Tom: Piggybacking rewriting on Java**, in: [1023], pp. 36–47.
- [530] E. Balland, P.E. Moreau, A. Reilles, **Bytecode rewriting in Tom**, in: M. Huisman, F. Spoto (Eds.), Proceedings of the Second Workshop on Bytecode Semantics, Verification, Analysis and Transformation, Bytecode 2007, Braga, Portugal, March 31, 2007, volume 190(1) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2007, pp. 19–33.
- [531] E. Balland, P.E. Moreau, A. Reilles, **Rewriting strategies in Java**, in: [1107], pp. 97–111.
- [532] H. Cirstea, C. Kirchner, R. Kopetz, P.E. Moreau, **Anti-patterns for rule-based languages**, *Journal of Symbolic Computation* 45 (2010) 523–550.
- [533] H. Cirstea, P.E. Moreau, A.S. de Oliveira, **Rewrite based specification of access control policies**, in: [1041], pp. 37–54.
- [534] H. Cirstea, P.E. Moreau, A. Reilles, **Rule-based programming in Java for protocol verification**, in: [1085], pp. 209–227.
- [535] H. Cirstea, P.E. Moreau, A. Reilles, **TomML: A rule language for structured data**, in: G. Governatori, J. Hall, A. Paschke (Eds.), Rule Interchange and Applications, International Symposium, RuleML 2009, Las Vegas, Nevada, USA, November 5–7, 2009. Proceedings, volume 5858 of *Lecture Notes in Computer Science*, Springer, 2009, pp. 262–271.
- [536] C. Kirchner, R. Kopetz, P.E. Moreau, **Anti-pattern matching**, in: R. De Nicola (Ed.), Programming Languages and Systems, 16th European Symposium on Programming, ESOP 2007, Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2007, Braga, Portugal, March 24–April 1, 2007, Proceedings, volume 4421 of *Lecture Notes in Computer Science*, Springer, 2007, pp. 110–124.
- [537] C. Kirchner, R. Kopetz, P.E. Moreau, **Anti-pattern matching modulo**, in: C. Martín-Vide, F. Otto, H. Fernau (Eds.), Language and Automata Theory and Applications, Second International Conference, LATA 2008, Tarragona, Spain, March 13–19, 2008. Revised Papers, volume 5196 of *Lecture Notes in Computer Science*, Springer, 2008, pp. 275–286.
- [538] C. Kirchner, P.E. Moreau, C. Tavares, **A type system for Tom**, in: I. Mackie, A.M. Moreira (Eds.), Proceedings of the Tenth International Workshop on Rule-Based Programming, RULE 2009, Brasília, Brazil, June 28, 2009, volume 21 of *Electronic Proceedings in Theoretical Computer Science*, Computing Research Repository (CoRR), 2010, pp. 51–64.
- [539] P.E. Moreau, A. Reilles, **Rules and strategies in Java**, in: [1060], pp. 71–82.
- [540] P.E. Moreau, C. Ringeissen, M. Vittek, **A pattern matching compiler for multiple target languages**, in: G. Hedin (Ed.), Compiler Construction, 12th International Conference, CC 2003, Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2003, Warsaw, Poland, April 7–11, 2003, Proceedings, volume 2622 of *Lecture Notes in Computer Science*, Springer, 2003, pp. 61–76.
- [541] A. Reilles, **Canonical abstract syntax trees**, in: [1039], pp. 165–179.

4.4. Maude

The Maude language is developed at SRI International and the University of Illinois at Urbana-Champaign, USA, together with some collaborators in Spain. Maude supports functional modules, which are executable membership equational logic theories; system modules, which are executable rewrite theories; and object-oriented modules. Moreover, both equations and rules can be conditional, and rewriting can be modulo axioms such as associativity, commutativity, and/or identity. Maude also supports quite efficiently the reflective properties of rewriting logic discussed in Section 2.6, thus providing powerful metaprogramming features. In addition, the Maude system includes analysis capabilities based on breadth-first search and LTL model-checking. Some recently added features include order-sorted unification modulo axioms and narrowing.

- [542] M. Clavel, F. Durán, S. Eker, S. Escobar, P. Lincoln, N. Martí-Oliet, J. Meseguer, C.L. Talcott, **Unification and narrowing in Maude 2.4**, in: R. Treinen (Ed.), Rewriting Techniques and Applications, 20th International Conference, RTA 2009, Brasília, Brazil, June 29–July 1, 2009, Proceedings, volume 5595 of *Lecture Notes in Computer Science*, Springer, 2009, pp. 380–390.
- [543] M. Clavel, F. Durán, S. Eker, P. Lincoln, N. Martí-Oliet, J. Meseguer, J.F. Quesada, **Maude: specification and programming in rewriting logic**, 1999. Manual distributed as documentation of the Maude system, Computer Science Laboratory, SRI International.
- [544] M. Clavel, F. Durán, S. Eker, P. Lincoln, N. Martí-Oliet, J. Meseguer, J.F. Quesada, **The Maude system**, in: P. Narendran, M. Rusinowitch (Eds.), Rewriting Techniques and Applications, 10th International Conference, RTA-99, Trento, Italy, July 2–4, 1999, Proceedings, volume 1631 of *Lecture Notes in Computer Science*, Springer, 1999, pp. 240–243.
- [545] M. Clavel, F. Durán, S. Eker, P. Lincoln, N. Martí-Oliet, J. Meseguer, J.F. Quesada, **A Maude tutorial**, 2000. Tutorial distributed as documentation of the Maude system, Computer Science Laboratory, SRI International. Presented at the *European Joint Conference on Theory and Practice of Software, ETAPS 2000*, Berlin, Germany, March 25, 2000.

- [546] M. Clavel, F. Durán, S. Eker, P. Lincoln, N. Martí-Oliet, J. Meseguer, J.F. Quesada, **Towards Maude 2.0**, in: [1052], pp. 294–315.
- [547] M. Clavel, F. Durán, S. Eker, P. Lincoln, N. Martí-Oliet, J. Meseguer, J.F. Quesada, **Using Maude**, in: T.S.E. Maibaum (Ed.), *Fundamental Approaches to Software Engineering, Third International Conference, FASE 2000, Held as Part of the European Joint Conferences on the Theory and Practice of Software, ETAPS 2000, Berlin, Germany, March 25–April 2, 2000, Proceedings*, volume 1783 of *Lecture Notes in Computer Science*, Springer, 2000, pp. 371–374.
- [548] M. Clavel, F. Durán, S. Eker, P. Lincoln, N. Martí-Oliet, J. Meseguer, J.F. Quesada, **Maude: specification and programming in rewriting logic**, *Theoretical Computer Science* 285 (2002) 187–243.
- [549] M. Clavel, F. Durán, S. Eker, P. Lincoln, N. Martí-Oliet, J. Meseguer, C. Talcott, **Maude Manual (Version 2.6)**, 2011.
- [550] M. Clavel, F. Durán, S. Eker, P. Lincoln, N. Martí-Oliet, J. Meseguer, C.L. Talcott, **The Maude 2.0 system**, in: [1090], pp. 76–87.
- [551] M. Clavel, F. Durán, S. Eker, P. Lincoln, N. Martí-Oliet, J. Meseguer, C.L. Talcott, **All About Maude – A High-Performance Logical Framework, How to Specify, Program and Verify Systems in Rewriting Logic**, volume 4350 of *Lecture Notes in Computer Science*, Springer, 2007.
- [552] M. Clavel, F. Durán, J. Hendrix, S. Lucas, J. Meseguer, P.C. Ölveczky, **The Maude formal tool environment**, in: [1087], pp. 173–178.
- [553] M. Clavel, F. Durán, N. Martí-Oliet, **Polytypic programming in Maude**, in: [1052], pp. 339–360.
- [554] M. Clavel, S. Eker, P. Lincoln, J. Meseguer, **Principles of Maude**, in: [1086], pp. 65–89.
- [555] F. Durán, **A Reflective Module Algebra with Applications to the Maude Language**, Ph.D. thesis, Universidad de Málaga, Spain, 1999.
- [556] F. Durán, **The extensibility of Maude's module algebra**, in: [1100], pp. 422–437.
- [557] F. Durán, **Maude's internal strategies**, in: [1018], pp. 11–15.
- [558] F. Durán, S. Eker, S. Escobar, J. Meseguer, C.L. Talcott, **Variants, unification, narrowing, and symbolic reachability in Maude 2.6**, in: M. Schmidt-Schauß (Ed.), *Proceedings of the 22nd International Conference on Rewriting Techniques and Applications, RTA 2011, Novi Sad, Serbia, May 30–June 1, 2011, volume 10 of Leibniz International Proceedings in Informatics (LIPIcs)*, Schloss Dagstuhl – Leibniz-Zentrum fuer Informatik, 2011, pp. 31–40.
- [559] F. Durán, J. Meseguer, **An extensible module algebra for Maude**, in: [1071], pp. 174–195.
- [560] F. Durán, J. Meseguer, **The Maude specification of Full Maude**, 1999. Manuscript, Computer Science Laboratory, SRI International.
- [561] F. Durán, J. Meseguer, **Parameterized theories and views in Full Maude 2.0**, in: [1052], pp. 316–338.
- [562] F. Durán, J. Meseguer, **Maude's module algebra**, *Science of Computer Programming* 66 (2007) 125–153.
- [563] S. Eker, **Associative-commutative rewriting on large terms**, in: [1090], pp. 14–29.
- [564] P. Lincoln, N. Martí-Oliet, J. Meseguer, **Specification, transformation, and programming of concurrent systems in rewriting logic**, in: G.E. Blelloch, K.M. Chandy, S. Jagannathan (Eds.), *Specification of Parallel Algorithms, DIMACS Workshop, May 9–11, 1994, volume 18 of DIMACS Series in Discrete Mathematics and Theoretical Computer Science*, American Mathematical Society, 1994, pp. 309–339.
- [565] P. Lincoln, N. Martí-Oliet, J. Meseguer, L. Ricciulli, **Compiling rewriting onto SIMD and MIMD/SIMD machines**, in: [1064], pp. 37–48.
- [566] N. Martí-Oliet, **An introduction to Maude and some of its applications**, in: M. Carro, R. Peña (Eds.), *Practical Aspects of Declarative Languages, 12th International Symposium, PADL 2010, Madrid, Spain, January 18–19, 2010. Proceedings*, volume 5937 of *Lecture Notes in Computer Science*, Springer, 2010, pp. 4–9.
- [567] N. Martí-Oliet, M. Palomino, A. Verdejo, **A tutorial on specifying data structures in Maude**, in: [1081], pp. 105–132.
- [568] J. Meseguer, T.C. Winkler, **Parallel programming in Maude**, in: J.P. Banâtre, D.L. Métayer (Eds.), *Research Directions in High-Level Parallel Programming Languages, Mont Saint-Michel, France, June 17–19, 1991, Proceedings*, volume 574 of *Lecture Notes in Computer Science*, Springer, 1992, pp. 253–293.
- [569] P.C. Ölveczky, **Teaching formal methods based on rewriting logic and Maude**, in: J. Gibbons, J.N. Oliveira (Eds.), *Teaching Formal Methods, Second International Conference, TFM 2009, Eindhoven, The Netherlands, November 2–6, 2009. Proceedings*, volume 5846 of *Lecture Notes in Computer Science*, Springer, 2009, pp. 20–38.
- [570] M. Palomino, N. Martí-Oliet, A. Verdejo, **Playing with Maude**, in: [1014], pp. 3–23.
- [571] T.C. Winkler, **Programming in OBJ and Maude**, in: P.E. Lauer (Ed.), *Functional Programming, Concurrency, Simulation and Automated Reasoning: International Lecture Series 1991–1992*, McMaster University, Hamilton, Ontario, Canada, volume 693 of *Lecture Notes in Computer Science*, Springer, 1993, pp. 229–277.

5. Tools

This section collects papers describing tools mostly written in Maude. We distinguish between Maude tools whose goal is to reason about Maude modules themselves, and other tools designed with the objective of using rewriting logic-based methods to specify and analyze systems in several application domains, supporting the corresponding domain-specific notations.

5.1. Maude tools

As discussed in Section 2.3, in order to become executable, rewrite theories must satisfy some requirements, such as coherence between rules and equations, for example. Moreover, the user might also be interested in proving properties satisfied by the specified system; as seen in Section 2.8, modal and temporal logics are suitable for this. The tools described in the following papers provide support for the verification of properties of these two kinds, including a Church-Rosser checker, a coherence checker, a sufficient completeness checker, a termination tool, a declarative debugger, an inductive theorem prover, and model checkers for LTL and fragments of TLR. The Maude Formal Environment is a recent effort to integrate and interoperate most of these tools. There are a couple of tools which could be included here, but we have decided instead to consider them in their respective application areas, namely, Real-Time Maude for the specification and analysis of real-time systems in Section 6.2, and PMAude for probabilistic systems in Section 6.6.

- [572] K. Bae, J. Meseguer, **A rewriting-based model checker for the temporal logic of rewriting**, in: [1073], pp. 46–60.
- [573] K. Bae, J. Meseguer, **The linear temporal logic of rewriting Maude model checker**, in: [1093], pp. 208–225.
- [574] K. Bae, J. Meseguer, **Model checking LTLR formulas under localized fairness**, in: [1042]. To appear.
- [575] R. Caballero, N. Martí-Oliet, A. Riesco, A. Verdejo, **Declarative debugging of Maude functional modules**, Technical Report SIC-4/07, Dpto. Sistemas Informáticos y Computación, Universidad Complutense de Madrid, 2007.
- [576] R. Caballero, N. Martí-Oliet, A. Riesco, A. Verdejo, **Declarative debugging of membership equational logic specifications**, in: [1038], pp. 174–193.
- [577] R. Caballero, N. Martí-Oliet, A. Riesco, A. Verdejo, **A declarative debugger for Maude functional modules**, in: [1099], pp. 63–81.
- [578] M. Clavel, F. Durán, S. Eker, J. Meseguer, **Design and Implementation of the Cafe Prover and the Church-Rosser Checker Tools**, Technical Report, Computer Science Laboratory, SRI International, 1998.
- [579] M. Clavel, M. Palomino, A. Riesco, **Introducing the ITP tool: a tutorial**, *Journal of Universal Computer Science* 12 (2006) 1618–1650.
- [580] M. Clavel, M. Palomino, J. Santa-Cruz, **Integrating decision procedures in reflective rewriting-based theorem provers**, in: S. Antoy, Y. Toyama (Eds.), *Preliminary Proceedings of the Fourth International Workshop on Reduction Strategies in Rewriting and Programming, WRS 2004, Aachen, Germany, June 2, 2004*, pp. 15–24. Technical report AIB-2004-06, Department of Computer Science, RWTH, Aachen.

- [581] M. Clavel, J. Santa-Cruz, ASIP + ITP: A verification tool based on algebraic semantics, in: [1080], pp. 149–158.
- [582] F. Durán, Coherence checker and completion tools for Maude specifications, 2000. Manuscript, Computer Science Laboratory, SRI International.
- [583] F. Durán, Termination checker and Knuth-Bendix completion tools for Maude equational specifications, 2000. Manuscript, Computer Science Laboratory, SRI International.
- [584] F. Durán, S. Escobar, S. Lucas, New evaluation commands for Maude within Full Maude, in: [1085], pp. 263–284.
- [585] F. Durán, S. Escobar, S. Lucas, Towards (constructor) normal forms for Maude within Full Maude, in: [1082], pp. 125–136.
- [586] F. Durán, S. Lucas, J. Meseguer, MTT: The Maude termination tool (system description), in: A. Armando, P. Baumgartner, G. Dowek (Eds.), Automated Reasoning, 4th International Joint Conference, IJCAR 2008, Sydney, Australia, August 12–15, 2008, Proceedings, volume 5195 of *Lecture Notes in Computer Science*, Springer, 2008, pp. 313–319.
- [587] F. Durán, S. Lucas, J. Meseguer, F. Gutiérrez, Web services and interoperability for the Maude termination tool, in: [1016], pp. 83–92.
- [588] F. Durán, J. Meseguer, A Church-Rosser checker tool for Maude equational specifications, 2000. Manuscript, Computer Science Laboratory, SRI International.
- [589] F. Durán, J. Meseguer, A Church-Rosser checker tool for conditional order-sorted equational Maude specifications, in: [1093], pp. 69–85.
- [590] F. Durán, J. Meseguer, A Maude coherence checker tool for conditional order-sorted rewrite theories, in: [1093], pp. 86–103.
- [591] F. Durán, J. Meseguer, On the Church-Rosser and coherence properties of conditional order-sorted rewrite theories, *Journal of Logic and Algebraic Programming* (2012). This volume.
- [592] F. Durán, P.C. Ölveczky, A guide to extending Full Maude illustrated with the implementation of Real-Time Maude, in: [1099], pp. 83–102.
- [593] F. Durán, C. Rocha, J.M. Álvarez, Tool interoperability in the Maude formal environment, in: [1036], pp. 400–406.
- [594] F. Durán, C. Rocha, J.M. Álvarez, Towards a Maude formal environment, in: [1015], pp. 329–351.
- [595] S. Eker, J. Meseguer, A. Sridharanarayanan, The Maude LTL model checker and its implementation, in: T. Ball, S.K. Rajamani (Eds.), Model Checking Software, 10th International SPIN Workshop. Portland, OR, USA, May 9–10, 2003, Proceedings, volume 2648 of *Lecture Notes in Computer Science*, Springer, 2003, pp. 230–234.
- [596] S. Eker, J. Meseguer, A. Sridharanarayanan, The Maude LTL model checker, in: [1056], pp. 162–187.
- [597] J. Hendrix, M. Clavel, J. Meseguer, A sufficient completeness reasoning tool for partial specifications, in: [1059], pp. 165–174.
- [598] J. Hendrix, D. Kapur, J. Meseguer, Coverset induction with partiality and subsorts: A powerlist case study, in: M. Kaufmann, L.C. Paulson (Eds.), Interactive Theorem Proving, First International Conference, ITP 2010, Edinburgh, UK, July 11–14, 2010. Proceedings, volume 6172 of *Lecture Notes in Computer Science*, Springer, 2010, pp. 275–290.
- [599] J. Hendrix, J. Meseguer, H. Ohsaki, A sufficient completeness checker for linear order-sorted specifications modulo axioms, in: U. Furbach, N. Shankar (Eds.), Automated Reasoning, Third International Joint Conference, IJCAR 2006, Seattle, WA, USA, August 17–20, 2006, Proceedings, volume 4130 of *Lecture Notes in Computer Science*, Springer, 2006, pp. 151–155.
- [600] J. Hendrix, H. Ohsaki, J. Meseguer, Sufficient Completeness Checking with Propositional Tree Automata, Technical Report UIUCDCS-R-2005-2635, Department of Computer Science, University of Illinois at Urbana-Champaign, 2005.
- [601] A. Riesco, A. Verdejo, R. Caballero, N. Martí-Oliet, Declarative debugging of Maude modules, Technical Report SIC-6/08, Dpto. Sistemas Informáticos y Computación, Universidad Complutense de Madrid, 2008.
- [602] A. Riesco, A. Verdejo, R. Caballero, N. Martí-Oliet, Declarative debugging of rewriting logic specifications, in: [1037], pp. 308–325.
- [603] A. Riesco, A. Verdejo, N. Martí-Oliet, Declarative Debugging of Missing Answers in Rewriting Logic, Technical Report SIC-6/09, Dpto. Sistemas Informáticos y Computación, Universidad Complutense de Madrid, 2009.
- [604] A. Riesco, A. Verdejo, N. Martí-Oliet, Declarative debugging of missing answers for Maude, in: [1084], pp. 277–294.
- [605] A. Riesco, A. Verdejo, N. Martí-Oliet, Enhancing the debugging of Maude specifications, in: [1093], pp. 226–242.
- [606] A. Riesco, A. Verdejo, N. Martí-Oliet, A complete declarative debugger for Maude, in: [1068], pp. 216–225.
- [607] A. Riesco, A. Verdejo, N. Martí-Oliet, R. Caballero, A declarative debugger for Maude, in: J. Meseguer, G. Roşu (Eds.), Algebraic Methodology and Software Technology, 12th International Conference, AMAST 2008, Urbana, IL, USA, July 28–31, 2008, Proceedings, volume 5140 of *Lecture Notes in Computer Science*, Springer, 2008, pp. 116–121.
- [608] A. Riesco, A. Verdejo, N. Martí-Oliet, R. Caballero, Declarative Debugging of Rewriting Logic Specifications, Technical Report SIC-02/10, Dpto. Sistemas Informáticos y Computación, Universidad Complutense de Madrid, 2010.
- [609] A. Riesco, A. Verdejo, N. Martí-Oliet, R. Caballero, Declarative debugging of rewriting logic specifications, *Journal of Logic and Algebraic Programming* (2012). This volume.
- [610] R. Sasse, J. Meseguer, Java+ITP: A Verification Tool Based on Hoare Logic and Algebraic Semantics, Technical Report UIUCDCS-R-2006-2685, Department of Computer Science, University of Illinois at Urbana-Champaign, 2006.
- [611] R. Sasse, J. Meseguer, Java+ITP: A verification tool based on Hoare logic and algebraic semantics, in: [1039], pp. 29–46.

5.2. Other tools

Rewriting logic in general, and Maude in particular, have been used to define and implement a considerable number of tools for describing and analyzing systems of varied nature; to name just a couple of examples, JavaFAN is a tool for the formal analysis of Java programs, while CIRC implements and automates the theorem-proving circular coinduction technique. Other tools, like the InterOperability Platform, are designed with the goal of making easier the interaction of Maude with external tools. There are many more tools that could have been included in this section; however, whenever we have found another section devoted to an application area where the tool in question is particularly relevant, we have preferred to include the corresponding papers there; for example, the open calculus of constructions is included in Section 3.1 on rewriting logic as a logical framework, the K system plays an essential role in Section 3.5 on the K framework, the Maude-NPA protocol analyzer appears in Section 6.5 on security applications, and the Pathway Logic Assistant is the main tool in Section 6.7 on bioinformatics applications, to name a few.

- [612] M. Alpuente, M.A. Feliú, C. Joubert, A. Villanueva, Defining Datalog in rewriting logic, in: [1102], pp. 188–204.
- [613] A. Arusoaie, T.F. Şerbănuţă, C. Ellison, G. Roşu, Making Maude definitions more interactive, in: [1042]. To appear.
- [614] K. Bae, P.C. Ölveczky, J. Meseguer, A. Al-Nayem, The SynchAADL2Maude tool, in: [1076], pp. 59–62.
- [615] K.M. Begnum, M. Burgess, Understanding promise theory using rewriting logic, in: A.K. Bandara, M. Burgess (Eds.), Inter-Domain Management, First International Conference on Autonomous Infrastructure, Management and Security, AIMS 2007, Oslo, Norway, June 21–22, 2007, Proceedings, volume 4543 of *Lecture Notes in Computer Science*, Springer, 2007, pp. 204–207.
- [616] L. Bendiksen, Specification and Analysis of Priced Systems in Priced-Timed Maude, Master's thesis, Department of Informatics, University of Oslo, 2008.
- [617] L. Bendiksen, P.C. Ölveczky, The Priced-Timed Maude tool, in: [1075], pp. 443–448.
- [618] M. Codescu, T. Mossakowski, A. Riesco, C. Maeder, Integrating Maude into Hets, Technical Report SIC-7/10, Dpto. Sistemas Informáticos y Computación, Universidad Complutense de Madrid, 2010.
- [619] M. Codescu, T. Mossakowski, A. Riesco, C. Maeder, Integrating Maude into Hets, in: [1068], pp. 60–75.

- [620] G. Denker, C.L. Talcott, G. Roşu, M. van den Brand, S. Eker, T.F. Şerbănuţă, **Rewriting logic systems**, in: [1039], pp. 233–247.
- [621] F. Durán, M. Roldán, J.C. Bach, E. Balland, M. van den Brand, J.R. Cordy, S. Eker, L. Engelen, M. de Jonge, K.T. Kalleberg, L.C.L. Kats, P.E. Moreau, E. Visser, **The third rewrite engines competition**, in: [1093], pp. 243–261.
- [622] F. Durán, M. Roldán, E. Balland, M. van den Brand, S. Eker, K.T. Kalleberg, L.C.L. Kats, P.E. Moreau, R. Schevchenko, E. Visser, **The second rewrite engines competition**, in: [1099], pp. 281–291.
- [623] A. Farzan, **Static and Dynamic Formal Analysis of Concurrent Systems and Languages: A Semantics-Based Approach**, Ph.D. thesis, Department of Computer Science, University of Illinois at Urbana-Champaign, 2007.
- [624] A. Farzan, F. Chen, J. Meseguer, G. Roşu, **Formal analysis of Java programs in JavaFAN**, in: R. Alur, D. Peled (Eds.), *Computer Aided Verification*, 16th International Conference, CAV 2004, Boston, MA, USA, July 13–17, 2004, Proceedings, volume 3114 of *Lecture Notes in Computer Science*, Springer, 2004, pp. 501–505.
- [625] A. Farzan, J. Meseguer, G. Roşu, **Formal JVM code analysis in JavaFAN**, in: [1098], pp. 132–147.
- [626] E.I. Goriac, D. Lucanu, G. Roşu, **Automating coinduction with case analysis**, in: [1040], pp. 220–236.
- [627] K. Havelund, G. Roşu, **Java PathExplorer – A runtime verification tool**, in: Proceedings of the 6th International Symposium on Artificial Intelligence, Robotics and Automation in Space, ISAIRAS 2001, Montreal, Canada, June 18–22, 2001.
- [628] K. Havelund, G. Roşu, **Monitoring Java programs with Java PathExplorer**, in: K. Havelund, G. Roşu (Eds.), Proceedings of the First Workshop on Runtime Verification (in connection with CAV ’01), RV 2001, Paris, France, July 23, 2001, volume 55(2) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2001, pp. 200–217.
- [629] K. Havelund, G. Roşu, **An overview of the runtime verification tool Java PathExplorer**, *Formal Methods in System Design* 24 (2004) 189–215.
- [630] M. Kühn, **Integrating Maude into Hets**, Master’s thesis, Universität Bremen, 2010. Diplomarbeit.
- [631] D. Lucanu, E.I. Goriac, C. Caltais, G. Roşu, **CIRC: A behavioral verification tool based on circular coinduction**, in: [1075], pp. 433–442.
- [632] D. Lucanu, G. Roşu, **CIRC: A circular coinductive prover**, in: [1087], pp. 372–378.
- [633] D. Lucanu, G. Roşu, **Circular coinduction with special contexts**, in: [1031], pp. 639–659.
- [634] A.M. Maidl, C. Carvilhe, M.A. Musicante, **Maude object-oriented action tool**, in: M. Ayala-Rincón, E.H. Haeusler (Eds.), Proceedings of the Second Workshop on Logical and Semantic Frameworks, with Applications, LSFA 2007, Ouro Preto, Brazil, August 28–29, 2007, volume 205 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2008, pp. 105–121.
- [635] A.M. Maidl, C. Carvilhe, M.A. Musicante, **An implementation of object-oriented action semantics in Maude**, in: [1094], pp. 81–105.
- [636] I.A. Mason, C.L. Talcott, **IOP: The InterOperability Platform & iMaude: An interactive extension of Maude**, in: [1085], pp. 315–333.
- [637] I.A. Mason, C.L. Talcott, **The InterOperability Platform Manual**, 2006.
- [638] P. Naumov, M.O. Stehr, J. Meseguer, **The HOL/NuPRL proof translator (a practical approach to formal interoperability)**, in: R.J. Boulton, P.B. Jackson (Eds.), *Theorem Proving in Higher Order Logics*, 14th International Conference, TPHOLS 2001, Edinburgh, Scotland, UK, September 3–6, 2001, Proceedings, volume 2152 of *Lecture Notes in Computer Science*, Springer, 2001, pp. 329–345.

6. Applications

As discussed in Section 3, rewriting logic has very good properties as a logical and semantic framework; this implies, in particular, that many kinds of systems have very natural representations as (executable) rewrite theories. Moreover, the rewriting logic-based languages are indeed general-purpose specification and programming languages, and therefore there is no limit to the applications that can be developed using them. However, the experience accumulated along these 20 years of work and compiled in this bibliography shows that they have been preferred in some areas, such as distributed architectures and networks, and security (of course, another reason for this is the fact that these areas are fundamental in the current developments in Computer Science). As in other occasions, both the named areas and the classification of papers in them follow somewhat arbitrary conventions, so, for example, automated deduction applications do not appear in this section, because they have been included in previous sections. Finally, it is clear that some extensions of Maude with particular goals have important applications in the intended areas: Real-Time Maude for real-time systems, PMAude for probabilistic systems, Maude-NPA for security, and the Pathway Logic Assistant for bioinformatics.

6.1. Network systems

The explicit emphasis on rewriting logic as a unifying framework for concurrency has endowed it with a considerable flexibility to model distributed objects and the many different modes of communication and interaction between them; for this reason, it is very well suited for the specification and analysis of network architectures and of communication protocols, including active networks and wireless sensor networks. Although most of the case studies included in this section are applied to existing network protocols, some others have also been used to design and study new protocols before their implementation. Moreover, the work reported in this section is complemented by the work on real-time systems in Section 6.2, because time is paramount in some protocols, and by the work on security in Section 6.5, because many protocols are, for example, cryptographic protocols or have essential security concerns. Finally, also included here is work on Mobile Maude, an extension to handle mobile agents deployed on networks.

- [639] J.V. Baalen, T. Böhne, **Automated protocol analysis in Maude**, in: M.G. Hinchey, J.L. Rash, W. Truszkowski, C. Rouff, D.F. Gordon-Spears (Eds.), *Formal Approaches to Agent-Based Systems*, Second International Workshop, FAABS 2002, Greenbelt, MD, USA, October 29–31, 2002, Revised Papers, volume 2699 of *Lecture Notes in Computer Science*, Springer, 2003, pp. 68–78.
- [640] J.V. Baalen, J.L. Caldwell, S. Mishra, **Specifying and checking fault-tolerant agent-based protocols using Maude**, in: J.L. Rash, C. Rouff, W. Truszkowski, D.F. Gordon, M.G. Hinchey (Eds.), *Formal Approaches to Agent-Based Systems*, First International Workshop, FAABS 2000, Greenbelt, MD, USA, April 5–7, 2000, Revised Papers, volume 1871 of *Lecture Notes in Computer Science*, Springer, 2001, pp. 180–193.
- [641] G. Denker, J.J. García-Luna-Aceves, J. Meseguer, P.C. Ölveczky, J. Raju, B. Smith, C.L. Talcott, **Specification and analysis of a reliable broadcasting protocol in Maude**, in: B. Hajek, R.S. Sreenivas (Eds.), Proceedings of the 37th Allerton Conference on Communication, Control and Computation, University of Illinois, 1999, pp. 738–747.
- [642] G. Denker, J.J. García-Luna-Aceves, J. Meseguer, P.C. Ölveczky, J. Raju, B. Smith, C.L. Talcott, **Specifying a Reliable Broadcasting Protocol in Maude**, Technical Report, Computer Science Laboratory, SRI International, 1999.

- [643] G. Denker, J. Meseguer, C.L. Talcott, **Protocol specification and analysis in Maude**, in: N. Heintze, J. Wing (Eds.), *Proceedings of the Workshop on Formal Methods and Security Protocols, FMSP'98*, Indianapolis, Indiana, June 25, 1998.
- [644] G. Denker, J. Meseguer, C.L. Talcott, **Formal specification and analysis of active networks and communication protocols: The Maude experience**, in: [1074], pp. 251–265.
- [645] G. Denker, J. Millen, **Modeling group communication protocols using multiset term rewriting**, in: [1056], pp. 20–39.
- [646] G. Denker, C.L. Talcott, **A formal framework for goal net analysis**, in: *Proceedings of the ICAPS'05 Workshop on Verification and Validation of Model-Based Planning and Scheduling Systems*, Monterey, California, 6–7 June 2005, AAAI, 2005.
- [647] F. Durán, S. Eker, P. Lincoln, J. Meseguer, **Principles of Mobile Maude**, in: D. Kotz, F. Mattern (Eds.), *Agent Systems, Mobile Agents, and Applications*, Second International Symposium on Agent Systems and Applications and Fourth International Symposium on Mobile Agents, ASA/MA 2000, Zürich, Switzerland, September 13–15, 2000, *Proceedings*, volume 1882 of *Lecture Notes in Computer Science*, Springer, 2000, pp. 73–85.
- [648] F. Durán, F. Gutiérrez, P. López, E. Pimentel, **A formalization of the SMEPP model in Maude**, in: V. Cahill (Ed.), *Proceedings of the 5th Annual International Conference on Mobile and Ubiquitous Systems: Computing, Networking, and Services, MobiQuitous 2008*, July 21–25, 2008, Dublin, Ireland, ACM, 2008.
- [649] F. Durán, F. Gutiérrez, P. López, E. Pimentel, **On the formal analysis of P2P algorithms with aSmOL and Maude**, in: *Proceedings of the XIV Ibero-American Conference on Software Engineering (CibSE 2011)*, Rio de Janeiro, Brazil, April 27–29, 2011.
- [650] F. Durán, M. Ouederni, G. Salaün, **Checking protocol compatibility using Maude**, in: G. Salaün, M. Sirjani (Eds.), *Proceedings of the 8th International Workshop on the Foundations of Coordination Languages and Software Architectures, FOCLASA 2009*, Rhodes, Greece, July 11, 2009, volume 255 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2009, pp. 65–81.
- [651] F. Durán, M. Ouederni, G. Salaün, **A generic framework for n-protocol compatibility checking**, *Science of Computer Programming* (2011). To appear.
- [652] F. Durán, A. Riesco, A. Verdejo, **A distributed implementation of Mobile Maude**, in: [1039], pp. 113–131.
- [653] F. Durán, A. Verdejo, **A conference reviewing system in Mobile Maude**, in: [1056], pp. 127–143.
- [654] A. Goodloe, C.A. Gunter, M.O. Stehr, **Formal prototyping in early stages of protocol design**, in: C. Meadows (Ed.), *Proceedings of the POPL 2005 Workshop on Issues in the Theory of Security, WITS 2005*, Long Beach, California, USA, January 10–11, 2005, ACM, 2005, pp. 67–80.
- [655] S. Gutierrez-Nolasco, N. Venkatasubramanian, M.O. Stehr, C.L. Talcott, **Exploring adaptability of secure group communication using formal prototyping techniques**, in: F. Kon, F.M. Costa, N. Wang, R. Cerqueira (Eds.), *Proceedings of the 3rd Workshop on Adaptive and Reflective Middleware*, Toronto, Ontario, Canada, October 19, 2004, ACM, 2004, pp. 232–237.
- [656] M. Katelman, J. Meseguer, J.C. Hou, **Redesign of the LMST wireless sensor protocol through formal modeling and statistical model checking**, in: [1028], pp. 150–169.
- [657] I.A. Mason, C.L. Talcott, **Simple network protocol simulation within Maude**, in: [1052], pp. 274–291.
- [658] J. Meseguer, P.C. Ölveczky, M.O. Stehr, C.L. Talcott, **Maude as a wide-spectrum framework for formal modeling and analysis of active networks**, in: D. Maughan (Ed.), *Proceedings of the 2002 DARPA Active Networks Conference and Exposition, DANCE 2002*, San Francisco, CA, USA, May 29–31, 2002, IEEE Computer Society, 2002, pp. 494–510.
- [659] S. Nakajima, **Encoding mobility in CafeOBJ: An exercise of describing mobile code-based software architecture**, in: [1051].
- [660] P.C. Ölveczky, S. Meldal, **Specification and prototyping of network protocols in rewriting logic**, in: *Proceedings of the Norsk Informatikkonferanse, NIK'98*, Kristiansand, Norway, November 23–25, 1998.
- [661] M. Ouederni, **On Checking the Compatibility of Service Interaction Protocols**, Ph.D. thesis, Universidad de Málaga, Spain, 2011.
- [662] I. Pita, **A formal specification of the Kademlia distributed hash table**, in: V.M. Gulías, J. Silva, A. Villanueva (Eds.), *Actas de las X Jornadas sobre Programación y Lenguajes, PROLE 2010*, Valencia, Septiembre 8–10, 2010, Garceta grupo editorial, 2010, pp. 223–234.
- [663] I. Pita, N. Martí-Oliet, **A Maude specification of an object-oriented database model for telecommunication networks**, in: [1086], pp. 405–423.
- [664] I. Pita, N. Martí-Oliet, **A Maude specification of an object-oriented model for telecommunication networks**, *Theoretical Computer Science* 285 (2002) 407–439.
- [665] A. Riesco, **Distributed and Mobile Applications in Maude**, Master's thesis, Facultad de Informática, Universidad Complutense de Madrid, 2007.
- [666] A. Riesco, A. Verdejo, **The EIGRP Protocol in Maude**, Technical Report SIC-3/07, Dpto. Sistemas Informáticos y Computación, Universidad Complutense de Madrid, 2007.
- [667] A. Riesco, A. Verdejo, **Implementing and analyzing in Maude the Enhanced Interior Gateway Routing Protocol**, in: [1099], pp. 249–266.
- [668] D.E. Rodríguez, **Case studies in the specification and analysis of protocols in Maude**, in: [1052], pp. 257–273.
- [669] D.E. Rodríguez, **On modelling sensor networks in Maude**, in: [1039], pp. 199–213.
- [670] M.O. Stehr, C.L. Talcott, J.M. Rushby, P. Lincoln, M. Kim, S. Cheung, A. Poggio, **Fractionated software for networked cyber-physical systems: Research directions and long-term vision**, in: [1015], pp. 110–143.
- [671] A. Verdejo, I. Pita, N. Martí-Oliet, **The leader election protocol of IEEE 1394 in Maude**, in: [1052], pp. 383–404.
- [672] A. Verdejo, I. Pita, N. Martí-Oliet, **Specification and verification of the tree identify protocol of IEEE 1394 in rewriting logic**, *Formal Aspects of Computing* 14 (2003) 228–246.
- [673] B.Y. Wang, J. Meseguer, C.A. Gunter, **Specification and formal analysis of a PLAN algorithm in Maude**, in: T.H. Lai (Ed.), *Proceedings of the 2000 ICDCS International Workshop on Distributed System Validation and Verification*, Taipei, Taiwan, ROC, April 10, 2000, pp. E49–E56.
- [674] A. Wang, C.L. Talcott, A.J.T. Gurney, B.T. Loo, A. Scedrov, **Reduction-Based Formal Analysis of BGP Instances**, Technical Report, Department of Computer and Information, University of Pennsylvania, 2012.
- [675] A. Wang, C.L. Talcott, A.J.T. Gurney, B.T. Loo, A. Scedrov, **Reduction-based formal analysis of BGP instances**, in: C. Flanagan, B. König (Eds.), *Tools and Algorithms for the Construction and Analysis of Systems – 18th International Conference, TACAS 2012, Held as Part of the European Joint Conferences on Theory and Practice of Software, ETAPS 2012*, Tallinn, Estonia, March 24–April 1, 2012. *Proceedings*, volume 7214 of *Lecture Notes in Computer Science*, Springer, 2012, pp. 283–298.
- [676] A. Wang, C.L. Talcott, L. Jia, B.T. Loo, A. Scedrov, **Analyzing BGP instances in Maude**, in: R. Bruni, J. Dingel (Eds.), *Formal Techniques for Distributed Systems – Joint 13th IFIP WG 6.1 International Conference, FMOODS 2011, and 31st IFIP WG 6.1 International Conference, FORTE 2011*, Reykjavik, Iceland, June 6–9, 2011. *Proceedings*, volume 6722 of *Lecture Notes in Computer Science*, Springer, 2011, pp. 334–348.

6.2. Real-time and hybrid systems

Most of the following papers put into practice the theory on real-time systems described in Section 2.10, typically by means of the Real-Time Maude Tool. Indeed, Real-Time Maude is a specification language and a tool written in Maude itself which provides special syntax to specify real-time systems and suitable commands to analyze the timed properties by means of rewriting, search, and model checking. As this section evidences, there has been a substantial body of work in this area, applying these techniques to several protocols, distributed services, embedded systems, synchronous systems, and visual modeling, among others. More recent work on Real-Time Maude focuses on extending its implementation to handle other continuous variables, like temperature, thus broadening its applicability to a wide range of hybrid systems.

- [677] M. AlTurki, **Rewriting-based formal modeling, analysis and implementation of real-time distributed services**, Ph.D. thesis, Department of Computer Science, University of Illinois at Urbana-Champaign, 2011.

- [678] M. Alturki, D. Dhurjati, D. Yu, A. Chander, H. Inamura, **Formal specification and analysis of timing properties in software systems**, in: [1032], pp. 262–277.
- [679] K. Bae, P.C. Ölveczky, **Extending the Real-Time Maude semantics of Ptolemy to hierarchical DE models**, in: [1092], pp. 46–66.
- [680] K. Bae, P.C. Ölveczky, A. Al-Nayem, J. Meseguer, **Synchronous AADL and its formal analysis in Real-Time Maude**, in: S. Qin, Z. Qiu (Eds.), *Formal Methods and Software Engineering – 13th International Conference on Formal Engineering Methods, ICFEM 2011, Durham, UK, October 26–28, 2011. Proceedings, volume 6991 of Lecture Notes in Computer Science*, Springer, 2011, pp. 651–667.
- [681] K. Bae, P.C. Ölveczky, A. Al-Nayem, J. Meseguer, **Synchronous AADL and its Formal Analysis in Real-Time Maude**, Technical Report, Department of Computer Science, University of Illinois at Urbana-Champaign, 2011.
- [682] K. Bae, P.C. Ölveczky, T.H. Feng, E.A. Lee, S. Tripakis, **Verifying hierarchical Ptolemy II discrete-event models using Real-Time Maude**, *Science of Computer Programming* (2011). To appear.
- [683] K. Bae, P.C. Ölveczky, T.H. Feng, S. Tripakis, **Verifying Ptolemy II discrete-event models using Real-Time Maude**, in: [1031], pp. 717–736.
- [684] E. Boffara, O. Bournez, H. Kacem, C. Kirchner, **Verification of timed automata using rewrite rules and strategies**, in: N. Dershowitz, A. Frank (Eds.), *Proceedings of the Seventh Biennial Bar-Ilan International Symposium on the Foundations of Artificial Intelligence, BISFAI 2001, Ramat-Gan, Israel, June 25–27, 2001, Computing Research Repository (CoRR)*, 2001.
- [685] J. Bjørk, E.B. Johnsen, O. Owe, R. Schlatte, **Lightweight time modeling in timed Creol**, in: [1092], pp. 67–81.
- [686] A. Boronat, P.C. Ölveczky, **Formal real-time model transformations in MOMENT2**, in: D.S. Rosenblum, G. Taentzer (Eds.), *Fundamental Approaches to Software Engineering, 13th International Conference, FASE 2010, Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2010, Paphos, Cyprus, March 20–28, 2010. Proceedings, volume 6013 of Lecture Notes in Computer Science*, Springer, 2010, pp. 29–43.
- [687] F. Boutekkouk, **Rewriting logic based performance estimation of embedded systems**, in: K. Al-Begain, D. Fiems, W.J. Knottenbelt (Eds.), *Analytical and Stochastic Modeling Techniques and Applications, 17th International Conference, ASMTA 2010, Cardiff, UK, June 14–16, 2010. Proceedings, volume 6148 of Lecture Notes in Computer Science*, Springer, 2010, pp. 117–129.
- [688] H. Ding, C. Zheng, G. Agha, L. Sha, **Automated verification of the dependability of object-oriented real-time systems**, in: *Proceedings of the 9th IEEE International Workshop on Object-Oriented Real-Time Dependable Systems (WORDS 2003 Fall), Anacapri (Capri Island), Italy, October 1–3, 2003, IEEE Computer Society*, 2004, pp. 171–178.
- [689] F. Durán, P.C. Ölveczky, J.E. Rivera, **Formal visual modeling of real-time systems in e-Motions: Two case studies**, in: [1043], pp. 49–63.
- [690] M. Fadlisyah, E. Ábrahám, D. Lepri, P.C. Ölveczky, **A rewriting-logic-based technique for modeling thermal systems**, in: [1092], pp. 82–100.
- [691] M. Fadlisyah, P.C. Ölveczky, E. Ábrahám, **Adaptive-step-size numerical methods in rewriting-logic-based formal analysis of interacting hybrid systems**, in: M. Zhang, V. Stolz (Eds.), *Proceedings of the 4th International Workshop on Harnessing Theories for Tool Support in Software, TTSS 2010, Shanghai, China, November 15, 2010, volume 274 of Electronic Notes in Theoretical Computer Science*, Elsevier, 2011, pp. 17–32.
- [692] M. Fadlisyah, P.C. Ölveczky, E. Ábrahám, **Formal modeling and analysis of hybrid systems in rewriting logic using higher-order numerical methods and discrete-event detection**, in: F. Arbab, H. Mirian (Eds.), *Proceedings of the 2011 CSI International Symposium on Computer Science and Software Engineering, CSSE 2011, Tehran, Iran, June 15–16, 2011, IEEE*, 2011, pp. 1–8.
- [693] M. Fadlisyah, P.C. Ölveczky, E. Ábrahám, **Object-oriented formal modeling and analysis of interacting hybrid systems in HI-Maude**, in: G. Barthe, A. Pardo, G. Schneider (Eds.), *Software Engineering and Formal Methods – 9th International Conference, SEFM 2011, Montevideo, Uruguay, November 14–18, 2011. Proceedings, volume 7041 of Lecture Notes in Computer Science*, Springer, 2011, pp. 415–430.
- [694] M. Grimeland, **Modeling and Analysis of Time-Dependent Security Protocols in Real-Time Maude**, Master's thesis, Department of Informatics, University of Oslo, 2006.
- [695] M. Katelman, J. Meseguer, **Using the PALS architecture to verify a distributed topology control protocol for wireless multi-hop networks in the presence of node failures**, in: [1092], pp. 101–116.
- [696] M. Kim, M.O. Stehr, C.L. Talcott, N. Dutt, N. Venkatasubramanian, **Combining formal verification with observed system execution behavior to tune system parameters**, in: J.F. Raskin, P.S. Thiagarajan (Eds.), *Formal Modeling and Analysis of Timed Systems, 5th International Conference, FORMATS 2007, Salzburg, Austria, October 3–5, 2007. Proceedings, volume 4763 of Lecture Notes in Computer Science*, Springer, 2007, pp. 257–273.
- [697] D. Lepri, P.C. Ölveczky, E. Ábrahám, **Model checking classes of metric LTL properties of object-oriented Real-Time Maude specifications**, in: [1092], pp. 117–136.
- [698] A. Lescaylle, A. Villanueva, **The tcpcp interpreter**, in: [1083], pp. 63–77.
- [699] E. Lien, **Formal Modelling and Analysis of the NORM Multicast Protocol Using Real-Time Maude**, Master's thesis, Department of Linguistics, University of Oslo, 2004.
- [700] E. Lien, P.C. Ölveczky, **Formal modeling and analysis of an IETF multicast protocol**, in: D.V. Hung, P. Krishnan (Eds.), *Proceedings of the Seventh IEEE International Conference on Software Engineering and Formal Methods, SEFM 2009, Hanoi, Vietnam, November 23–27, 2009, IEEE Computer Society*, 2009, pp. 273–282.
- [701] F.J. Lucas, J.A. Toval Álvarez, **Model transformations powered by rewriting logic**, in: Z. Bellasene, C. Woo, E. Hunt, X. Franch, R. Coletta (Eds.), *Proceedings of the Forum at the CAiSE'08 Conference, Montpellier, France, June 18–20, 2008, volume 344 of CEUR Workshop Proceedings, CEUR-WS.org*, 2008, pp. 41–44.
- [702] T. Marir, F. Mokhati, H. Seridi-Bouchelaghem, **Formalizing ARTIS agent model using RT-Maude**, in: L. Braubach, W. van der Hoek, P. Petta, A. Pokahr (Eds.), *Multiagent System Technologies, 7th German Conference, MATES 2009, Hamburg, Germany, September 9–11, 2009. Proceedings, volume 5774 of Lecture Notes in Computer Science*, Springer, 2009, pp. 226–231.
- [703] J. Meseguer, P.C. Ölveczky, **Formalization and Correctness of the PALS Architectural Pattern for Distributed Real-Time Systems**, Technical Report, Department of Computer Science, University of Illinois at Urbana-Champaign, 2010.
- [704] J. Meseguer, P.C. Ölveczky, **Formalization and correctness of the PALS architectural pattern for distributed real-time systems**, in: [1040], pp. 303–320.
- [705] V. Nigam, T.B. Kirigin, A. Scedrov, C.L. Talcott, M.I. Kanovich, R. Perovic, **Towards an automated assistant for clinical investigations**, in: G. Luo, J. Liu, C.C. Yang (Eds.), *ACM International Health Informatics Symposium, IHI 2012, Miami, FL, USA, January 28–30, 2012, ACM*, 2012, pp. 773–778.
- [706] K. Ogata, K. Futatsugi, **Modeling and verification of distributed real-time systems based on CafeOBJ**, in: [1044], pp. 185–192.
- [707] K. Ogata, K. Futatsugi, **Modeling and verification of real-time systems based on equations**, *Science of Computer Programming* 66 (2007) 162–180.
- [708] P.C. Ölveczky, **Real-Time Maude 2.3 Manual**, 2007.
- [709] P.C. Ölveczky, **Formal modeling and analysis of a distributed database protocol in Maude**, in: K.C. Li, N. Maillard, T.L. Xu, S.C. Lo (Eds.), *Proceedings of the 2008 International Symposium on Scientific and Engineering Computing, SEC 2008, Sao Paulo, Brazil, July 16–18, 2008, IEEE Computer Society*, 2008, pp. 37–44.
- [710] P.C. Ölveczky, **Towards formal modeling and analysis of networks of embedded medical devices in Real-Time Maude**, in: P. Muenchaisri (Ed.), *Proceedings of the Ninth ACIS International Conference on Software Engineering, Artificial Intelligence, Networking and Parallel/Distributed Computing, SNPDC 2008, Phuket, Thailand, August 6–8, 2008, IEEE Computer Society*, 2008, pp. 241–248.
- [711] P.C. Ölveczky, **Formal model engineering for embedded systems using Real-Time Maude**, in: [1043], pp. 3–13.
- [712] P.C. Ölveczky, **Semantics, simulation, and formal analysis of modeling languages for embedded systems in Real-Time Maude**, in: [1015], pp. 368–402.
- [713] P.C. Ölveczky, A. Boronat, J. Meseguer, **Formal semantics and analysis of behavioral AADL models in Real-Time Maude**, in: [1065], pp. 47–62.
- [714] P.C. Ölveczky, M. Caccamo, **Formal simulation and analysis of the CASH scheduling algorithm in Real-Time Maude**, in: [1027], pp. 357–372.
- [715] P.C. Ölveczky, M. Grimeland, **Formal analysis of time-dependent cryptographic protocols in Real-Time Maude**, in: [1095], pp. 1–8.
- [716] P.C. Ölveczky, M. Keaton, J. Meseguer, C.L. Talcott, S. Zabele, **Specification and analysis of the AER/NCA active network protocol suite in Real-Time Maude**, in: H. Hußmann (Ed.), *Fundamental Approaches to Software Engineering, 4th International Conference, FASE 2001, Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2001, Genova, Italy, April 2–6, 2001. Proceedings, volume 2029 of Lecture Notes in Computer Science*, Springer, 2001, pp. 333–348.

- [717] P.C. Ölveczky, P. Kosiuczenko, M. Wirsing, **An object-oriented algebraic steam-boiler control specification**, in: J.R. Abrial, E. Börger, H. Langmaack (Eds.), *Formal Methods for Industrial Applications, Specification and Programming the Steam Boiler Control*, volume 1165 of *Lecture Notes in Computer Science*, Springer, 1996, pp. 379–402.
- [718] P.C. Ölveczky, J. Meseguer, **Real-Time Maude: A tool for simulating and analyzing real-time and hybrid systems**, in: [1052], pp. 361–382.
- [719] P.C. Ölveczky, J. Meseguer, **Real-Time Maude 2.1**, in: [1085], pp. 285–314.
- [720] P.C. Ölveczky, J. Meseguer, **Specification and analysis of real-time systems using Real-Time Maude**, in: M. Wermelinger, T. Margaria (Eds.), *Fundamental Approaches to Software Engineering, 7th International Conference, FASE 2004*, Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2004 Barcelona, Spain, March 29–April 2, 2004, Proceedings, volume 2984 of *Lecture Notes in Computer Science*, Springer, 2004, pp. 354–358.
- [721] P.C. Ölveczky, J. Meseguer, **Recent advances in Real-Time Maude**, in: [1046], pp. 65–81.
- [722] P.C. Ölveczky, J. Meseguer, **The Real-Time Maude tool**, in: C.R. Ramakrishnan, J. Rehof (Eds.), *Tools and Algorithms for the Construction and Analysis of Systems, 14th International Conference, TACAS 2008*, Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2008, Budapest, Hungary, March 29–April 6, 2008, Proceedings, volume 4963 of *Lecture Notes in Computer Science*, Springer, 2008, pp. 332–336.
- [723] P.C. Ölveczky, J. Meseguer, **Specification and verification of distributed embedded systems: A traffic intersection product family**, in: [1092], pp. 137–157.
- [724] P.C. Ölveczky, J. Meseguer, C.L. Talcott, **Specification and analysis of the AER/NCA active network protocol suite in Real-Time Maude**, *Formal Methods in System Design* 29 (2006) 253–293.
- [725] P.C. Ölveczky, P. Prabhakar, X. Liu, **Formal modeling and analysis of real-time resource-sharing protocols in Real-Time Maude**, in: Y. Robert (Ed.), *22nd IEEE International Symposium on Parallel and Distributed Processing, IPDPS 2008*, Miami, Florida USA, April 14–18, 2008, IEEE, 2008, pp. 1–8.
- [726] P.C. Ölveczky, S. Thorvaldsen, **Formal modeling and analysis of wireless sensor network algorithms in Real-Time Maude**, in: A.L. Rosenberg (Ed.), *Proceedings of the 20th International Parallel and Distributed Processing Symposium, IPDPS 2006*, Rhodes Island, Greece, April 25–29, 2006, IEEE, 2006.
- [727] P.C. Ölveczky, S. Thorvaldsen, **Formal modeling and analysis of the OGDC wireless sensor network algorithm in Real-Time Maude**, in: [1029], pp. 122–140.
- [728] P.C. Ölveczky, S. Thorvaldsen, **Formal modeling, performance estimation, and model checking of wireless sensor network algorithms in Real-Time Maude**, *Theoretical Computer Science* 410 (2009) 254–280.
- [729] K. Pattabiraman, N. Nakka, Z. Kalbarczyk, R.K. Iyer, **SymPLIFIED: Symbolic program-level fault injection and error detection framework**, in: *Proceedings of the 38th Annual IEEE/IFIP International Conference on Dependable Systems and Networks, DSN 2008*, Anchorage, Alaska, USA, June 24–27, 2008, IEEE Computer Society, 2008, pp. 472–481.
- [730] J.E. Rivera, **On the Semantics of Real-Time Domain Specific Modeling Languages**, Ph.D. thesis, Universidad de Málaga, Spain, 2010.
- [731] J.E. Rivera, F. Durán, A. Vallecillo, **On the behavioral semantics of real-time domain specific visual languages**, in: [1093], pp. 174–190.
- [732] J.E. Rivera, C. Vicente-Chicoté, A. Vallecillo, **Extending visual modeling languages with timed behavioral specifications**, in: *Actas de la Conferencia de Ingeniería de Requisitos y Ambientes de Software, IDEAS 2009*, Medellín, Colombia, Abril 13–17, 2009, pp. 44–57.
- [733] L.J. Steggle, P. Kosiuczenko, **A timed rewriting logic semantics for SDL: A case study of the alternating bit protocol**, in: [1071], pp. 83–104.
- [734] L.J. Steggle, P. Kosiuczenko, **A formal model for SDL specifications based on timed rewriting logic**, *Automated Software Engineering* 7 (2000) 61–90.
- [735] M. Sun, J. Meseguer, **Distributed real-time emulation of formally-defined patterns for safe medical device control**, in: [1092], pp. 158–177.
- [736] M. Sun, J. Meseguer, L. Sha, **A formal pattern architecture for safe medical systems**, in: [1093], pp. 157–173.
- [737] S. Thorvaldsen, **Modeling and Analysis of the OGDC Wireless Sensor Network Algorithm in Real-Time Maude**, Master's thesis, Department of Informatics, University of Oslo, 2005.
- [738] S. Thorvaldsen, P.C. Ölveczky, **Formal modeling and analysis of the OGDC wireless sensor network algorithm in Real-Time Maude**, 2007.
- [739] J. Troya, J.M. Bautista, F. López-Romero, A. Vallecillo, **Lightweight testing of communication networks with e-Motions**, in: M. Gogolla, B. Wolff (Eds.), *Tests and Proofs – 5th International Conference, TAP 2011*, Zurich, Switzerland, June 30–July 1, 2011, Proceedings, volume 6706 of *Lecture Notes in Computer Science*, Springer, 2011, pp. 187–204.
- [740] L. Wang, Z. Kalbarczyk, R.K. Iyer, **Formalizing system behavior for evaluating a system hang detector**, in: *Proceedings of the 27th IEEE Symposium on Reliable Distributed Systems, SRDS 2008*, Napoli, Italy, October 6–8, 2008, IEEE, 2008, pp. 269–278.
- [741] M. Wirsing, S.S. Bauer, A. Schroeder, **Modeling and analyzing adaptive user-centric systems in Real-Time Maude**, in: [1092], pp. 1–25.

6.3. Distributed architectures and components

For the same reason explained at the beginning of Section 6.1, rewriting logic is also very well suited for the specification and analysis of distributed architectures and components, and this is explicitly confirmed by all the relevant work reported in the following papers. These include specifications and appropriate analyses for distributed software architectures, object-oriented distributed systems and frameworks, middleware architectures for composable services, reference models for Open Distributed Processing, multi-agent systems, UML diagrams and metamodels, model driven architectures, model management and model transformations, validation of OCL properties, parameterized skeletons for distributed programming, and cyber physical systems, among others.

- [742] A. Albarrán, F. Durán, A. Vallecillo, **From Maude specifications to SOAP distributed implementations: A smooth transition**, in: O. Díaz, A. Illarramendi, M. Piattini (Eds.), *Actas de las VI Jornadas de Ingeniería del Software y Bases de Datos, JISBD 2001*, Almagro (Ciudad Real), España, Noviembre 21–23, 2001, pp. 419–434.
- [743] A. Albarrán, F. Durán, A. Vallecillo, **Maude meets CORBA**, in: G. Fernandez, C. Pons (Eds.), *Proceedings of the Second Argentine Symposium on Software Engineering, ASSE 2001*, Buenos Aires, Argentina, September 10–11, 2001.
- [744] A. Albarrán, F. Durán, A. Vallecillo, **On the smooth implementation of component-based system specifications**, in: J. Bosch, C. Szyperski, W. Weck (Eds.), *Proceedings of the 6th ECOOP International Workshop on Component-Oriented Programming, WCOP 2001*, Budapest, Hungary, June 19, 2001.
- [745] N. Aoumeur, K. Barkaoui, G. Saake, **Towards Maude-TLA based foundation for complex concurrent systems specification and certification**, in: S. Latifi (Ed.), *Proceedings of the Fifth International Conference on Information Technology: New Generations, ITNG 2008*, Las Vegas, Nevada, USA, April 7–8, 2008, IEEE Computer Society, 2008, pp. 1305–1307.
- [746] N. Aoumeur, G. Saake, **On the specification and validation of cooperative information systems using an extended Maude**, in: [1053], pp. 95–114.
- [747] L. Astefanoaei, **An Executable Theory of Multi-Agent Systems Refinement**, Ph.D. thesis, Center for Mathematics and Computer Science (CWI), Leiden University, The Netherlands, 2011.
- [748] L. Astefanoaei, F.S. de Boer, M. Dastani, **Strategic executions of choreographed timed normative multi-agent systems**, in: W. van der Hoek, G.A. Kaminka, Y. Lespérance, M. Luck, S. Sen (Eds.), *Proceedings of the 9th International Conference on Autonomous Agents and Multiagent Systems, AAMAS 2010*, Toronto, Canada, May 10–14, 2010, Volume 1–3, IFAAMAS, 2010, pp. 965–972.
- [749] L. Astefanoaei, M. Dastani, J.J.C. Meyer, F.S. de Boer, **On the semantics and verification of normative multi-agent systems**, *Journal of Universal Computer Science* 15 (2009) 2629–2652.
- [750] M. Benammar, F. Belala, F. Latreche, **AADL behavioral annex based on generalized rewriting logic**, in: O. Pastor, A. Flory, J.L. Cavarero (Eds.), *Proceedings of the IEEE International Conference on Research Challenges in Information Science, RCIS 2008*, Marrakech, Morocco, June 3–6, 2008, IEEE, 2008, pp. 1–8.
- [751] A. Boronat, **MOMENT: A Formal Framework for Model Management**, Ph.D. thesis, Universitat Politècnica de València, Spain, 2007.

- [752] A. Boronat, R. Bruni, A. Lluch-Lafuente, U. Montanari, G. Paolillo, Exploiting the hierarchical structure of rule-based specifications for decision planning, in: [1065], pp. 2–16.
- [753] A. Boronat, J.A. Carsí, I. Ramos, An algebraic baseline for automatic transformations in MDA, in: [1027], pp. 31–47.
- [754] A. Boronat, J.A. Carsí, I. Ramos, Automatic reengineering in MDA using rewriting logic as transformation engine, in: N. Gold, T. Systä (Eds.), Proceedings of the 9th European Conference on Software Maintenance and Reengineering, CSMR 2005, Manchester, UK, March 21–23, 2005, Proceedings, IEEE Computer Society, 2005, pp. 228–231.
- [755] A. Boronat, J.A. Carsí, I. Ramos, Automatic support for traceability in a generic model management framework, in: A. Hartman, D. Kreishe (Eds.), Model Driven Architecture – Foundations and Applications, First European Conference, ECMDA-FA 2005, Nuremberg, Germany, November 7–10, 2005, Proceedings, volume 3748 of *Lecture Notes in Computer Science*, Springer, 2005, pp. 316–330.
- [756] A. Boronat, J.A. Carsí, I. Ramos, Algebraic specification of a model transformation engine, in: L. Baresi, R. Heckel (Eds.), Fundamental Approaches to Software Engineering, 9th International Conference, FASE 2006, Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2006, Vienna, Austria, March 27–28, 2006, Proceedings, volume 3922 of *Lecture Notes in Computer Science*, Springer, 2006, pp. 262–277.
- [757] A. Boronat, J.A. Carsí, I. Ramos, P. Letelier, Formal model merging applied to class diagram integration, in: T. Mens, M. D'Hondt (Eds.), Proceedings of the ERCIM Working Group on Software Evolution, France, April 6–7, 2006, volume 166 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2007, pp. 5–26.
- [758] A. Boronat, R. Heckel, J. Meseguer, Rewriting logic semantics and verification of model transformations, in: [1032], pp. 18–33.
- [759] A. Boronat, J. Iborra, J.A. Carsí, I. Ramos, A. Gómez, Utilización de Maude desde Eclipse Modeling Framework en gestión de modelos: Maude aplicado a Eclipse Modeling Framework, in: J.A. Toval Álvarez, J. Hernández Núñez (Eds.), Actas de las X Jornadas de Ingeniería del Software y Bases de Datos, JISBD 2005, Granada, España, Septiembre 14–16, 2005, Thomson, 2005, pp. 253–258.
- [760] A. Boronat, J. Iborra, J.A. Carsí, I. Ramos, A. Gómez, Utilización de Maude desde Eclipse Modeling Framework para la gestión de modelos, in: A. Estévez, V. Pelechano, A. Vallecillo (Eds.), Actas del Taller sobre Desarrollo Dirigido por Modelos, MDA y Aplicaciones, Granada, España, Septiembre 13, 2005, volume 157 of *CEUR Workshop Proceedings*, CEUR-WS.org, 2005.
- [761] A. Boronat, A. Knapp, J. Meseguer, M. Wirsing, What is a multi-modeling language?, in: [1037], pp. 71–87.
- [762] A. Boronat, J. Meseguer, Algebraic Semantics of EMOF/OCL Metamodels, Technical Report UIUCDCS-R-2007-2904, Department of Computer Science, University of Illinois at Urbana-Champaign, 2007.
- [763] A. Boronat, J. Meseguer, An algebraic semantics for MOF, in: J.L. Fiadeiro, P. Inverardi (Eds.), Fundamental Approaches to Software Engineering, 11th International Conference, FASE 2008, Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2008, Budapest, Hungary, March 29–April 6, 2008, Proceedings, volume 4961 of *Lecture Notes in Computer Science*, Springer, 2008, pp. 377–391.
- [764] A. Boronat, J. Meseguer, Algebraic semantics of OCL-constrained metamodel specifications, in: M. Oriol, B. Meyer (Eds.), Objects, Components, Models and Patterns, 47th International Conference, TOOLS EUROPE 2009, Zurich, Switzerland, June 29–July 3, 2009, Proceedings, volume 33 of *Lecture Notes in Business Information Processing*, Springer, 2009, pp. 96–115.
- [765] A. Boronat, J. Meseguer, MOMENT2: EMF model transformations in Maude, in: A. Vallecillo, G. Sagardui (Eds.), Actas de las XIV Jornadas de Ingeniería del Software y Bases de Datos, JISBD 2009, San Sebastián, España, Septiembre 8–11, 2009, pp. 178–179.
- [766] A. Boronat, J. Meseguer, An algebraic semantics for MOF, Formal Aspects of Computing 22 (2010) 269–296.
- [767] A. Boronat, J. Meseguer, Automated model synchronization: A case study on UML with Maude, Electronic Communications of the EASST 41 (2011).
- [768] A. Boronat, J. Oriente, A. Gómez, I. Ramos, J.A. Carsí, An algebraic specification of generic OCL queries within the Eclipse modeling framework, in: A. Rensink, J. Warmer (Eds.), Model Driven Architecture – Foundations and Applications, Second European Conference, ECMDA-FA 2006, Bilbao, Spain, July 10–13, 2006, Proceedings, volume 4066 of *Lecture Notes in Computer Science*, Springer, 2006, pp. 316–330.
- [769] R. Bruni, A. Bucchiarone, S. Gnesi, D. Hirsch, A. Lluch-Lafuente, Graph-based design and analysis of dynamic software architectures, in: [1038], pp. 37–56.
- [770] R. Bruni, A. Lluch-Lafuente, Evaluating the performance of model transformation styles in Maude, in: Formal Aspects of Component Software, 8th International Symposium, FACS 2011, Oslo, Norway, September 14–16, 2011, Proceedings, Lecture Notes in Computer Science, Springer, 2011. To appear.
- [771] R. Bruni, A. Lluch-Lafuente, U. Montanari, Hierarchical design rewriting with Maude, in: [1099], pp. 45–62.
- [772] R. Bruni, A. Lluch-Lafuente, U. Montanari, On structured model-driven transformations, International Journal of Software and Informatics 5 (2011) 185–206.
- [773] M. Clavel, M. Egea, ITP/OCL: A rewriting-based validation tool for UML+OCL static class diagrams, in: [1069], pp. 368–373.
- [774] M. Clavel, M. Egea, Using reflection to implement in Maude a rewriting-based validation tool for UML+OCL static class diagrams, in: I. Ramos, J.A. Carsí, A. Boronat (Eds.), Proceedings of the First International Workshop on Algebraic Foundations for OCL and Applications, WAFOCA 2006, Valencia, Spain, March 22, 2006.
- [775] G. Denker, C.L. Talcott, Formal checklists for remote agent dependability, in: [1085], pp. 229–248.
- [776] F. Durán, M. Gogolla, M. Roldán, Tracing properties of UML and OCL models with Maude, in: [1043], pp. 81–97.
- [777] F. Durán, J. Herrador, A. Vallecillo, Using UML and Maude for writing and reasoning about ODP policies, in: J. Moffett, F. Garcia (Eds.), Proceedings of the 4th IEEE International Workshop on Policies for Distributed Systems and Networks, POLICY 2003, Lake Como, Italy, June 4–6, 2003, IEEE Computer Society, 2003, pp. 15–25.
- [778] F. Durán, M. Roldán, A. Vallecillo, Using Maude to write and execute ODP information viewpoint specifications, Computer Standards & Interfaces 27 (2005) 597–620.
- [779] F. Durán, A. Vallecillo, Specifying the ODP information viewpoint using Maude, in: H. Kilov, K. Baclawski (Eds.), Proceedings of the Tenth OOPSLA Workshop on Behavioral Semantics, Tampa Bay, Florida, pp. 44–57.
- [780] F. Durán, A. Vallecillo, Writing ODP enterprise specifications in Maude, in: J.A.M. Cordeiro, H. Kilov (Eds.), Open Distribute Processing: Enterprise, Computation, Knowledge, Engineering and Realisation, Proceedings of the 1st International Workshop on Open Distribute Processing: Enterprise, Computation, Knowledge, Engineering and Realisation, WOODPECKER 2001, In conjunction with ICEIS 2001, Setúbal, Portugal, July 6, 2001, ICEIS Press, 2001, pp. 55–68.
- [781] F. Durán, A. Vallecillo, Formalizing ODP enterprise specifications in Maude, Computer Standards & Interfaces 25 (2003) 83–102.
- [782] M. Egea, ITP/OCL: A Theorem Prover-Based Tool for UML+OCL Class Diagrams, Master's thesis, Departamento de Sistemas Informáticos y Programación, Universidad Complutense de Madrid, 2005.
- [783] M. Egea, An Executable Formal Semantics for OCL with Applications to Formal Analysis and Validation, Ph.D. thesis, Departamento de Sistemas Informáticos y Computación, Universidad Complutense de Madrid, 2008.
- [784] M. Egea, V. Rusu, Formal executable semantics for conformance in the MDE framework, Innovations in Systems and Software Engineering 6 (2009) 73–81.
- [785] J.L. Fernández Alemán, J.A. Toval Álvarez, Can intuition become rigorous? Foundations for UML model verification tools, in: F.M. Titsworth (Ed.), Proceedings of the 11th International Symposium on Software Reliability Engineering, ISSRE 2000, San Jose, CA, USA, October 8–11, 2000, IEEE Computer Society, 2000, pp. 344–355.
- [786] J.L. Fernández Alemán, J.A. Toval Álvarez, Seamless formalizing the UML semantics through metamodels, in: K. Siau, T. Halpin (Eds.), Unified Modeling Language: Systems Analysis, Design and Development Issues, Idea Group Global, 2001, pp. 224–248.
- [787] A. Gómez, A. Boronat, L. Hoyos, J.A. Carsí, I. Ramos, Definición de operaciones complejas con un lenguaje específico de dominio en gestión de modelos, in: J.C.R. Santos, P. Botella (Eds.), Actas de las XI Jornadas de Ingeniería del Software y Bases de Datos, JISBD 2006, Sitges, Barcelona, España, Octubre 3–6, 2006, pp. 215–224.
- [788] S. Iida, M. Matsumoto, R. Diaconescu, K. Futatsugi, D. Lucanu, Concurrent Object Composition in CafeOBJ, Technical Report JAIST IS-RR-98-0009S, Japan Advanced Institute of Science and Technology, 1998.
- [789] C. Jerad, K. Barkaoui, On the use of rewriting logic for verification of distributed software architecture description based LfP, in: P. Athanas, B. Michael (Eds.), Proceedings of the 16th IEEE International Workshop on Rapid System Prototyping, RSP 2005, Montreal, Canada, June 8–10, 2005, IEEE Computer Society, 2005, pp. 202–208.

- [790] C. Jerad, K. Barkaoui, A. Grissa-Touzi, **Hierarchical verification in Maude of LfP software architectures**, in: F. Oquendo (Ed.), Software Architecture, First European Conference, ECSA 2007, Aranjuez, Spain, September 24–26, 2007, Proceedings, volume 4758 of *Lecture Notes in Computer Science*, Springer, 2007, pp. 156–170.
- [791] E.B. Johnsen, O. Owe, R. Schlatte, S.L.T. Tarifa, **Dynamic resource reallocation between deployment components**, in: [1040], pp. 646–661.
- [792] M. Kim, M.O. Stehr, C.L. Talcott, N.D. Dutt, N. Venkatasubramanian, **A probabilistic formal analysis approach to cross layer optimization in distributed embedded systems**, in: [1029], pp. 285–300.
- [793] M. Kim, M.O. Stehr, C.L. Talcott, N. Dutt, N. Venkatasubramanian, **A formal methodology for compositional cross-layer optimization**, in: [1015], pp. 207–222.
- [794] A. Knapp, **Generating rewrite theories from UML collaborations**, in: [1055], pp. 97–120.
- [795] A. Knapp, **A Formal Approach to Object-Oriented Software Engineering**, Shaker Verlag, Aachen, Germany, 2001. PhD thesis, Institut für Informatik, Universität München, 2000.
- [796] U. Lechner, **Object-oriented specifications of distributed systems in the μ -calculus and Maude**, in: [1086], pp. 385–404.
- [797] U. Lechner, **Object-Oriented Specification of Distributed Systems**, Ph.D. thesis, Fakultät für Mathematik und Informatik, Universität Passau, 1997.
- [798] U. Lechner, **Object-oriented specification of distributed systems**, in: [1071], pp. 394–403.
- [799] U. Lechner, C. Lengauer, F. Nickl, M. Wirsing, **(Objects + concurrency) & reusability – A proposal to circumvent the inheritance anomaly**, in: P. Cointe (Ed.), *ECOP'96 – Object-Oriented Programming*, 10th European Conference, Linz, Austria, July 8–12, 1996, Proceedings, volume 1098 of *Lecture Notes in Computer Science*, Springer, 1996, pp. 232–247.
- [800] U. Lechner, C. Lengauer, M. Wirsing, **An object-oriented airport: Specification and refinement in Maude**, in: [1021], pp. 351–367.
- [801] D. Lucanu, **Algebraic specification of object aggregation – An event oriented approach**, in: [1053], pp. 115–132.
- [802] J. Meseguer, K. Futatsugi, T. Winkler, **Using rewriting logic to specify, program, integrate, and reuse open concurrent systems of cooperating agents**, in: Proceedings of the 1992 International Symposium on New Models for Software Architecture, Tokyo, Japan, November 1992, Research Institute of Software Engineering, 1992, pp. 61–106.
- [803] F. Mokhati, M. Badri, **Generating Maude specifications from UML use case diagrams**, *Journal of Object Technology* 8 (2009) 319–336.
- [804] F. Mokhati, M. Badri, P. Gagnon, **Translating UML diagrams into Maude formal specifications: A systematic approach**, in: K. Zhang, G. Spanoudakis, G. Visaggio (Eds.), Proceedings of the Eighteenth International Conference on Software Engineering & Knowledge Engineering, SEKE 2006, San Francisco, CA, USA, July 5–7, 2006, pp. 572–577.
- [805] F. Mokhati, N. Boudiaf, M. Badri, L. Badri, **Translating AUML diagrams into Maude specifications: A formal verification of agents interaction protocols**, *Journal of Object Technology* 6 (2007).
- [806] F. Mokhati, P. Gagnon, M. Badri, **Verifying UML diagrams with model checking: A rewriting logic based approach**, in: A. Mathur, W.E. Wong (Eds.), Proceedings of the Seventh International Conference on Quality Software, QSI 2007, Portland, Oregon, USA, October 11–12, 2007, IEEE Computer Society, 2007, pp. 356–362.
- [807] T. Mühlbauer, **Formal Specification and Analysis of Cloud Computing Management**, Master's thesis, Ludwig Maximilians Universität München, Technische Universität München, Universität Augsburg, Germany, 2011.
- [808] S. Nakajima, **Using algebraic specification techniques in development of object-oriented frameworks**, in: [1109], pp. 1664–1683.
- [809] K. Ogata, K. Futatsugi, **Formal verification of the MCS list-based queuing lock**, in: P.S. Thiagarajan, R.H.C. Yap (Eds.), Advances in Computing Science – ASIAN'99, 5th Asian Computing Science Conference, Phuket, Thailand, December 10–12, 1999, Proceedings, volume 1742 of *Lecture Notes in Computer Science*, Springer, 1999, pp. 281–293.
- [810] K. Ogata, K. Futatsugi, **Specification and verification of some classical mutual exclusion algorithms with CafeOBJ**, in: [1053], pp. 159–178.
- [811] K. Ogata, K. Futatsugi, **Specifying and verifying a railroad crossing with CafeOBJ**, in: V. Kumar (Ed.), Proceedings of the 15th International Parallel & Distributed Processing Symposium, IPDPS 2001, San Francisco, CA, USA, April 23–27, 2001, IEEE Computer Society, 2001, p. 150.
- [812] P. Queralt, L. Hoyos, A. Boronat, J.A. Carsí, **Un motor de transformación de modelos con soporte para el lenguaje QVT Relations**, in: A. Vallecillo, V. Pelechano, A. Estévez (Eds.), Actas del Taller sobre Desarrollo de Software Dirigido por Modelos. MDA y Aplicaciones. Sitges, España, Octubre 3, 2006, volume 227 of *CEUR Workshop Proceedings*, CEUR-WS.org, 2007.
- [813] A. Rademaker, C. Braga, A. Sztajnberg, **A rewriting semantics for a software architecture description language**, in: A. Mota, A. Moura (Eds.), Proceedings of the Brazilian Symposium on Formal Methods, SBMF 2004, Recife, Pernambuco, Brazil, November 29–December 1, 2004, volume 130 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2005, pp. 345–377.
- [814] A. Riesco, A. Verdejo, **Distributed applications implemented in Maude with parameterized skeletons**, in: [1029], pp. 91–106.
- [815] A. Riesco, A. Verdejo, **Parameterized skeletons in Maude**, Technical Report SIC-1/07, Dpto. Sistemas Informáticos y Computación, Universidad Complutense de Madrid, 2007.
- [816] J.E. Rivera, F. Durán, A. Vallecillo, **Formal specification and analysis of domain specific models using Maude**, *Simulation: Transactions of the Society for Modeling and Simulation International* 85 (2009) 778–792.
- [817] J.E. Rivera, F. Durán, A. Vallecillo, **A graphical approach for modeling time-dependent behavior of DSLs**, in: R. DeLine, M. Minas (Eds.), Proceedings of the IEEE Symposium on Visual Languages and Human-Centric Computing, VL/HCC 2009, Corvallis, OR, USA, September 20–24, 2009, IEEE, 2009, pp. 51–55.
- [818] J.E. Rivera, E. Guerra, J. de Lara, A. Vallecillo, **Analyzing rule-based behavioral semantics of visual modeling languages with Maude**, in: D. Gasevic, R. Lämmel, E.V. Wyk (Eds.), *Software Language Engineering, First International Conference, SLE 2008*, Toulouse, France, September 29–30, 2008. Revised Selected Papers, volume 5452 of *Lecture Notes in Computer Science*, Springer, 2009, pp. 54–73.
- [819] J.E. Rivera, A. Vallecillo, **Adding behavior to models**, in: M. Spies, M.B. Blake (Eds.), Proceedings of the 11th IEEE International Enterprise Distributed Object Computing Conference, EDOC 2007, Annapolis, Maryland, USA, October 15–19, 2007, IEEE Computer Society, 2007, pp. 169–180.
- [820] J.E. Rivera, A. Vallecillo, **Representing and operating with model differences**, in: R.F. Paige, B. Meyer (Eds.), *Objects, Components, Models and Patterns*, 46th International Conference, TOOLS EUROPE 2008, Zurich, Switzerland, June 30–July 4, 2008, Proceedings, volume 11 of *Lecture Notes in Business Information Processing*, Springer, 2008, pp. 141–160.
- [821] M. Roldán Castro, **Estrategias para el Guiado y la Monitorización de la Ejecución de Sistemas en Maude**, Ph.D. thesis, Universidad de Málaga, Spain, 2011.
- [822] J.R. Romero, J.E. Rivera, F. Durán, A. Vallecillo, **Formal and tool support for model driven engineering with Maude**, *Journal of Object Technology* 6 (2007) 187–207.
- [823] J.R. Romero, A. Vallecillo, **Formalizing ODP computational viewpoint specifications in Maude**, in: D.H. Akehurst, M. van Sinderen (Eds.), Proceedings of the 8th International Enterprise Distributed Object Computing Conference, EDOC 2004, Monterey, California, USA, September 20–24, 2004, IEEE Computer Society, 2004, pp. 212–223.
- [824] J.R. Romero, A. Vallecillo, F. Durán, **Writing and executing ODP computational viewpoint specifications using Maude**, *Computer Standards & Interfaces* 29 (2007) 481–498.
- [825] B. Salmansberger, **Objektorientierte Spezifikation von verteilten Systemen in Maude am Beispiel eines Flughafens**, Master's thesis, Fakultät für Mathematik und Informatik, Universität Passau, 1993.
- [826] M. Sánchez, J.L. Herrero, J.M. Murillo, J.H. Núñez, **Guaranteeing coherent software systems when composing coordinated components**, in: A. Porto, G.C. Roman (Eds.), *Coordination Languages and Models*, 4th International Conference, COORDINATION 2000, Limassol, Cyprus, September 11–13, 2000, Proceedings, volume 1906 of *Lecture Notes in Computer Science*, Springer, 2000, pp. 341–346.
- [827] L. Sha, J. Meseguer, **Design of complex cyber physical systems with formalized architectural patterns**, in: [1110], pp. 92–100.
- [828] D.B. Skillicorn, D. Talia, **Models and languages for parallel computation**, *ACM Computing Surveys* 30 (1998) 123–169.
- [829] C.L. Talcott, **Composable semantic models for actor theories**, in: M. Abadi, T. Ito (Eds.), *Theoretical Aspects of Computer Software*, Third International Symposium, TACS '97, Sendai, Japan, September 23–26, 1997, Proceedings, volume 1281 of *Lecture Notes in Computer Science*, Springer, 1997, pp. 321–364.
- [830] C.L. Talcott, **Composable semantic models for actor theories**, *Higher-Order and Symbolic Computation* 11 (1998) 281–343.
- [831] C.L. Talcott, **Towards a toolkit for actor system specification**, in: [1100], pp. 391–406.

- [832] C.L. Talcott, **Policy-based coordination in PAGODA: A case study**, in: G. Boella, M. Dastani, A. Omicini, L. van der Torre, I. Cerna, I. Linden (Eds.), Combined Proceedings of the Second International Workshop on Coordination and Organization, CoOrg 2006, and the Second International Workshop on Methods and Tools for Coordinating Concurrent, Distributed and Mobile Systems, MTCoord 2006, Bologna, Italy, June 13, 2006, volume 181 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2007, pp. 97–112.
- [833] J.A. Toval Álvarez, J.L. Fernández, **Improving system reliability via rigorous software modeling: The UML case**, in: Proceedings of the IEEE Aerospace Conference, Volume 6, Big Sky, MT, USA, March 10–17, 2001, IEEE Press, 2001, pp. 6–17.
- [834] J.A. Toval Álvarez, J.L. Fernández Alemán, **Formally modeling UML and its evolution: A holistic approach**, in: [1104], pp. 183–206.
- [835] J. Troya, J.E. Rivera, A. Vallecillo, **On the specification of non-functional properties of systems by observation**, in: S. Ghosh (Ed.), Models in Software Engineering, Workshops and Symposia at MODELS 2009, Denver, CO, USA, October 4–9, 2009, Reports and Revised Selected Papers, volume 6002 of *Lecture Notes in Computer Science*, Springer, 2010, pp. 296–309.
- [836] J. Troya, J.E. Rivera, A. Vallecillo, **Simulating domain specific visual models by observation**, in: M.K. Traoré (Ed.), Proceedings of the Symposium on Theory of Modeling and Simulation, DEVS 2010, Orlando, FL, USA, April 11–15, 2010, pp. 44–57.
- [837] J. Troya, A. Vallecillo, **Towards a rewriting logic semantics for ATL**, in: L. Tratt, M. Gogolla (Eds.), Theory and Practice of Model Transformations, Third International Conference, ICMT 2010, Malaga, Spain, June 28–July 2, 2010. Proceedings, volume 6142 of *Lecture Notes in Computer Science*, Springer, 2010, pp. 230–244.
- [838] M. Wirsing, A. Knapp, **A formal approach to object-oriented software engineering**, in: [1086], pp. 322–360.
- [839] M. Wirsing, A. Knapp, **A formal approach to object-oriented software engineering**, *Theoretical Computer Science* 285 (2002) 519–560.

6.4. Software/hardware modeling and verification

This is the most general area, since it goes without saying that the vast majority of computational specifications will be developed for software or hardware systems of various kinds. Furthermore, the areas considered in previous sections on networks, real-time systems, and distributed components can very well be viewed as subareas of the present one, but their special and distinctive characteristics justify a separated treatment. This makes the collection of papers compiled here a miscellaneous assortment of rewriting logic applications, including web modeling and verification, prototyping of reconfigurable systems, static checking of units, asynchronous hardware designs, protocols for electronic payments, test-case generation, soft constraints, and certification of domain-specific properties, among many others.

- [840] G. Aguilera, P. Cordero, M. Enciso, A. Mora, I.P. de Guzmán, **A non-explosive treatment of functional dependencies using rewriting logic**, in: A.L.C. Bazzan, S. Labidi (Eds.), Advances in Artificial Intelligence – SBIA 2004, 17th Brazilian Symposium on Artificial Intelligence, São Luis, Maranhão, Brazil, September 29–October 1, 2004. Proceedings, volume 3171 of *Lecture Notes in Computer Science*, Springer, 2004, pp. 31–40.
- [841] W. Ahrendt, A. Roth, R. Sasse, **Automatic validation of transformation rules for Java verification against a rewriting semantics**, in: [1105], pp. 412–426.
- [842] S. Alexander, **Conceptual data modeling with constraints in Maude**, in: [1087], pp. 96–110.
- [843] M. Alpuente, D. Ballis, J. Espert, D. Romero, **Model-checking web applications with Web-TLR**, in: A. Bouajjani, W.N. Chin (Eds.), Automated Technology for Verification and Analysis – 8th International Symposium, ATVA 2010, Singapore, September 21–24, 2010. Proceedings, volume 6252 of *Lecture Notes in Computer Science*, Springer, 2010, pp. 341–346.
- [844] M. Alpuente, D. Ballis, D. Romero, **Specification and verification of web applications in rewriting logic**, in: A. Cavalcanti, D. Dams (Eds.), FM 2009: Formal Methods, Second World Congress, Eindhoven, The Netherlands, November 2–6, 2009. Proceedings, volume 5850 of *Lecture Notes in Computer Science*, Springer, 2009, pp. 790–805.
- [845] M. Alpuente, D. Romero, **A visual technique for web pages comparison**, in: D. Ballis, S. Escobar (Eds.), Proceedings of the 4th International Workshop on Automated Specification and Verification of Web Systems, WWV 2008, Siena, Italy, July 4, 2008, volume 235 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2009, pp. 3–18.
- [846] M. Alpuente, D. Romero, **A tool for computing the visual similarity of web pages**, in: M. Nakamura, J. Voas, J. Choe (Eds.), 10th International Symposium on Applications and the Internet, SAINT 2010, Seoul, Korea, July 19–23, 2010. Proceedings, IEEE Computer Society, 2010, pp. 45–51.
- [847] C. Artho, D. Drusinsky, A. Goldberg, K. Havelund, M.R. Lowry, C.S. Pasareanu, G. Roşu, W. Visser, **Experiments with test case generation and runtime analysis**, in: E. Börger, A. Gargantini, E. Riccobene (Eds.), Abstract State Machines, Advances in Theory and Practice, 10th International Workshop, ASM 2003, Taormina, Italy, March 3–7, 2003. Proceedings, volume 2589 of *Lecture Notes in Computer Science*, Springer, 2003, pp. 87–107.
- [848] M. Ayala-Rincón, R.W. Hartenstein, R.M. Neto, R.P. Jacobi, C.H. Llanos, **Architectural specification, exploration and simulation through rewriting-logic**, *Revista Colombiana de Computación* 3 (2002).
- [849] M. Ayala-Rincón, R.P. Jacobi, L.G.A. Carvalho, C.H. Llanos, R.W. Hartenstein, **Modeling and prototyping dynamically reconfigurable systems for efficient computation of dynamic programming methods by rewriting-logic**, in: E.N. da Silva Barros, F.R. Wagner, L. Carro, F.J. Rammig (Eds.), Proceedings of the 17th Annual Symposium on Integrated Circuits and Systems Design, SBCCI 2004, Pernambuco, Brazil, September 7–11, 2004, ACM, 2004, pp. 248–253.
- [850] M. Ayala-Rincón, R.P. Jacobi, C.H. Llanos, R.W. Hartenstein, **Using rewriting-logic notation for functional verification in data-stream based reconfigurable computing**, in: E. Villar, A. Vachoux, P. van der Putten, W. Müller (Eds.), Forum on Specification and Design Languages, FDL 2003, Frankfurt, Germany, September 23–26, 2003. Proceedings, ECSI, 2003, pp. 492–504.
- [851] M. Ayala-Rincón, C.H. Llanos, R.P. Jacobi, R.W. Hartenstein, **Prototyping time- and space-efficient computations of algebraic operations over dynamically reconfigurable systems modeled by rewriting logic**, *ACM Transactions on Design Automation of Electronic Systems* 11 (2006) 251–281.
- [852] M. Ayala-Rincón, R.M. Neto, R.P. Jacobi, C.H. Llanos, R.W. Hartenstein, **Applying ELAN strategies in simulating processors over simple architectures**, in: B. Gramlich, S. Lucas (Eds.), Proceedings of the Second International Workshop on Reduction Strategies in Rewriting and Programming, WRS 2002, Copenhagen, Denmark, July 21, 2002, volume 70(6) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2002, pp. 84–99.
- [853] M. Ayala-Rincón, R.B. Nogueira, C.H. Llanos, R.P. Jacobi, R.W. Hartenstein, **Efficient computation of algebraic operations over dynamically reconfigurable systems specified by rewriting-logic environments**, in: A. Rodríguez (Ed.), Proceedings of the 23rd International Conference of the Chilean Computer Science Society, SCCC 2003, Chillan, Chile, November 6–7, 2003, IEEE Computer Society, 2003, pp. 60–69.
- [854] M. Ayala-Rincón, R.B. Nogueira, C.H. Llanos, R.P. Jacobi, R.W. Hartenstein, **Modeling a reconfigurable system for computing the FFT in place via rewriting logic**, in: R. Bergamaschi, W. Rosenstiel (Eds.), Proceedings of the 16th Annual Symposium on Integrated Circuits and Systems Design, SBCCI 2003, Sao Paulo, Brazil, September 8–11, 2003, IEEE Computer Society, 2003, pp. 205–210.
- [855] M. van den Brand, P. Klint, C. Verhoef, **Term rewriting for sale**, in: [1071], pp. 218–241.
- [856] F. Chen, G. Roşu, R.P. Venkatesan, **Rule-based analysis of dimensional safety**, in: [1090], pp. 197–207.
- [857] D. Elenius, G. Denker, M.O. Stehr, **A semantic web reasoner for rules, equations and constraints**, in: D. Calvanese, G. Lausen (Eds.), Web Reasoning and Rule Systems, Second International Conference, RR 2008, Karlsruhe, Germany, October 31–November 1, 2008. Proceedings, volume 5341 of *Lecture Notes in Computer Science*, Springer, 2008, pp. 135–149.
- [858] B. Fischer, G. Roşu, **Interpreting abstract interpretations in membership equational logic**, in: [1030], pp. 271–285.
- [859] S. Flores, S. Lucas, A. Villanueva, **Formal verification of websites**, in: [1026], pp. 103–118.
- [860] K. Futatsugi, **Verifying specifications with proof scores in CafeOBJ**, in: S. Uchitel, S. Easterbrook (Eds.), Proceedings of the 21st IEEE/ACM International Conference on Automated Software Engineering, ASE 2006, Tokyo, Japan, September 18–22, 2006, IEEE Computer Society, 2006, pp. 3–10.

- [861] K. Futatsugi, *Fostering proof scores in CafeOBJ*, in: [1040], pp. 1–20.
- [862] A. Garrido, J. Meseguer, *Formal specification and verification of Java refactorings*, in: Proceedings of the Sixth IEEE International Workshop on Source Code Analysis and Manipulation, SCAM 2006, Philadelphia, Pennsylvania, September 27–29, 2006, IEEE, 2006, pp. 165–174.
- [863] A. Garrido, J. Meseguer, R. Johnson, *Algebraic semantics of the C preprocessor and correctness of its refactorings*, Technical Report UIUCDCS-R-2006-2688, Department of Computer Science, University of Illinois at Urbana-Champaign, 2006.
- [864] N.A. Harman, *Correctness and Verification of Hardware Systems Using Maude*, Technical Report 3-2000, Department of Computer Science, University of Wales Swansea, 2000.
- [865] N.A. Harman, *Verifying a simple pipelined microprocessor using Maude*, in: M. Cerioli, G. Reggio (Eds.), Recent Trends in Algebraic Development Techniques, 15th International Workshop, WADT 2001, Joint with the CoFI WG Meeting, Genova, Italy, April 1–3, 2001, Selected Papers, volume 2267 of *Lecture Notes in Computer Science*, Springer, 2001, pp. 128–151.
- [866] M. Hills, F. Chen, G. Roşu, *A rewriting logic approach to static checking of units of measurement in C*, in: [1073], pp. 76–91.
- [867] M.M. Hölzl, M. Meier, M. Wirsing, *Which soft constraints do you prefer?*, in: [1099], pp. 189–205.
- [868] E.B. Johnsen, O. Owe, A.B. Torjusen, *Validating behavioral component interfaces in rewriting logic*, in: [1020], pp. 187–204.
- [869] E.B. Johnsen, O. Owe, A.B. Torjusen, *Validating behavioral component interfaces in rewriting logic*, *Fundamenta Informaticae* 82 (2008) 341–359.
- [870] M.K. Katelman, *A Meta-Language for Functional Verification*, Ph.D. thesis, Department of Computer Science, University of Illinois at Urbana-Champaign, 2011.
- [871] M. Katelman, S. Keller, J. Meseguer, *Concurrent rewriting semantics and analysis of asynchronous digital circuits*, in: [1093], pp. 140–156.
- [872] M. Katelman, S. Keller, J. Meseguer, *Rewriting semantics of production rule sets*, *Journal of Logic and Algebraic Programming* (2012). This volume.
- [873] M. Katelman, J. Meseguer, *vlogs: A strategy language for simulation-based verification of hardware*, in: S. Barner, I.G. Harris, D. Kroening, O. Raz (Eds.), *Hardware and Software: Verification and Testing – 6th International Haifa Verification Conference, HVC 2010, Haifa, Israel, October 4–7, 2010*, Proceeding Selected Papers, volume 6504 of *Lecture Notes in Computer Science*, Springer, 2011, pp. 129–145.
- [874] M. Katelman, J. Meseguer, S. Escobar, *Directed-logical testing for functional verification of microprocessors*, in: S.A. Edwards, K. Schneider (Eds.), Proceedings of the 6th ACM & IEEE International Conference on Formal Methods and Models for Co-Design, MEMOCODE 2008, Anaheim, CA, USA, June 5–7, 2008, IEEE Computer Society, 2008, pp. 89–100.
- [875] W. Kong, K. Ogata, K. Futatsugi, *Algebraic approaches to formal analysis of the mondex electronic purse system*, in: J. Davies, J. Gibbons (Eds.), *Integrated Formal Methods, 6th International Conference, IFM 2007, Oxford, UK, July 2–5, 2007*, Proceedings, volume 4591 of *Lecture Notes in Computer Science*, Springer, 2007, pp. 393–412.
- [876] H. Kuruma, K. Futatsugi, *Incremental specification based on the combination of data types*, in: [1053], pp. 95–114.
- [877] C. Landauer, *Discrete event systems in rewriting logic*, in: [1086], pp. 310–321.
- [878] U. Lechner, C. Lengauer, *Modal- μ -Maude: Specification and properties of concurrent objects*, in: B. Freitag, C.B. Jones, C. Lengauer, H.J. Schek (Eds.), *Object Orientation with Parallelism and Persistence*, Kluwer Academic Publishers, 1995, pp. 43–62.
- [879] L. Leuştean, G. Roşu, *Certifying Kalman Filters*, Technical Report RIACS 03.02, Research Institute for Advanced Computer Science, 2003.
- [880] M.R. Lowry, T. Pressburger, G. Roşu, *Certifying domain-specific policies*, in: [1044], pp. 81–90.
- [881] S. Lucas, *Rewriting-based navigation of web sites: Looking for models and logics*, in: M. Alpuente, S. Escobar, M. Falaschi (Eds.), Proceedings of the International Workshop on Automated Specification and Verification of Web Sites, WWWV 2005, Valencia, Spain, March 14–15, 2005, volume 157(2) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2006, pp. 79–85.
- [882] J. Meseguer, *Software specification and verification in rewriting logic*, in: M. Broy, M. Pizka (Eds.), Proceedings of the NATO Advanced Study Institute on Models, Algebras and Logic of Engineering Software, Held in Marktberdorf, Germany, 30 July–11 August, 2002, volume 191 of *NATO Science Series III: Computer and Systems Sciences*, IOS Press, 2003, pp. 133–193.
- [883] A. Mori, K. Futatsugi, *CafeOBJ as a tool for behavioral system verification*, in: [1091], pp. 461–470.
- [884] C. Morra, *Configware design space exploration using rewriting logic*, in: [1078], pp. 1–2.
- [885] C. Morra, J. Becker, M. Ayala-Rincón, R.W. Hartenstein, *FELIX: Using rewriting-logic for generating functionally equivalent implementations*, in: T. Rissa, S.J.E. Wilton, P.H.W. Leong (Eds.), Proceedings of the 2005 International Conference on Field Programmable Logic and Applications, FPL 2005, Tampere, Finland, August 24–26, 2005, IEEE, 2005, pp. 25–30.
- [886] C. Morra, J. Bispo, J.M.P. Cardoso, J. Becker, *Combining rewriting-logic, architecture generation, and simulation to exploit coarse-grained reconfigurable architectures*, in: K.L. Pocek, D.A. Buell (Eds.), Proceedings of the 16th IEEE International Symposium on Field-Programmable Custom Computing Machines, FCCM 2008, Stanford, Palo Alto, California, USA, April 14–15, 2008, IEEE Computer Society, 2008, pp. 320–321.
- [887] C. Morra, J.M.P. Cardoso, J. Becker, *Using rewriting logic to match patterns of instructions from a compiler intermediate form to coarse-grained processing elements*, in: [1095], pp. 1–8.
- [888] C. Morra, J.M.P. Cardoso, J. Bispo, J. Becker, *Retargeting, evaluating, and generating reconfigurable array-based architectures*, in: A. Orailoglu, L. Pozzi (Eds.), Proceedings of the 6th IEEE Symposium on Application Specific Processors, SASP 2008, held in conjunction with the DAC 2008, Anaheim, California, USA, June 8–9, 2008, IEEE, 2008, pp. 34–41.
- [889] C. Morra, M. Sackmann, J. Becker, R.W. Hartenstein, *Using rewriting logic to generate different implementations of polynomial approximations in coarse-grained architectures*, in: G. Sassatelli, L.S. Indrusiak, M. Glesner, L. Torres (Eds.), Proceedings of the 2nd International Workshop on Reconfigurable Communication-centric Systems-on-Chip, ReCoSoC 2006, Montpellier, France, July 3–5, 2006, Univ. Montpellier II, 2006, pp. 46–51.
- [890] C. Morra, M. Sackmann, S. Shukla, J. Becker, R.W. Hartenstein, *From equation to VHDL: Using rewriting logic for automated function generation*, in: [1078], pp. 1–4.
- [891] S. Nakajima, K. Futatsugi, *An object-oriented modeling method for algebraic specifications in CafeOBJ*, in: Proceedings of the 19th International Conference on Software Engineering, ICSE'97, Boston, Massachusetts, May 17–23, 1997, ACM Press, 1997.
- [892] M. Nakamura, W. Kong, K. Ogata, K. Futatsugi, *A specification translation from behavioral specifications to rewrite specifications*, *IEICE Transactions on Information and Systems* 91-D (2008) 1492–1503.
- [893] M. Nakamura, T. Seino, *Generating test cases for invariant properties from proof scores in the OTS/CafeOBJ method*, *IEICE Transactions on Information and Systems* 92-D (2009) 1012–1021.
- [894] K. Ogata, K. Futatsugi, *Flaw and modification of the iKP electronic payment protocols*, *Information Processing Letters* 86 (2003) 57–62.
- [895] K. Ogata, K. Futatsugi, *Formal analysis of the iKP electronic payment protocols*, in: [1091], pp. 441–460.
- [896] K. Ogata, K. Futatsugi, *Formal verification of the Horn-Preneel micropayment protocol*, in: L.D. Zuck, P.C. Attie, A. Cortesi, S. Mukhopadhyay (Eds.), *Verification, Model Checking, and Abstract Interpretation, 4th International Conference, VMCAI 2003, New York, NY, USA, January 9–11, 2002*, Proceedings, volume 2575 of *Lecture Notes in Computer Science*, Springer, 2003, pp. 238–252.
- [897] K. Ogata, K. Futatsugi, *Proof scores in the OTS/CafeOBJ method*, in: [1089], pp. 170–184.
- [898] K. Ogata, K. Futatsugi, *Equational approach to formal verification of SET*, in: H.D. Ehrich, K.D. Schewe (Eds.), Proceedings of the 4th International Conference on Quality Software, QSI 2004, Braunschweig, Germany, September 8–10, 2004, IEEE Computer Society, 2004, pp. 50–59.
- [899] K. Ogata, K. Futatsugi, *Formal analysis of the NetBill electronic commerce protocol*, in: K. Futatsugi, F. Mizoguchi, N. Yonezaki (Eds.), *Software Security – Theories and Systems, Second Mext-NSF-JSPS International Symposium, ISSS 2003, Tokyo, Japan, November 4–6, 2003*, Revised Papers, volume 3233 of *Lecture Notes in Computer Science*, Springer, 2004, pp. 45–64.
- [900] K. Ogata, K. Futatsugi, *Analysis of the Suzuki-Kasami algorithm with the Maude model checker*, in: J. Lee, P. Jalote, W.C.C. Chu (Eds.), Proceedings of the 12th Asia-Pacific Software Engineering Conference, APSEC 2005, Taipei, Taiwan, December 15–17, 2005, IEEE Computer Society, 2005, pp. 159–166.
- [901] K. Ogata, K. Futatsugi, *Some tips on writing proof scores in the OTS/CafeOBJ method*, in: [1054], pp. 596–615.
- [902] K. Ogata, K. Futatsugi, *Comparison of Maude and SAL by conducting case studies model checking a distributed algorithm*, *IEICE Transactions on Information and Systems* 90-A (2007) 1690–1703.

- [903] K. Ogata, K. Futatsugi, [Proof score approach to verification of liveness properties](#), IEICE Transactions on Information and Systems 91-D (2008) 2804–2817.
- [904] K. Ogata, K. Futatsugi, [Simulation-based verification for invariant properties in the OTS/CafeOBJ method](#), in: E. Boiten, J. Derrick, G. Smith (Eds.), Proceedings of the BCS-FACS Refinement Workshop, REFINÉ 2007, Oxford, UK, July 2, 2007, volume 201 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2008, pp. 127–154.
- [905] K. Ogata, M. Nakano, M. Nakamura, K. Futatsugi, [Chocolat/SMV: A translator from CafeOBJ into SMV](#), in: K. Nakano (Ed.), Proceedings of the Sixth International Conference on Parallel and Distributed Computing, Applications and Technologies, PDCAT 2005, Dalian, China, December 5–8, 2005, IEEE Computer Society, 2005, pp. 416–420.
- [906] M.L. Rebaiaia, M. Benmohamed, J.M. Jaam, A. Hasnah, [A rewriting logic-based computation and deduction approach to avoid reactive system malfunctions](#), in: H.R. Arabnia, Y. Mun (Eds.), Proceedings of the International Conference on Parallel and Distributed Processing Techniques and Applications, PDPTA '03, Las Vegas, Nevada, USA, June 23–26, 2003, Volume 2, CSREA Press, 2003, pp. 573–579.
- [907] A. Riesco, [Test-case generation for Maude functional modules](#), in: T. Mossakowski, H.J. Kreowski (Eds.), Recent Trends in Algebraic Development Techniques, 20th International Workshop, WADT 2010, Schloss Etelsen, Germany, July 1–4, 2010, Revised Selected Papers, volume 7137 of *Lecture Notes in Computer Science*, Springer, 2012, pp. 287–301.
- [908] A. Riesco, [Using narrowing to test Maude specifications](#), in: [1042]. To appear.
- [909] C. Rocha, J. Meseguer, [A rewriting decision procedure for Dijkstra-Scholten's syllogistic logic with complements](#), Revista Colombiana de Computación 8 (2007).
- [910] D. Romero, [Métodos Formales en la Verificación y Reparación de Sitios Web](#), Master's thesis, Departamento de Sistemas Informáticos y Computación, Universidad Politécnica de Valencia, 2007.
- [911] G. Roşu, F. Chen, [Certifying measurement unit safety policy](#), in: J. Grundy, J. Penix (Eds.), Proceedings of the 18th IEEE International Conference on Automated Software Engineering, ASE 2003, Montreal, Canada, October 6–10, 2003, IEEE Computer Society, 2003, pp. 304–309.
- [912] G. Roşu, R.P. Venkatesan, J. Whittle, L. Leustean, [Certifying optimality of state estimation programs](#), in: W.A.H. Jr., F. Somenzi (Eds.), Computer Aided Verification, 15th International Conference, CAV 2003, Boulder, CO, USA, July 8–12, 2003, Proceedings, volume 2725 of *Lecture Notes in Computer Science*, Springer, 2003, pp. 301–314.
- [913] G. Roşu, J. Whittle, [Towards certifying domain-specific properties of synthesized code](#), in: M. Leuschel, U. Ultes-Nitsche (Eds.), Proceedings of the Third International Workshop on Verification and Computational Logic, VCL 2002, Pittsburgh, PA, USA, October 5, 2002. Technical Report DSSE-TR-2002-5, Declarative Systems and Software Engineering Research Group, University of Southampton.
- [914] G. Roşu, J. Whittle, [Towards certifying domain-specific properties of synthesized code](#), in: W. Emmerich, D. Wile (Eds.), Proceedings of the 17th IEEE International Conference on Automated Software Engineering, ASE 2002, Edinburgh, Scotland, UK, September 23–27, 2002, IEEE Computer Society, 2002, pp. 289–294.
- [915] V. Rusu, [Combining theorem proving and narrowing for rewriting-logic specifications](#), in: G. Fraser, A. Gargantini (Eds.), Tests and Proofs, 4th International Conference, TAP 2010, Málaga, Spain, July 1–2, 2010. Proceedings, volume 6143 of *Lecture Notes in Computer Science*, Springer, 2010, pp. 135–150.
- [916] V. Rusu, M. Clavel, [Vérification d'invariants pour des systèmes spécifiés en logique de réécriture](#), in: A. Schmitt (Ed.), JFLA 2009, Vingtièmes Journées Francophones des Langages Applicatifs, Saint Quentin sur Isère, France, January 31–February 3, 2009, Proceedings, volume 7.2 of *Studia Informatica Universalis*, pp. 317–350.
- [917] P. Salverda, G. Roşu, C.B. Zilles, [Formally defining and verifying master/slave speculative parallelization](#), in: J. Fitzgerald, I.J. Hayes, A. Tarlecki (Eds.), FM 2005: Formal Methods, International Symposium of Formal Methods Europe, Newcastle, UK, July 18–22, 2005, Proceedings, volume 3582 of *Lecture Notes in Computer Science*, Springer, 2005, pp. 123–138.
- [918] T. Seino, K. Ogata, K. Futatsugi, [A toolkit for generating and displaying proof scores in the OTS/CafeOBJ method](#), in: [1034], pp. 57–72.
- [919] K. Sen, G. Roşu, [Generating optimal monitors for extended regular expressions](#), in: B. Cook, W. Visser (Eds.), Proceedings of the Run-time Verification Satellite Workshop of CAV 2003, RV 2003, Boulder, Colorado, USA, July 14, 2003, volume 89(2) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2003, pp. 226–245.
- [920] A. Vandin, A. Lluch-Lafuente, [Towards a Maude tool for model checking temporal graph properties](#), in: F. Gadducci, L. Mariani (Eds.), Proceedings of the 10th International Workshop on Graph Transformation and Visual Modeling Techniques, GT-VMT 2011, Saarbrücken, Germany, April 2–3, 2011, volume 41 of *Electronic Communications of the EASST*.
- [921] M. Wirsing, G. Denker, C.L. Talcott, A. Poggio, L. Briesemeister, [A rewriting logic framework for soft constraints](#), in: [1039], pp. 181–197.
- [922] J. Xiang, D. Bjørner, K. Futatsugi, [Formal digital license language with OTS/CafeOBJ method](#), in: A. Kamal (Ed.), Proceedings of the 6th ACS/IEEE International Conference on Computer Systems and Applications, AICCSA 2008, Doha, Qatar, March 31–April 4, 2008, IEEE, 2008, pp. 652–660.
- [923] J. Xiang, K. Futatsugi, Y. He, [Fault tree and formal methods in system safety analysis](#), in: H. Wang, Z. Peng, A. Kara (Eds.), Proceedings of the 2004 International Conference on Computer and Information Technology, CIT 2004, Wuhan, China, September 14–16, 2004, IEEE Computer Society, 2004, pp. 1108–1115.
- [924] M. Zhang, K. Ogata, M. Nakamura, [Specification translation of state machines from equational theories into rewrite theories](#), in: [1040], pp. 678–693.

6.5. Security

Rewriting logic and its associated languages have been successfully applied to analyze security properties, such as secrecy, authenticity, availability, non-interference, etc., of many systems, including cryptographic protocols, network security, browser security, access control, and code security. Maude has been used to define the semantics of the cryptographic protocol specification language CAPSL and of the MSR security specification formalism, providing in this way execution and formal analysis environments for them. Using narrowing as a semi-decision procedure for symbolic reachability analysis, the Maude-NPA protocol analysis tool has been developed as an updated and greatly improved version of the NRL Protocol Analyzer. Maude-NPA takes into account the algebraic properties of the protocol, such as Diffie-Hellman exponentiation and exclusive or, which an attacker could use to break the protocol. Work on network security includes dynamically adaptive secure group communication protocols as well as the design of sophisticated protocols resilient to denial-of-service (DoS) attacks. Maude has also provided the basis for a formal specification of internet browsers whose model checking uncovered new attack scenarios, and then to design secure browsers. Concerning access control, Tom has been used to generate Java monitoring code for access control policies, which are previously analyzed by means of narrowing-based techniques. Finally, rewriting logic semantics of programming languages (see Section 3.4) have been used to study security properties of code at different levels of abstraction.

- [925] M. Alba-Castro, [Abstract Certification of Java Programs in Rewriting Logic](#), Ph.D. thesis, Departamento de Sistemas Informáticos y Computación, Universidad Politécnica de Valencia, Spain, 2011.
- [926] M. Alba-Castro, M. Alpuente, S. Escobar, [Automatic certification of Java source code in rewriting logic](#), in: S. Leue, P. Merino (Eds.), Formal Methods for Industrial Critical Systems, 12th International Workshop, FMICS 2007, Berlin, Germany, July 1–2, 2007, Revised Selected Papers, volume 4916 of *Lecture Notes in Computer Science*, Springer, 2008, pp. 200–217.

- [927] M. Alba-Castro, M. Alpuente, S. Escobar, **Automated certification of non-interference in rewriting logic**, in: D.D. Cofer, A. Fantechi (Eds.), *Formal Methods for Industrial Critical Systems*, 13th International Workshop, FMICS 2008, L'Aquila, Italy, September 15–16, 2008, Revised Selected Papers, volume 5596 of *Lecture Notes in Computer Science*, Springer, 2009, pp. 182–198.
- [928] M. Alba-Castro, M. Alpuente, S. Escobar, **Abstract certification of global non-interference in rewriting logic**, in: M. Leuschel, S. Hallerstede, F. de Boer, M. Bonsangue (Eds.), *Formal Methods for Components and Objects*, 8th International Symposium, FMCO 2009, Eindhoven, The Netherlands, November 4–6, 2009, Revised Selected Papers, volume 6286 of *Lecture Notes in Computer Science*, Springer, 2010, pp. 105–124.
- [929] M. Alba-Castro, M. Alpuente, S. Escobar, **Approximating non-interference and erasure in rewriting logic**, in: [1067], pp. 124–132.
- [930] M. Alba-Castro, M. Alpuente, S. Escobar, **Confidentiality certification of source Java code in JavaPCC**, in: M. Leuschel (Ed.), *Proceedings of the 10th International Workshop on Automated Verification of Critical Systems, AVOCS 2010, Düsseldorf, September 20–23, 2010*, Electronic Communications of the EASST, EASST, 2010. To appear.
- [931] M. Alba-Castro, M. Alpuente, S. Escobar, P. Ojeda, D. Romero, **A tool for automated certification of Java source code in Maude**, in: [1016], pp. 19–29.
- [932] D. Basin, G. Denker, **Maude versus Haskell: An experimental comparison in security protocol analysis**, in: [1052], pp. 235–256.
- [933] I. Cervasato, M.O. Stehr, **Representing the MSR cryptoprotocol specification language in an extension of rewriting logic with dependent types**, in: [1085], pp. 183–207.
- [934] I. Cervasato, M.O. Stehr, **Representing the MSR cryptoprotocol specification language in an extension of rewriting logic with dependent types**, *Higher-Order and Symbolic Computation* 20 (2007) 3–35.
- [935] R. Chadha, C.A. Gunter, J. Meseguer, R. Shankes, M. Viswanathan, **Modular preservation of safety properties by cookie-based DoS-protection wrappers**, in: [1028], pp. 39–58.
- [936] S. Chen, J. Meseguer, R. Sasse, H.J. Wang, Y.M. Wang, **A systematic approach to uncover security flaws in GUI logic**, in: B. Pfizmann, P. McDaniel (Eds.), *Proceedings of the 2007 IEEE Symposium on Security and Privacy (S&P 2007)*, Oakland, California, USA, May 20–23, 2007, IEEE Computer Society, 2007, pp. 71–85.
- [937] S. Chen, K. Pattabiraman, Z. Kalbarczyk, R.K. Iyer, **Formal reasoning of various categories of widely exploited security vulnerabilities by pointer taintedness semantics**, in: Y. Deswarte, F. Cuppens, S. Jajodia, L. Wang (Eds.), *19th International Information Security Conference, SEC 2004*, Toulouse, France, August 22–27, 2004, Proceedings, Kluwer, 2004, pp. 83–100.
- [938] G. Denker, J. Millen, **CAPSL and CIL Language Design: A Common Authentication Protocol Specification Language and Its Intermediate Language**, Technical Report SRI-CSL-99-02, Computer Science Laboratory, SRI International, 1999.
- [939] G. Denker, J. Millen, **CAPSL intermediate language**, in: N. Heintze, E. Clarke (Eds.), *Proceedings of the Workshop on Formal Methods and Security Protocols, FMSP'99*, Trento, Italy, July 5, 1999.
- [940] G. Denker, J. Millen, **CAPSL integrated protocol environment**, in: [1074], pp. 207–222.
- [941] G. Denker, J. Millen, **The CAPSL Integrated Protocol Environment**, Technical Report SRI-CSL-2000-02, Computer Science Laboratory, SRI International, 2000.
- [942] D.J. Dougherty, C. Kirchner, H. Kirchner, A.S. de Oliveira, **Modular access control via strategic rewriting**, in: J. Biskup, J. Lopez (Eds.), *Computer Security – ESORICS 2007*, 12th European Symposium On Research In Computer Security, Dresden, Germany, September 24–26, 2007, Proceedings, volume 4734 of *Lecture Notes in Computer Science*, Springer, 2007, pp. 578–593.
- [943] J. Eckhardt, **A Formal Analysis of Security Properties in Cloud Computing**, Master's thesis, Ludwig Maximilians Universität München, Technische Universität München, Universität Augsburg, Germany, 2011.
- [944] J. Eckhardt, T. Mühlbauer, M. Alturki, J. Meseguer, M. Wirsing, **Stable availability under denial of service attacks through formal patterns**, in: [1076], pp. 78–93.
- [945] S. Escobar, J. Hendrix, C. Meadows, J. Meseguer, **Diffie-Hellman cryptographic reasoning in the Maude-NRL Protocol Analyzer**, in: M. Nesi, R. Treinen (Eds.), *Proceedings of the Second International Workshop on Security and Rewriting Techniques, SecReT 2007*, Paris, France, June 29, 2007.
- [946] S. Escobar, D. Kapur, C. Lynch, C. Meadows, J. Meseguer, P. Narendran, R. Sasse, **Protocol analysis in Maude-NPA using unification modulo homomorphic encryption**, in: [1101], pp. 65–76.
- [947] S. Escobar, C. Meadows, J. Meseguer, **A rewriting-based inference system for the NRL Protocol Analyzer: grammar generation**, in: V. Atluri, P. Samarati, R. Küsters, J.C. Mitchell (Eds.), *Proceedings of the 2005 ACM workshop on Formal methods in security engineering, FMSE 2005*, Fairfax, VA, USA, November 11, 2005, ACM, 2005, pp. 1–12.
- [948] S. Escobar, C. Meadows, J. Meseguer, **A rewriting-based inference system for the NRL Protocol Analyzer and its meta-logical properties**, *Theoretical Computer Science* 367 (2006) 162–202.
- [949] S. Escobar, C. Meadows, J. Meseguer, **Equational cryptographic reasoning in the Maude-NRL Protocol Analyzer**, in: [1045], pp. 23–36.
- [950] S. Escobar, C. Meadows, J. Meseguer, **State space reduction in the Maude-NRL Protocol Analyzer**, in: S. Jajodia, J. López (Eds.), *Computer Security – ESORICS 2008*, 13th European Symposium on Research in Computer Security, Málaga, Spain, October 6–8, 2008, Proceedings, volume 5283 of *Lecture Notes in Computer Science*, Springer, 2008, pp. 548–562.
- [951] S. Escobar, C. Meadows, J. Meseguer, **Maude-NPA: Cryptographic protocol analysis modulo equational properties**, in: A. Aldini, G. Barthe, R. Gorrieri (Eds.), *Foundations of Security Analysis and Design V, FOSAD 2007/2008/2009 Tutorial Lectures*, volume 5705 of *Lecture Notes in Computer Science*, Springer, 2009, pp. 1–50.
- [952] S. Escobar, C. Meadows, J. Meseguer, **State space reduction in the Maude-NRL protocol analyzer**, 2011.
- [953] S. Escobar, C. Meadows, J. Meseguer, S. Santiago, **Sequential protocol composition in Maude-NPA**, in: D. Gritzalis, B. Preneel, M. Theoharidou (Eds.), *Computer Security – ESORICS 2010*, 15th European Symposium on Research in Computer Security, Athens, Greece, September 20–22, 2010, Proceedings, volume 6345 of *Lecture Notes in Computer Science*, Springer, 2010, pp. 303–318.
- [954] S. Barker, M. Fernández, **Term rewriting for access control**, in: E. Damiani, P. Liu (Eds.), *Data and Applications Security XX, 20th Annual IFIP WG 11.3 Working Conference on Data and Applications Security*, Sophia Antipolis, France, July 31–August 2, 2006, Proceedings, volume 4127 of *Lecture Notes in Computer Science*, Springer, 2006, pp. 179–193.
- [955] A. Goodloe, M. Jacobs, G. Shah, C. Gunter, **L3A: A protocol for layer three accounting**, in: *Proceedings of the First Workshop on Secure Network Protocols, NPSEC 2005*, Boston, Massachusetts, November 6, 2005, IEEE Computer Society, 2005, pp. 1–6.
- [956] A. Goodloe, M. McDougall, C.A. Gunter, M.O. Stehr, **Design and Analysis of Sectrace: A Protocol to Set Up Security Associations**, Technical Report, CIS Department, University of Pennsylvania, 2004.
- [957] G. Grier, S. Tang, S.T. King, **Secure web browsing with the OP web browser**, in: 2008 IEEE Symposium on Security and Privacy (S&P 2008), Oakland, California, May 18–21, 2008, IEEE Computer Society, 2008, pp. 402–416.
- [958] S. Gutierrez-Nolasco, N. Venkatasubramanian, M.O. Stehr, C.L. Talcott, **Towards adaptive secure group communication: Bridging the gap between formal specification and network simulation**, in: G. Ciardo, Y.S. Dai (Eds.), *Proceedings of the 12th IEEE Pacific Rim International Symposium on Dependable Computing, PRDC 2006*, University of California, Riverside, USA, December 18–20, 2006, IEEE Computer Society, 2006, pp. 113–120.
- [959] S. Gutierrez-Nolasco, N. Venkatasubramanian, C.L. Talcott, **A semantic model for safe protocol interaction**, in: H. Haddad (Ed.), *Proceedings of the 2006 ACM Symposium on Applied Computing, SAC 2006*, Dijon, France, April 23–27, 2006, ACM, 2006, pp. 1599–1600.
- [960] C. Kirchner, H. Kirchner, A.S. de Oliveira, **Analysis of rewrite-based access control policies**, in: [1041], pp. 55–75.
- [961] W. Kong, K. Ogata, K. Futatsugi, **Specification and verification of workflows with RBAC mechanism and SoD constraints**, *International Journal of Software Engineering and Knowledge Engineering* 17 (2007) 3–32.
- [962] M. LeMay, C.A. Gunter, **Cumulative attestation kernels for embedded systems**, in: [1024], pp. 655–670.
- [963] J. Millen, **Applications of term rewriting to cryptographic protocol analysis**, in: [1052], pp. 229–234.
- [964] K. Ogata, K. Futatsugi, **Rewriting-based verification of authentication protocols**, in: [1056], pp. 208–222.
- [965] K. Ogata, K. Futatsugi, **Equational approach to formal analysis of TLS**, in: A. Arora (Ed.), *Proceedings of the 25th International Conference on Distributed Computing Systems, ICDCS 2005*, Columbus, OH, USA, June 6–10, 2005, IEEE Computer Society, 2005, pp. 795–804.

- [966] K. Ogata, K. Futatsugi, **A combination of forward and backward reachability analysis methods**, in: [1040], pp. 501–517.
- [967] A.S. de Oliveira, **Rewriting-based access control policies**, in: [1045], pp. 59–72.
- [968] A.S. de Oliveira, E.K. Wang, C. Kirchner, H. Kirchner, **Weaving rewrite-based access control policies**, in: P. Ning, V. Atluri, V.D. Gligor, H. Mantel (Eds.), *Proceedings of the 2007 ACM workshop on Formal methods in security engineering, FMSE 2007*, Fairfax, VA, USA, November 2, 2007, ACM, 2007, pp. 71–80.
- [969] A.S. de Oliveira, **Term Rewriting and Modularity for Security Policies**, Ph.D. thesis, Université Henri Poincaré, France, 2008.
- [970] K. Pattabiraman, N. Nakka, Z. Kalbarczyk, R.K. Iyer, **Discovering application-level insider attacks using symbolic execution**, in: D. Grzitalis, J. Lopez (Eds.), *Emerging Challenges for Security, Privacy and Trust, 24th IFIP TC 11 International Information Security Conference, SEC 2009*, Pafos, Cyprus, May 18–20, 2009, *Proceedings*, volume 297 of *IFIP Advances in Information and Communication Technology*, Springer, 2009, pp. 63–75.
- [971] S. Reich, **Implementing and Extending the MSR Crypto-Protocol Specification Language**, Master's thesis, Fachbereich Informatik, Universität Hamburg, 2006.
- [972] D.E. Rodríguez, **A secret-sharing protocol modelled in Maude**, in: [1056], pp. 223–239.
- [973] S. Santiago, C.L. Talcott, S. Escobar, C. Meadows, J. Meseguer, **A graphical user interface for Maude-NPA**, in: [1083], pp. 3–20.
- [974] R. Sasse, S. Escobar, C. Meadows, J. Meseguer, **Protocol analysis modulo combination of theories: A case study in Maude-NPA**, in: J. Cuéllar, J. Lopez, G. Barthe, A. Pretschner (Eds.), *Security and Trust Management – 6th International Workshop, STM 2010*, Athens, Greece, September 23–24, 2010, *Revised Selected Papers*, volume 6710 of *Lecture Notes in Computer Science*, Springer, 2011, pp. 163–178.
- [975] R. Shankes, M. Alturki, R. Sasse, C.A. Gunter, J. Meseguer, **Model-checking DoS amplification for VoIP session initiation**, in: [1024], pp. 390–405.
- [976] V. Shmatikov, C.L. Talcott, **Reputation-based trust management**, *Journal of Computer Security* 13 (2005) 167–190.
- [977] S. Tang, **Towards Secure Web Browsing**, Ph.D. thesis, Department of Computer Science, University of Illinois at Urbana-Champaign, 2011.
- [978] J. Xiang, K. Futatsugi, Y. He, **Formal fault tree construction and system safety analysis**, in: M.H. Hamza (Ed.), *Proceedings of the IASTED International Conference on Software Engineering*, part of the 22nd Multi-Conference on Applied Informatics, Innsbruck, Austria, February 17–19, 2004, IASTED/ACTA Press, 2004, pp. 378–384.

6.6. Probabilistic systems

Probabilistic rewrite theories (see Section 2.11) and statistical model-checking have been used to model and formally analyze adaptive designs of energy-constrained distributed embedded systems that can provide desired quality-of-service guarantees, such as multimedia applications and wireless sensor networks. The same techniques have also been applied to the specification and verification of distributed object-based stochastic hybrid systems. Another important application of probabilistic systems is the analysis of DoS-resistant network protocols. Finally, PMAude is a rewrite-based specification language for probabilistic object systems, while PVeStA is a parallel statistical model checking and quantitative analysis tool; both of them have been used in applications in this area.

- [979] G.A. Agha, M. Greenwald, C.A. Gunter, S. Khanna, J. Meseguer, K. Sen, P. Thati, **Formal modeling and analysis of DoS using probabilistic rewrite theories**, in: A. Sabelfeld (Ed.), *Proceedings of the Workshop on Foundations of Computer Security, FCS'05*, (Affiliated with LICS'05), Chicago, IL, June 30–July 1, 2005, pp. 91–102.
- [980] G.A. Agha, J. Meseguer, K. Sen, **PMAude: Rewrite-based specification language for probabilistic object systems**, in: A. Cerone, H. Wiklicky (Eds.), *Proceedings of the Third Workshop on Quantitative Aspects of Programming Languages, QAPL 2005*, Edinburgh, UK, April 2–3, 2005, volume 153(2) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2006, pp. 213–239.
- [981] M. Alturki, J. Meseguer, **PVeStA: A parallel statistical model checking and quantitative analysis tool**, in: [1036], pp. 386–392.
- [982] M. Alturki, J. Meseguer, C.A. Gunter, **Probabilistic modeling and analysis of DoS protection for the ASV protocol**, in: [1041], pp. 3–18.
- [983] O. Andrei, H. Kirchner, **Graph rewriting and strategies for modeling biochemical networks**, in: V. Negru, T. Jelebelean, D. Petcu, D. Zaharie (Eds.), *Proceedings of the Ninth International Symposium on Symbolic and Numeric Algorithms for Scientific Computing, SYNASC 2007*, Timisoara, Romania, September 26–29, 2007, IEEE Computer Society, 2007, pp. 407–414.
- [984] O. Andrei, H. Kirchner, **A port graph calculus for autonomic computing and invariant verification**, in: A. Corradini (Ed.), *Proceedings of the Fifth International Workshop on Computing with Terms and Graphs, TERMGRAPH 2009*, York, UK, March 22, 2009, volume 253(4) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2009, pp. 17–38.
- [985] O. Bournez, C. Kirchner, **Probabilistic rewrite strategies. Applications to ELAN**, in: [1106], pp. 252–266.
- [986] M. Casadei, L. Gardelli, M. Viroli, **Simulating emergent properties of coordination in Maude: the collective sort case**, in: C. Canal, M. Viroli (Eds.), *Proceedings of the Fifth International Workshop on the Foundations of Coordination Languages and Software Architectures, FOCLASA 2006*, Bonn, Germany, August 31, 2006, volume 175(2) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2007, pp. 59–80.
- [987] M. Kim, M.O. Stehr, C.L. Talcott, N. Dutt, N. Venkatasubramanian, **Constraint refinement for online verifiable cross-layer system adaptation**, in: *Design, Automation and Test in Europe, DATE 2008*, Munich, Germany, March 10–14, 2008, IEEE, 2008, pp. 646–651.
- [988] J. Meseguer, R. Sharykin, **Specification and analysis of distributed object-based stochastic hybrid systems**, in: J.P. Hespanha, A. Tiwari (Eds.), *Hybrid Systems: Computation and Control, 9th International Workshop, HSCC 2006*, Santa Barbara, CA, USA, March 29–31, 2006, *Proceedings*, volume 3927 of *Lecture Notes in Computer Science*, Springer, 2006, pp. 460–475.

6.7. Bioinformatics and chemical systems

On the bioinformatics side, biological cells can be modeled as concurrent systems whose transitions are precisely their biochemical reactions; this gives rise to rewriting-based symbolic models of biological systems which can be analyzed like any other rewrite theory. Following this basic idea, the Pathway Logic researchers at SRI International have used rewriting logic to develop sophisticated analyses of cell behavior in biological pathways, and have built useful notations and visualization tools, such as the Pathway Logic Assistant to represent the Maude-based analyses in forms more familiar to biologists. A related recent development is the use of rewriting logic in neuroinformatics, describing models of neurons and their interconnections. Concerning chemical systems, ELAN and Tom have been applied to the development of symbolic representations of chemical graphs and to the modeling of chemical reactions as term rewriting modulo associativity and commutativity, guided by appropriate strategies to identify the dynamic constraints satisfied by chemical processes.

- [989] A. Abate, Y. Bai, N. Sznajder, C.L. Talcott, A. Tiwari, **Quantitative and probabilistic modeling in pathway logic**, in: M.M. Zhu, Y. Zhang, H.R. Arabnia, Y. Deng (Eds.), *Proceedings of the 7th IEEE International Conference on Bioinformatics and Bioengineering, BIBE 2007*, Harvard Medical School, Boston, MA, USA, October 14–17, 2007, IEEE, 2007, pp. 922–929.

- [990] T.J. Anastasio, **Data-driven modeling of Alzheimer Disease pathogenesis**, Journal of Theoretical Biology 290 (2011) 60–72.
- [991] O. Andrei, L. Ibănescu, H. Kirchner, **Non-intrusive formal methods and strategic rewriting for a chemical application**, in: [1054], pp. 194–215.
- [992] T.A. Basuki, A. Cerone, P. Milazzo, **Translating stochastic CLS into Maude**, in: G. Giobanu (Ed.), Proceedings of the Second International Meeting on Membrane Computing and Biologically Inspired Process Calculi, MeCBIC 2008, Iași, Romania, September 3–4, 2008, volume 227 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2009, pp. 37–58.
- [993] O. Bournez, G.M. Côme, V. Conraud, H. Kirchner, L. Ibănescu, **Automated generation of kinetic chemical mechanisms using rewriting**, in: P.M.A. Sloot, D. Abramson, A.V. Bogdanov, J. Dongarra, A.Y. Zomaya, Y.E. Gorbachev (Eds.), Computational Science – ICCS 2003, International Conference, Melbourne, Australia and St. Petersburg, Russia, June 2–4, 2003. Proceedings, Part III, volume 2659 of *Lecture Notes in Computer Science*, Springer, 2003, pp. 367–376.
- [994] O. Bournez, G.M. Côme, V. Conraud, H. Kirchner, L. Ibănescu, **A rule-based approach for automated generation of kinetic chemical mechanisms**, in: [1090], pp. 30–45.
- [995] O. Bournez, L. Ibănescu, H. Kirchner, **From chemical rules to term rewriting**, in: [1034], pp. 113–134.
- [996] S. Eker, M. Knapp, K. Laderoute, P. Lincoln, J. Meseguer, M.K. Sönmez, **Pathway logic: Symbolic analysis of biological signaling**, in: R.B. Altman, A.K. Dunker, L. Hunter, T.E. Klein (Eds.), Proceedings of the 7th Pacific Symposium on Biocomputing, PSB 2002, Lihue, Hawaii, USA, January 3–7, 2002, pp. 400–412.
- [997] S. Eker, M. Knapp, K. Laderoute, P. Lincoln, C. Talcott, **Pathway logic: Executable models of biological networks**, in: [1056], pp. 144–161.
- [998] S. Eker, K. Laderoute, P. Lincoln, M.G. Sriam, C.L. Talcott, **Representing and simulating protein functional domains in signal transduction using Maude**, in: C. Priami (Ed.), Computational Methods in Systems Biology, First International Workshop, CMSB 2003, Roverto, Italy, February 24–26, 2003, Proceedings, volume 2602 of *Lecture Notes in Computer Science*, Springer, 2003, pp. 164–165.
- [999] L. Ibănescu, **Programmation par Règles et Stratégies pour la Génération Automatique de Mécanismes de Combustion d’Hydrocarbures Polycycliques**, Ph.D. thesis, Institut National Polytechnique de Lorraine, France, 2004.
- [1000] M. Knapp, L. Briesemeister, S. Eker, P. Lincoln, A. Poggio, C.L. Talcott, K. Laderoute, **Pathway logic helping biologists understand and organize pathway information**, in: P. Markstein, Y. Xu (Eds.), Proceedings of the Fourth International IEEE Computer Society Computational Systems Bioinformatics Conference Workshops & Poster Abstracts (CSB 2005 Workshops), Stanford, CA, USA, August 8–11, 2005, IEEE Computer Society, 2005, pp. 155–156.
- [1001] A. Panikkar, M. Knapp, H. Mi, D. Anderson, K. Kodukula, A.K. Galande, C.L. Talcott, **Applications of pathway logic modeling to target identification**, in: [1015], pp. 434–445.
- [1002] M.G. Sriam, **Modelling protein functional domains in signal transduction using Maude**, Briefings in Bioinformatics 4 (2003) 236–245.
- [1003] C.L. Talcott, **Formal executable models of cell signaling primitives**, in: T. Margaria, B. Steffen (Eds.), Proceedings of the Leveraging Applications of Formal Methods Second International Symposium, ISoLA 2006, Paphos, Cyprus, November 15–19, 2006, IEEE, 2006, pp. 298–302.
- [1004] C.L. Talcott, **Symbolic modeling of signal transduction in Pathway Logic**, in: L.F. Perrone, B. Lawson, J. Liu, F.P. Wieland (Eds.), Proceedings of the Winter Simulation Conference, WSC 2006, Monterey, California, USA, December 3–6, 2006, WSC, 2006, pp. 1656–1665.
- [1005] C.L. Talcott, **Pathway logic**, in: M. Bernardo, P. Degano, G. Zavattaro (Eds.), Formal Methods for Computational Systems Biology, 8th International School on Formal Methods for the Design of Computer, Communication, and Software Systems, SFM 2008, Bertinoro, Italy, June 2–7, 2008, Advanced Lectures, volume 5016 of *Lecture Notes in Computer Science*, Springer, 2008, pp. 21–53.
- [1006] C.L. Talcott, D.L. Dill, **The pathway logic assistant**, in: G. Plotkin (Ed.), Proceedings of the Third International Workshop on Computational Methods in Systems Biology, pp. 228–239.
- [1007] C.L. Talcott, D.L. Dill, **Multiple representations of biological processes**, in: C. Priami, G.D. Plotkin (Eds.), Transactions on Computational Systems Biology VI, volume 4220 of *Lecture Notes in Computer Science*, Springer, 2006, pp. 221–245.
- [1008] C.L. Talcott, S. Eker, M. Knapp, P. Lincoln, K. Laderoute, **Pathway logic modeling of protein functional domains in signal transduction**, in: P. Markstein, Y. Xu (Eds.), Proceedings of the 2nd IEEE Computer Society Bioinformatics Conference, CSB 2003, Stanford, CA, USA, August 11–14, 2003, IEEE Computer Society, 2003, pp. 618–619.
- [1009] C.L. Talcott, S. Eker, M. Knapp, P. Lincoln, K. Laderoute, **Pathway logic modeling of protein functional domains in signal transduction**, in: R.B. Altman, A.K. Dunker, L. Hunter, T.A. Jung, T.E. Klein (Eds.), Proceedings of the 9th Pacific Symposium on Biocomputing, PSB 2004, Fairmont Orchid, Hawaii, USA, January 6–10, 2004, World Scientific, 2004, pp. 568–580.
- [1010] A. Tiwari, C.L. Talcott, **Analyzing a discrete model of aplysia central pattern generator**, in: M. Heiner, A.M. Uhrmacher (Eds.), Computational Methods in Systems Biology, 6th International Conference, CMSB 2008, Rostock, Germany, October 12–15, 2008. Proceedings, volume 5307 of *Lecture Notes in Computer Science*, Springer, 2008, pp. 347–366.
- [1011] A. Tiwari, C.L. Talcott, M. Knapp, P. Lincoln, K. Laderoute, **Analyzing pathways using SAT-based approaches**, in: H. Anai, K. Horimoto, T. Kutsia (Eds.), Algebraic Biology, Second International Conference, AB 2007, Castle of Hagenberg, Austria, July 2–4, 2007, Proceedings, volume 4545 of *Lecture Notes in Computer Science*, Springer, 2007, pp. 155–169.

6.8. Others

Despite the generality of the previous areas, it has been impossible to classify in them the following two papers, which apply rewriting logic in areas outside the more typical system specification, like cohomology or game theory.

- [1012] S. Boissière, M.A. Nieper-Wisskirchen, **Generating series in the cohomology of Hilbert schemes of points on surfaces**, LMS Journal of Computation and Mathematics 10 (2007) 254–270.
- [1013] C. Braga, E.H. Hæusler, **Programming games and their equilibria in Maude**, in: A.M. Moreira, I. Mackie (Eds.), Preliminary Proceedings of the Tenth International Workshop on Rule-Based Programming, RULE 2009, Brasília, Brazil, June 28, 2009, pp. 46–56.

7. Proceedings

This section collects the proceedings in which two or more of the previous papers have been published. These include the proceedings of the biennial Workshop on Rewriting Logic and its Applications, whose first meeting took place in Asilomar, California, in 1996.

- [1014] S. Abdennadher, C. Ringeissen (Eds.), Proceedings of the Fifth International Workshop on Rule-Based Programming, RULE 2004, Aachen, Germany, June 1, 2004, volume 124(1) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2005.
- [1015] G. Agha, O. Danvy, J. Meseguer (Eds.), **Formal Modeling: Actors, Open Systems, Biological Systems – Essays Dedicated to Carolyn Talcott on the Occasion of Her 70th Birthday**, volume 7000 of *Lecture Notes in Computer Science*, Springer, 2011.
- [1016] J.M. Almendros-Jiménez (Ed.), Proceedings of the Eighth Spanish Conference on Programming and Computer Languages, PROLE 2008, Gijón, Spain, October 8–10, 2008, volume 248 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2009.
- [1017] S. Antoy (Ed.), Proceedings of the Sixth International Workshop on Reduction Strategies in Rewriting and Programming, WRS 2006, Seattle, WA, USA, August 11, 2006, volume 174(10) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2007.

- [1018] S. Antoy, Y. Toyama (Eds.), Proceedings of the 4th International Workshop on Reduction Strategies in Rewriting and Programming, WRS 2004, Aachen, Germany, June 2, 2004, volume 124(2) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2005.
- [1019] K. Araki, S. Gnesi, D. Mandrioli (Eds.), FME 2003: Formal Methods, International Symposium of Formal Methods Europe, Pisa, Italy, September 8–14, 2003 Proceedings, volume 2805 of *Lecture Notes in Computer Science*, Springer, 2003.
- [1020] F. Arbab, M. Sirjani (Eds.), Proceedings of the First IPM International Workshop on Foundations of Software Engineering, FSEN 2005, Tehran, Iran, October 1–3, 2005, volume 159 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2006.
- [1021] E. Astesiano, G. Reggio, A. Tarlecki (Eds.), Recent Trends in Data Type Specification, 10th Workshop on Specification of Abstract Data Types Joint with the 5th COMPASS Workshop, S. Margherita, Italy, May 30–June 3, 1994, Selected Papers, volume 906 of *Lecture Notes in Computer Science*, Springer, 1995.
- [1022] F. Baader (Ed.), Automated Deduction – CADE-19, 19th International Conference on Automated Deduction Miami Beach, FL, USA, July 28–August 2, 2003, Proceedings, volume 2741 of *Lecture Notes in Computer Science*, Springer, 2003.
- [1023] F. Baader (Ed.), Term Rewriting and Applications, 18th International Conference, RTA 2007, Paris, France, June 26–28, 2007, Proceedings, volume 4533 of *Lecture Notes in Computer Science*, Springer, 2007.
- [1024] M. Backes, P. Ning (Eds.), Computer Security – ESORICS 2009, 14th European Symposium on Research in Computer Security, Saint-Malo, France, September 21–23, 2009, Proceedings, volume 5789 of *Lecture Notes in Computer Science*, Springer, 2009.
- [1025] J.C.M. Baeten, S. Mauw (Eds.), CONCUR'99: Concurrency Theory, 10th International Conference, Eindhoven, The Netherlands, August 24–27, 1999, Proceedings, volume 1664 of *Lecture Notes in Computer Science*, Springer, 1999.
- [1026] D. Ballis, S. Escobar, M. Marchiori (Eds.), Proceedings of the 3rd International Workshop on Automated Specification and Verification of Web Systems, WWW 2007, Venice, Italy, December 14, 2007, volume 200(3) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2008.
- [1027] L. Baresi, R. Heckel (Eds.), Fundamental Approaches to Software Engineering, 9th International Conference, FASE 2006, Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2006, Vienna, Austria, March 27–28, 2006, Proceedings, volume 3922 of *Lecture Notes in Computer Science*, Springer, 2006.
- [1028] G. Barthe, F.S. de Boer (Eds.), Formal Methods for Open Object-Based Distributed Systems, 10th IFIP WG 6.1 International Conference, FMOODS 2008, Oslo, Norway, June 4–6, 2008, Proceedings, volume 5051 of *Lecture Notes in Computer Science*, Springer, 2008.
- [1029] M.M. Bonsangue, E.B. Johnsen (Eds.), Formal Methods for Open Object-Based Distributed Systems, 9th IFIP WG 6.1 International Conference, FMOODS 2007, Paphos, Cyprus, June 6–8, 2007, Proceedings, volume 4468 of *Lecture Notes in Computer Science*, Springer, 2007.
- [1030] M. van den Brand, R. Verma (Eds.), Proceedings of the Second International Workshop on Rule-Based Programming, RULE 2001, Firenze, Italy, September 4, 2001, volume 59(4) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2001.
- [1031] K. Breitman, A. Cavalcanti (Eds.), Formal Methods and Software Engineering, 11th International Conference on Formal Engineering Methods, ICFEM 2009, Rio de Janeiro, Brazil, December 9–12, 2009, Proceedings, volume 5885 of *Lecture Notes in Computer Science*, Springer, 2009.
- [1032] M. Chechik, M. Wirsing (Eds.), Fundamental Approaches to Software Engineering, 12th International Conference, FASE 2009, Held as Part of the Joint, European Conferences on Theory and Practice of Software, ETAPS 2009, York, UK, March 22–29, 2009, Proceedings volume 5503 of *Lecture Notes in Computer Science*, Springer, 2009.
- [1033] W.C. Chu, N.J. Juzgado, W.E. Wong (Eds.), Proceedings of the 17th International Conference on Software Engineering and Knowledge Engineering, SEKE 2005, Taipei, Taiwan, Republic of China, July 14–16, 2005, 2005.
- [1034] H. Cirstea, N. Martí-Oliet (Eds.), Proceedings of the 6th International Workshop on Rule-Based Programming, RULE 2005, Nara, Japan, April 23, 2005, volume 147(1) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2006.
- [1035] H. Comon (Ed.), Rewriting Techniques and Applications, 8th International Conference, RTA-97, Sitges, Spain, June 2–5, 1997, Proceedings, volume 1232 of *Lecture Notes in Computer Science*, Springer, 1997.
- [1036] A. Corradini, B. Klin, C. Cirstea (Eds.), Algebra and Coalgebra in Computer Science – 4th International Conference, CALCO 2011, Winchester, UK, August 30–September 2, 2011, Proceedings, volume 6859 of *Lecture Notes in Computer Science*, Springer, 2011.
- [1037] A. Corradini, U. Montanari (Eds.), Recent Trends in Algebraic Development Techniques, 19th International Workshop, WADT 2008, Pisa, Italy, June 13–16, 2008, Revised Selected Papers, volume 5486 of *Lecture Notes in Computer Science*, Springer, 2009.
- [1038] P. Degano, R.D. Nicola, J. Meseguer (Eds.), Concurrency, Graphs and Models, Essays Dedicated to Ugo Montanari on the Occasion of His 65th Birthday, volume 5065 of *Lecture Notes in Computer Science*, Springer, 2008.
- [1039] G. Denker, C. Talcott (Eds.), Proceedings of the Sixth International Workshop on Rewriting Logic and its Applications, WRLA 2006, Vienna, Austria, April 1–2, 2006, volume 176(4) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2007.
- [1040] J.S. Dong, H. Zhu (Eds.), Formal Methods and Software Engineering – 12th International Conference on Formal Engineering Methods, ICFEM 2010, Shanghai, China, November 17–19, 2010, Proceedings, volume 6447 of *Lecture Notes in Computer Science*, Springer, 2010.
- [1041] D.J. Dougherty, S. Escobar (Eds.), Proceedings of the Third International Workshop on Security and Rewriting Techniques, SecReT 2008, Pittsburgh, PA, USA, June 22, 2008, volume 234 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2009.
- [1042] F. Durán (Ed.), Rewriting Logic and its Applications, 9th International Workshop, WRLA 2012, Held as a Satellite Event of ETAPS 2012, Tallinn, Estonia, March 24–25, 2012, Revised Selected Papers, Lecture Notes in Computer Science, Springer, 2012. To appear.
- [1043] F. Durán, V. Rusu (Eds.), Proceedings of the Second International Workshop on Algebraic Methods in Model-based Software Engineering, AMMS 2011, Zurich, Switzerland, June 30, 2011, volume 56 of *Electronic Proceedings in Theoretical Computer Science*, 2011.
- [1044] M.S. Feather, M. Goedicke (Eds.), Proceedings of the 16th IEEE International Conference on Automated Software Engineering, ASE 2001, Coronado Island, San Diego, CA, USA, November 26–29, 2001, IEEE Computer Society, 2001.
- [1045] M. Fernández, C. Kirchner (Eds.), Proceedings of the First International Workshop on Security and Rewriting Techniques, SecReT 2006, Venice, Italy, July 15, 2006, volume 171(4) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2007.
- [1046] M. Fernández, R. Lämmel (Eds.), Proceedings of the 7th International Workshop on Rule Based Programming, RULE 2006, Seattle, WA, USA, August 11, 2006, volume 174(1) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2007.
- [1047] J.L. Fiadeiro (Ed.), Recent Trends in Algebraic Development Techniques, 13th International Workshop, WADT '98, Lisbon, Portugal, April 2–4, 1998, Selected Papers, volume 1589 of *Lecture Notes in Computer Science*, Springer, 1999.
- [1048] J.L. Fiadeiro, N. Harman, M. Roggenbach, J.J.M.M. Rutten (Eds.), Algebra and Coalgebra in Computer Science: First International Conference, CALCO 2005, Swansea, UK, September 3–6, 2005, Proceedings, volume 3629 of *Lecture Notes in Computer Science*, Springer, 2005.
- [1049] J.L. Fiadeiro, P.D. Mosses, F. Orejas (Eds.), Recent Trends in Algebraic Development Techniques, 17th International Workshop, WADT 2004, Barcelona, Spain, March 27–29, 2004, Revised Selected Papers, volume 3423 of *Lecture Notes in Computer Science*, Springer, 2004.
- [1050] B. Fischer, E. Visser (Eds.), Proceedings of the 2002 ACM SIGPLAN Workshop on Rule-Based Programming, Pittsburgh, Pennsylvania, USA, 2002, ACM, 2002.
- [1051] K. Futatsugi (Ed.), Proceedings of the CafeOBJ Symposium '98, Numazu, Japan, CafeOBJ Project, 1998.
- [1052] K. Futatsugi (Ed.), Proceedings of the Third International Workshop on Rewriting Logic and its Applications, WRLA 2000, Kanazawa, Japan, September 18–20, 2000, volume 36 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2000.
- [1053] K. Futatsugi, J.A. Goguen, J. Meseguer (Eds.), OBJ/CafeOBJ/Maude Workshop at Formal Methods '99: Formal Specification, Proof, and Applications, Theta, 1999.
- [1054] K. Futatsugi, J.P. Jouannaud, J. Meseguer (Eds.), Algebra, Meaning, and Computation, Essays Dedicated to Joseph A. Goguen on the Occasion of His 65th Birthday, volume 4060 of *Lecture Notes in Computer Science*, Springer, 2006.
- [1055] K. Futatsugi, A.T. Nakagawa, T. Tamai (Eds.), Cafe: An Industrial-Strength Algebraic Formal Method, Elsevier, 2000.
- [1056] F. Gadducci, U. Montanari (Eds.), Proceedings of the Fourth International Workshop on Rewriting Logic and its Applications, WRLA 2002, Pisa, Italy, September 19–21, 2002, volume 71 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2004.

- [1057] J.P. Gallagher, J. Voigtländer (Eds.), Proceedings of the 2010 ACM SIGPLAN Workshop on Partial Evaluation and Program Manipulation, PEPM 2010, Madrid, Spain, January 18–19, 2010, ACM, 2010.
- [1058] J.L. Giavitto, P.E. Moreau (Eds.), Proceedings of the 4th International Workshop on Rule-Based Programming, RULE 2003, Valencia, Spain, June 9, 2003, volume 86(2) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2003.
- [1059] J. Giesl (Ed.), Term Rewriting and Applications, 16th International Conference, RTA 2005, Nara, Japan, April 19–21, 2005, Proceedings, volume 3467 of *Lecture Notes in Computer Science*, Springer, 2005.
- [1060] J. Giesl (Ed.), Proceedings of the 7th International Workshop on Reduction Strategies in Rewriting and Programming, WRS 2007, Paris, France, June 25, 2007, volume 204 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2008.
- [1061] B. Gramlich, H. Kirchner (Eds.), Proceedings of the CADE-14 Workshop on Strategies in Automated Deduction, Townsville, Australia, 1997.
- [1062] B. Gramlich, S. Lucas (Eds.), Proceedings of the 3rd International Workshop on Reduction Strategies in Rewriting and Programming, WRS 2003, Valencia, Spain, June 8, 2003, volume 86(4) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2003.
- [1063] B. Gramlich, F. Pfenning (Eds.), Proceedings of the CADE-15 Workshop on Strategies in Automated Deduction, Lindau, Germany, 1998.
- [1064] C. Halatsis, D.G. Maritsas, G. Philokyprou, S. Theodoridis (Eds.), PARLE'94: Parallel Architectures and Languages Europe, 6th International PARLE Conference, Athens, Greece, July 4–8, 1994, Proceedings, volume 817 of *Lecture Notes in Computer Science*, Springer, 1994.
- [1065] J. Hatcliff, E. Zucca (Eds.), Formal Techniques for Distributed Systems, Joint 12th IFIP WG 6.1 International Conference, FMOODS 2010 and 30th IFIP WG 6.1 International Conference, FORTE 2010, Amsterdam, The Netherlands, June 7–9, 2010, Proceedings, volume 6117 of *Lecture Notes in Computer Science*, Springer, 2010.
- [1066] J. Hsiang (Ed.), Rewriting Techniques and Applications, 6th International Conference, RTA-95, Kaiserslautern, Germany, April 5–7, 1995, Proceedings, volume 914 of *Lecture Notes in Computer Science*, Springer, 1995.
- [1067] T. Ida (Ed.), Proceedings of the 12th International Symposium on Symbolic and Numeric Algorithms for Scientific Computing, SYNASC 2010, Timisoara, Romania, September 23–26, 2010, IEEE Computer Society, 2010.
- [1068] M. Johnson, D. Pavlovic (Eds.), Algebraic Methodology and Software Technology, 13th International Conference, AMAST 2010, Lac-Beauport, QC, Canada, June 23–25, 2010, Revised Selected Papers volume 6486 of *Lecture Notes in Computer Science*, Springer, 2011.
- [1069] M. Johnson, V. Vene (Eds.), Algebraic Methodology and Software Technology, 11th International Conference, AMAST 2006, Kuressaare, Estonia, July 5–8, 2006, Proceedings, volume 4019 of *Lecture Notes in Computer Science*, Springer, 2006.
- [1070] C. Kirchner (Ed.), Proceedings of the 4th International ACM SIGPLAN Conference on Principles and Practice of Declarative Programming, PPDP 2002, Pittsburgh, PA, USA, October 6–8, 2002 (Affiliated with PLI 2002), ACM, 2002.
- [1071] C. Kirchner, H. Kirchner (Eds.), Proceedings of the Second International Workshop on Rewriting Logic and its Applications, WRLA'98, Pont-à-Mousson, France, September 1–4, 1998, volume 15 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 1998.
- [1072] H. Kirchner, G. Levi (Eds.), Algebraic and Logic Programming, Third International Conference, Volterra, Italy, September 2–4, 1992, Proceedings, volume 632 of *Lecture Notes in Computer Science*, Springer, 1992.
- [1073] G. Kniessel, J.S. Pinto (Eds.), Preliminary Proceedings of the Ninth International Workshop on Rule-Based Programming, RULE 2008, Hagenberg Castle, Austria, June 18, 2008, 2008. Technical Report IAI-TR-08-02, Institut für Informatik III, Rheinische Friedrich-Wilhelm-Universität Bonn.
- [1074] G. Koob, D. Maughan, S. Saydjari (Eds.), Proceedings of the DARPA Information Survivability Conference and Exposition, DISCEX 2000, Hilton Head Island, South Carolina, January 25–27, 2000, IEEE Computer Society Press, 2000.
- [1075] A. Kurz, M. Lenisa, A. Tarlecki (Eds.), Algebra and Coalgebra in Computer Science, Third International Conference, CALCO 2009, Udine, Italy, September 7–10, 2009, Proceedings, volume 5728 of *Lecture Notes in Computer Science*, Springer, 2009.
- [1076] J. de Lara, A. Zisman (Eds.), Fundamental Approaches to Software Engineering – 15th International Conference, FASE 2012, Held as Part of the European Joint Conferences on Theory and Practice of Software, ETAPS 2012, Tallinn, Estonia, March 24–April 1, 2012, Proceedings, volume 7212 of *Lecture Notes in Computer Science*, Springer, 2012.
- [1077] M. Lenisa, M. Miculan (Eds.), Proceedings of the Workshop on Theory of Concurrency, Higher Order Languages and Types, TOSCA 2001, Udine, Italy, November 19–21, 2001, volume 62 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2001.
- [1078] P. Leong, A. Koch (Eds.), Proceedings of the 2006 International Conference on Field Programmable Logic and Applications, FPL 2006, Madrid, Spain, August 28–30, 2006, IEEE, 2006.
- [1079] M. Leuschel, A. Podelski (Eds.), Proceedings of the 9th International ACM SIGPLAN Conference on Principles and Practice of Declarative Programming, PPDP 2007, Wrocław, Poland, July 14–16, 2007, ACM, 2007.
- [1080] F.J. López-Fraguas (Ed.), Actas de las V Jornadas sobre Programación y Lenguajes, PROLE 2005, Granada, España, Septiembre 14–16, 2005, Thomson, 2005.
- [1081] S. Lucas (Ed.), Proceedings of the Fourth Spanish Conference on Programming and Computer Languages, PROLE 2004, Málaga, Spain, November 10–12, 2004, volume 137(1) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2005.
- [1082] S. Lucas, M. del Mar Gallardo, E. Pimentel (Eds.), Actas de las IV Jornadas sobre Programación y Lenguajes, PROLE 2004, Málaga, España, Noviembre 11–12, 2004, 2004.
- [1083] P. Lucio, G. Moreno, R. Peña (Eds.), Proceedings of the Ninth Spanish Conference on Programming and Languages, PROLE 2009, San Sebastián, Spain, September 9–11, 2009, volume 258(1) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2009.
- [1084] C. Lynch (Ed.), Proceedings of the 21st International Conference on Rewriting Techniques and Applications, RTA 2010, Edinburgh, UK, July 11–13, 2010, volume 6 of *Leibniz International Proceedings in Informatics (LIPIcs)*, Schloss Dagstuhl–Leibniz-Zentrum fuer Informatik, 2010.
- [1085] N. Martí-Oliet (Ed.), Proceedings of the Fifth International Workshop on Rewriting Logic and its Applications, WRLA 2004, Barcelona, Spain, March 27–April 4, 2004, volume 117 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2004.
- [1086] J. Meseguer (Ed.), Proceedings of the First International Workshop on Rewriting Logic and its Applications, WRLA'96, Asilomar, California, September 3–6, 1996, volume 4 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 1996.
- [1087] T. Mossakowski, U. Montanari, M. Haveraen (Eds.), Algebra and Coalgebra in Computer Science, Second International Conference, CALCO 2007, Bergen, Norway, August 20–24, 2007, Proceedings, volume 4624 of *Lecture Notes in Computer Science*, Springer, 2007.
- [1088] P. Mosses, I. Ulidowski (Eds.), Proceedings of the Second Workshop on Structural Operational Semantics, SOS 2005, Lisbon, Portugal, July 10, 2005, volume 156(1) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2006.
- [1089] E. Najm, U. Nestmann, P. Stevens (Eds.), Formal Methods for Open Object-Based Distributed Systems, 6th IFIP WG 6.1 International Conference, FMOODS 2003, Paris, France, November 19–21, 2003, Proceedings, volume 2884 of *Lecture Notes in Computer Science*, Springer, 2003.
- [1090] R. Nieuwenhuis (Ed.), Rewriting Techniques and Applications, 14th International Conference, RTA 2003, Valencia, Spain, June 9–11, 2003, Proceedings, volume 2706 of *Lecture Notes in Computer Science*, Springer, 2003.
- [1091] M. Okada, B.C. Pierce, A. Scedrov, H. Tokuda, A. Yonezawa (Eds.), Software Security – Theories and Systems, Mext-NSF-JSPS International Symposium, ISSS 2002, Tokyo, Japan, November 8–10, 2002, Revised Papers, volume 2609 of *Lecture Notes in Computer Science*, Springer, 2003.
- [1092] P.C. Ölveczky (Ed.), Proceedings of the First International Workshop on Rewriting Techniques for Real-Time Systems, RTRTS 2010, Longyearbyen, Spitsbergen, Norway, April 6–9, 2010, volume 36 of *Electronic Proceedings in Theoretical Computer Science*, Computing Research Repository (CoRR), 2010.
- [1093] P.C. Ölveczky (Ed.), Rewriting Logic and its Applications. 8th International Workshop, WRLA 2010, Held as a Satellite Event of ETAPS 2010, Paphos, Cyprus, March 20–21, 2010, Revised Selected Papers, volume 6381 of *Lecture Notes in Computer Science*, Springer, 2010.
- [1094] J. Palsberg (Ed.), Semantics and Algebraic Specification, Essays Dedicated to Peter D. Mosses on the Occasion of His 60th Birthday, volume 5700 of *Lecture Notes in Computer Science*, Springer, 2009.
- [1095] D.K. Panda (Ed.), Proceedings of the 21th International Parallel and Distributed Processing Symposium, IPDPS 2007, Long Beach, California, USA, March 26–30, 2007, IEEE, 2007.

- [1096] F. Parisi-Presicce (Ed.), *Recent Trends in Algebraic Development Techniques*, 12th International Workshop, WADT'97, Tarquinia, Italy, June 3–7, 1997, Selected Papers, volume 1376 of *Lecture Notes in Computer Science*, Springer, 1997.
- [1097] F. Pfenning (Ed.), *Term Rewriting and Applications*, 17th International Conference, RTA 2006, Seattle, WA, USA, August 12–14, 2006, Proceedings, volume 4098 of *Lecture Notes in Computer Science*, Springer, 2006.
- [1098] C. Rattray, S. Maharaj, C. Shankland (Eds.), *Algebraic Methodology and Software Technology*, 10th International Conference, AMAST 2004, Stirling, Scotland, UK, July 12–16, 2004, Proceedings, volume 3116 of *Lecture Notes in Computer Science*, Springer, 2004.
- [1099] G. Roşu (Ed.), Proceedings of the Seventh International Workshop on Rewriting Logic and its Applications, WRLA 2008, Budapest, Hungary, March 29–30, 2008, volume 238(3) of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2009.
- [1100] T. Rus (Ed.), *Algebraic Methodology and Software Technology*, 8th International Conference, AMAST 2000, Iowa City, Iowa, USA, May 20–27, 2000, Proceedings, volume 1816 of *Lecture Notes in Computer Science*, Springer, 2000.
- [1101] P. Schneider-Kamp, M. Hanus (Eds.), Proceedings of the 13th International ACM SIGPLAN Symposium on Principles and Practices of Declarative Programming, PPDP 2011, Odense, Denmark, July 20–22, 2011, ACM, 2011.
- [1102] D.D. Schreye (Ed.), *Logic-Based Program Synthesis and Transformation*, 19th International Symposium, LOPSTR 2009, Coimbra, Portugal, September 9–11, 2009, Revised Selected Papers, volume 6037 of *Lecture Notes in Computer Science*, Springer, 2010.
- [1103] A. Sellink (Ed.), Proceedings of the Second International Workshop on the Theory and Practice of Algebraic Specifications, Amsterdam, The Netherlands, September 25–26, 1997, Electronic Workshops in Computing, British Computer Society, 1998.
- [1104] S.F. Smith, C.L. Talcott (Eds.), Formal Methods for Open Object-Based Distributed Systems IV, IFIP TC6/WG6.1 Fourth International Conference on Formal Methods for Open Object-Based Distributed Systems, FMOODS 2000, Stanford, California, USA, September 6–8, 2000, Proceedings, volume 177 of *IFIP Conference Proceedings*, Kluwer, 2000.
- [1105] G. Sutcliffe, A. Voronkov (Eds.), *Logic for Programming, Artificial Intelligence, and Reasoning*, 12th International Conference, LPAR 2005, Montego Bay, Jamaica, December 2–6, 2005, Proceedings, volume 3835 of *Lecture Notes in Computer Science*, Springer, 2005.
- [1106] S. Tison (Ed.), *Rewriting Techniques and Applications*, 13th International Conference, RTA 2002, Copenhagen, Denmark, July 22–24, 2002, Proceedings, volume 2378 of *Lecture Notes in Computer Science*, Springer, 2002.
- [1107] J. Visser, V. Winter (Eds.), Proceedings of the Eighth International Workshop on Rule Based Programming, RULE 2007, Paris, France, June 29, 2007, volume 219 of *Electronic Notes in Theoretical Computer Science*, Elsevier, 2008.
- [1108] A. Voronkov (Ed.), *Rewriting Techniques and Applications*, 19th International Conference, RTA 2008, Hagenberg, Austria, July 15–17, 2008, Proceedings, volume 5117 of *Lecture Notes in Computer Science*, Springer, 2008.
- [1109] J.M. Wing, J. Woodcock, J. Davies (Eds.), FM'99 – Formal Methods, World Congress on Formal Methods in the Development of Computing Systems, Toulouse, France, September 20–24, 1999, Proceedings, Volume II, volume 1709 of *Lecture Notes in Computer Science*, Springer, 1999.
- [1110] M. Wirsing, J.P. Banâtre, M.M. Hölzl, A. Rauschmayer (Eds.), *Software-Intensive Systems and New Computing Paradigms – Challenges and Visions*, volume 5380 of *Lecture Notes in Computer Science*, Springer, 2008.